

# A2SAN-H/-E/-L A2SAN-H/-E/-L-WOHS X11SAN X11SAN-WOHS

**USER'S MANUAL** 

Revision 1.0c

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Manual Revision 1.0c

Release Date: August 15, 2019

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# Preface

## About This Manual

This manual is written for system integrators, IT technicians and knowledgeable end users. It provides information for the installation and use of the A2SAN-H/-E/-L and X11SAN motherboard.

# About This Motherboard

The A2SAN-H/-E/-L and X11SAN motherboard provides powerful graphics and increased media processing performance with multi-frame technology. Paired with the Intel® Atom SoC (System-on-a-Chip) processor, the A2SAN-H/-E/-L and X11SAN delivers more computing power for faster memory speeds and bandwidth while maintaining energy efficiency. Utilizing Intel® TCC (Time Coordinated Computing) Technology, the A2SAN-H/-E/-L and X11SAN resolves latency issues in applications and improves determinism across connected devices. The motherboard features advanced technologies such as Intel® Virtualization to improve security and reliabity of systems, and Thermal Monitoring to reduce power consumption. It also comes with more I/O ports and high-speed connectivity.

Please note that this motherboard is intended to be installed and serviced by professional technicians only. For processor/memory updates, please refer to our website at http://www.supermicro.com/products/.

# **Conventions Used in the Manual**

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:



**Warning!** Indicates important information given to prevent equipment/property damage or personal injury.



Warning! Indicates high voltage may be encountered when performing a procedure.



**Important:** Important information given to ensure proper system installation or to relay safety precautions.



**Note:** Additional Information given to differentiate various models or provides information for correct system setup.

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# Chapter 1

# Introduction

Congratulations on purchasing your computer motherboard from an industry leader. Supermicro boards are designed to provide you with the highest standards in quality and performance. In additon to the motherboard, several important parts that are included with the system are listed below. If anything listed is damaged or missing, please contact your retailer.

# 1.1 Checklist

| Main Parts List (Retail Single Package)      |   |          |  |  |  |  |  |
|--|---|----------|--|--|--|--|--|
| Description                                  | Part Number   | Quantity |  |  |  |  |  |
| Supermicro Motherboard with passive heatsink | A2SAN-H/-E/-L<br>A2SAN-H/-E/-L-WOHS (without heatsink)<br>X11SAN<br>X11SAN-WOHS | 1        |  |  |  |  |  |
| Audio cable (20cm)                           | CBL-OTHR-0986   | 1        |  |  |  |  |  |
| SATA power cable (25cm)                      | CBL-PWEX-0982   | 1        |  |  |  |  |  |
| USB 2.0 cable (20cm)                         | CBL-CUSB-0983   | 1        |  |  |  |  |  |
| COM cable (20cm)                             | CBL-CUSB-0984   | 1        |  |  |  |  |  |
| SATA data cable (25cm)                       | CBL-SAST-0881   | 1        |  |  |  |  |  |
| Quick Reference Guide                        | MNL-1935-QRG  | 1        |  |  |  |  |  |

| Main Parts List (Bulk Package)               |   |          |  |  |  |  |  |
|--|---|----------|--|--|--|--|--|
| Description                                  | Part Number   | Quantity |  |  |  |  |  |
| Supermicro Motherboard with passive heatsink | A2SAN-H/-E/-L<br>A2SAN-H/-E/-L-WOHS (without heatsink)<br>X11SAN<br>X11SAN-WOHS | 1        |  |  |  |  |  |
| SATA power cable (25cm)                      | CBL-PWEX-0982   | 1        |  |  |  |  |  |
| SATA data cable (25cm)                       | CBL-SAST-0881   | 1        |  |  |  |  |  |

| Optional Parts List                               |                  |   |  |  |  |  |  |
|---|------------------|---|--|--|--|--|--|
| Description Part Number Quantity                  |                  |   |  |  |  |  |  |
| Mini PCI-E extended bracket                       | MCP-110-00097-0N | 1 |  |  |  |  |  |
| M.2 Module Bracket (for 2242/3042 module support) | MCP-290-00161-0N | 1 |  |  |  |  |  |

### **Important Links**

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your server.

- Supermicro product manuals: http://www.supermicro.com/support/manuals/
- Product drivers and utilities: https://www.supermicro.com/wftp/driver/
- Product safety info: http://www.supermicro.com/about/policies/safety\_information.cfm
- If you have any questions, please contact our support team at: support@supermicro.com

This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.

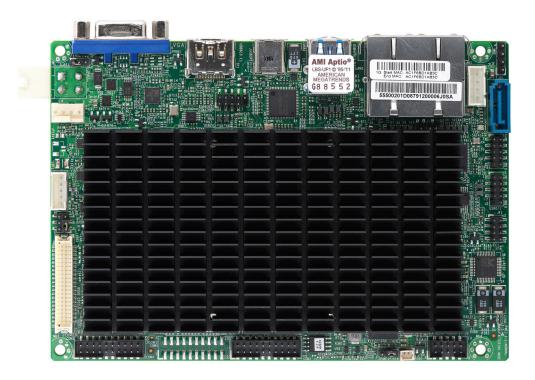
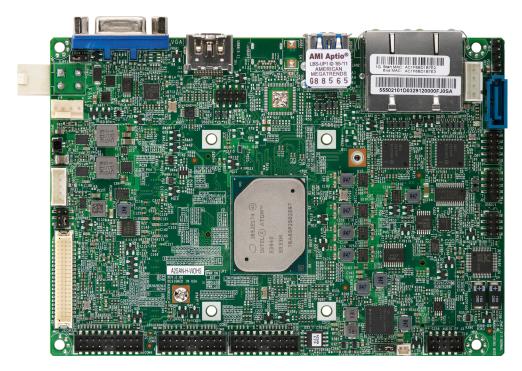


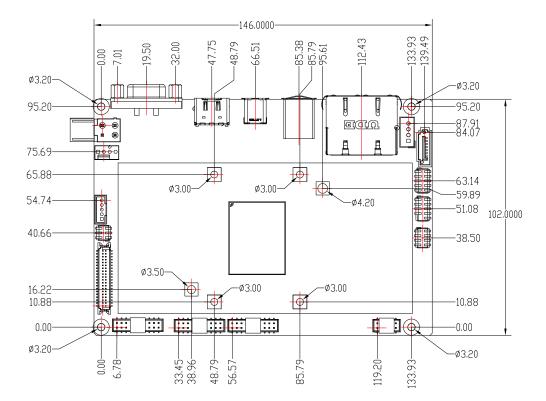
Figure 1-1. A2SAN-H Motherboard Image

Figure 1-2. A2SAN-H-WOHS Motherboard Image



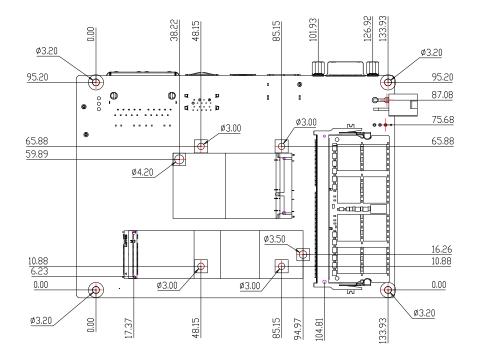
**Note:** All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.

#### Figure 1-3. A2SAN-H/-E/-L and X11SAN Motherboard Mechanical Drawings



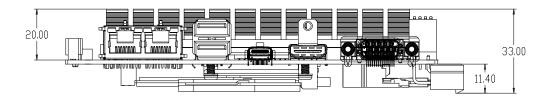
#### Motherboard Top Side

#### **Motherboard Bottom Side**

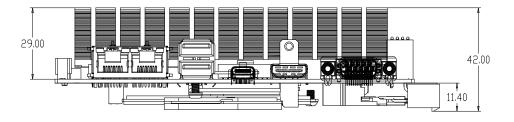


#### Figure 1-4. A2SAN-H/-E/-L and X11SAN Back Panel I/O Mechanical Drawings

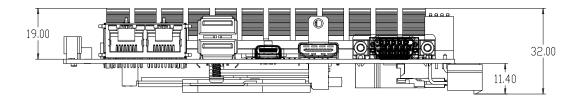




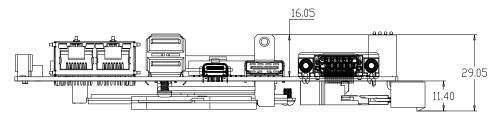
Back Panel I/O With Heatsink (A2SAN-E)



Back Panel I/O With Heatsink (X11SAN)



Back Panel I/O Without Heatsink (X11SAN-WOHS, A2SAN-H/E/L-WOHS)



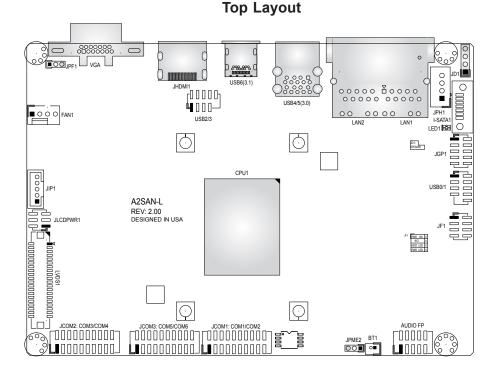
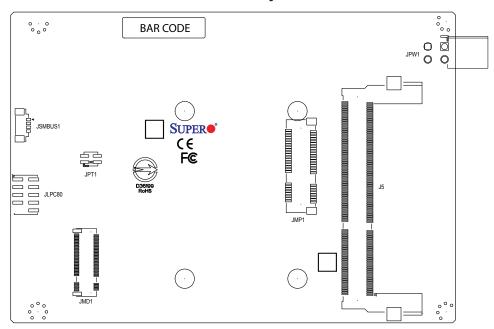
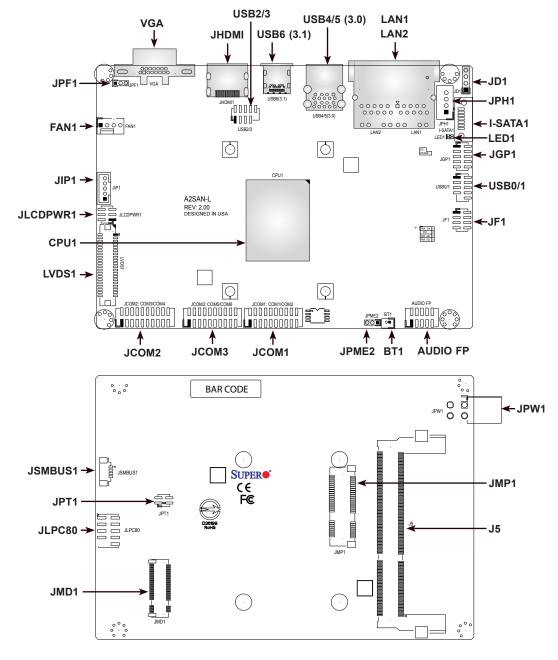


Figure 1-5. Motherboard Layout (not drawn to scale)

**Bottom Layout** 



**Note:** Components not documented are for internal testing only.



### **Quick Reference**

### Notes:

- See Chapter 2 for detailed information on jumpers, I/O ports, and JF1 front panel connections.
- "I indicates the location of Pin 1.
- Jumpers/LED indicators not indicated are used for testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer.

| Jumper   | Description     |  | Default Setting (*)   |
|--|-----------------|--|---|
| JLCDPWR1   | LVDS Panel VC   | C Power 3.3V / 5V / 12V  | Pins 1-3* (3.3V)<br>Pins 3-5 (5V)<br>Pins 3-4 (12V)   |
| JPF1   | Power Force Or  | 1  | Pins 1-2* (Power Force On)<br>Pins 2-3 (PWR BTN Power On)                                       |
| JPME2  | Manufacturing N | lode   | Pins 1-2* (Normal)<br>Pins 2-3 (Manufacturing Mode)   |
| JPT1   | TPM Enable/Dis  | able Header  | JPT1_1N2* (Enable)<br>JPT1_3N4 (Disable)  |
| LED  | Descriptio      | on   | Status  |
| LED1   | Power LED       | (for debugging only)   | Solid Green: S0 mode<br>Solid Red: S3/S4/S5 modes   |
| Connector  |                 | Description  |   |
| AUDIO FP   |                 | Front Panel Audio Header (N  | /lic-in/Headphone-out)  |
| BT1  |                 | Battery Connector<br>(To Clear CMOS, remove the<br>install the battery.) | e battery, short pins 1-2 for more than 10 seconds and  |
| FAN1   |                 | System Fan Header  |   |
| I-SATA1  |                 | Intel® PCH SATA 3.0 Port   |   |
| JCOM1: COM1/COM2<br>JCOM2: COM3/COM4<br>JCOM3: COM5/COM6 |                 | Serial COM Ports (JCOM2 s  | upports RS232/RS422/RS485)<br>upports RS232)<br>upports RS232) (Only supported on A2SAN-H-WOHS) |
| JD1  |                 | Speaker Header   |   |
| JF1  |                 | Front Control Panel Header   |   |
| JGP1   |                 | General Purpose I/O Header   | r   |
| JHDMI  |                 | Back Panel HDMI Port   |   |
| JIP1   |                 | LVDS Inverter Power Heade  | r   |
| JLPC80   |                 | Port 80 Connector (for debug   | gging only)   |
| JMD1   |                 | M.2 Slot (B-KEY) (supports I   | PCle Gen2 x 1 / SATA / USB 2.0)   |
| JMP1   |                 | Mini PCI-E Slot (supports PC   | Cle Gen2 x 1 / USB 2.0)   |
| JPH1   |                 | SATA Power Connector (for  | one HDD system)   |
| JPW1   |                 | 4-pin 12V-Standby R/A Type   | Power Connector   |
| JPW2   |                 | 12V DC Power   |   |
| JSMBUS1  |                 | System Management Bus H  | eader   |
| JLAN1: LAN1/LAN2   |                 | LAN (RJ45) Ports   |   |
| LVDS1  |                 | Dual Channel 48-bit LVDS C   | Connector   |
| USB4/5   |                 | Back Panel Universal Serial  | Bus (USB) 3.0 Ports   |
| USB0/1, USB2/3   |                 | USB 2.0 Headers  |   |
| USB6   |                 | USB 3.1 Type-C Header  |   |
| VGA  |                 | Back Panel VGA Port  |   |
|  |                 |  |   |

### **Quick Reference Table**

### **Motherboard Features**

#### Motherboard Features

#### CPU

- A2SAN-H/-E, A2SAN-H/-E-WOHS: Intel® Atom™ x5-E3940 Processor, Quad Core, 2M Cache, 1.6GHz-1.8GHz, 9.5W
- A2SAN-L, A2SAN-L-WOHS: Intel® Atom™ x5-E3930 Processor, Dual Core, 2M Cache, 1.3GHz-1.8GHz, 6.5W
- X11SAN, X11SAN-WOHS: Intel® Pentium® Processor N4200, Quad Core, 2M Cache, 1.1GHz-2.5GHz, 6W

#### Memory

Integrated memory controller supports DDR3L 1333/1600/1866MHz Non-ECC 204-pin SO-DIMM up to 8GB

#### **DIMM Size**

Single channel DDR3L 1333/1600/1866MHz Non-ECC 204-pin SO-DIMM up to 8GB

#### **Expansion Slots**

- One (1) Full Mini-PCI Express slot (USB2.0 x 1, PCIe Gen2 x 1)
- One (1) M.2 2280 B-Key for SATA or PCIe SSD (2242/3042 B-key M.2 module is supported by an extender bracket P/N: MCP-290-00161-0N)

#### Network

• Dual GbE LAN with Intel® Ethernet Controller I210

| Graphics   |   |  |  |  |  |  |
|--|---|--|--|--|--|--|
| Intel® HD Graphics GT Series                             | <ul> <li>Features: OpenGL 5.0, DirectX 12, OpenCL 2.1</li> <li>Hardware Decode: AVC/H.264, MPEG2, VC1/WMV9, JPEG/MJPEG, HEVC/H.265, VP8, VP9, MVC</li> <li>Hardware Encode: AVC/H.264, JPEG/MJPEG, HEVC/H.265, VP8, VP9, MVC</li> <li>Display: VGA (resolution up to 2560x1600 at 60Hz), HDMI 1.4 (resolution up to 3840x2160 at 30Hz), LVDS (dual channel 48-bit, resolution up to 1920x1080 at 60Hz)</li> </ul> |  |  |  |  |  |
| I/O Devices  |   |  |  |  |  |  |
| COM Ports  | <ul> <li>Four (4) front accessible ports (JCOM1 supports two RS232/RS422/RS485,<br/>JCOM2/3 supports two RS232)</li> </ul>  |  |  |  |  |  |
| SATA Ports   | One (1) SATA 3.0 port (I-SATA1)   |  |  |  |  |  |
| Audio Header   | One (1) HD Audio header with Mic-in/Headphone-out (Realtek ALC888S)   |  |  |  |  |  |
| SMBus Header   | One (1) SMBus box header  |  |  |  |  |  |
| Speaker  | One (1) Speaker header  |  |  |  |  |  |
| Peripheral Devices                                       |   |  |  |  |  |  |
| • Two (2) USB 3.0 ports on the re                        | ar I/O panel (USB4/5, Type A)   |  |  |  |  |  |
| • Four (4) USB 2.0 headers (USB 0/1, USB2/3, Pin Header) |   |  |  |  |  |  |

• One (1) USB 3.1 header on the rear I/O panel (USB6, Type C)

Note: The table above is continued on the next page.

#### **Motherboard Features**

#### BIOS

- 128Mb SPI AMI BIOS®
- ACPI 3.0 or later, SMBIOS 2.7 or later, PCI F/W 3.0, BIOS rescue hot-key, RTC (Real Time Clock) wakeup

#### **Power Management**

- ACPI power management
- S3, S4, S5
- Power button override mechanism
- Power-on mode for AC power recovery
- Wake-On-LAN
- TXE Management Engine
- Force Power On by Jumper
- RTC Battery (typical voltage: 3.0V, normal discharge capacity: 220mAh)

#### System Health Monitoring

- Onboard voltage monitoring for +1.35V, +12V, +3.3V, +5V, 3.3V standby, System level control, System temperature, VBAT, VCGI
- CPU switching phase voltage regulator
- CPU thermal trip support

#### **Fan Control**

• 4-pin fan headers

#### System Management

- Trusted Platform Module (TPM) 2.0 support
- SuperDoctor® 5, Watch Dog, RoHS

#### **LED Indicators**

Power/Suspend-state indicator LED

#### **Mehcanical Specification**

- Dimensions: 4" (L) x 5.75" (W) (102mm x 146mm) SBCs
- Height: A2SAN-H/-L 33mm, A2SAN-E 42mm, X11SAN 32mm, A2SAN-H/-E/-L-WOHS 29.05mm

#### Environment

- Operating Temperature Range: A2SAN-E/-L, A2SAN-E/-L-WOHS, and A2SAN-H-WOHS support -30°C ~ 75°C (-22°F ~ 167°F). X11SAN, X11SAN-WOHS, and A2SAN-H support 0°C ~ 60°C (32°F ~ 140°F)
- Non-Operating Temperature Range: -40°C 85°C (-40°F 185°F)
- Operating Relative Humidity Range: 8% 90% (non-condensing)
- Non-Operating Relative Humidity Range: 10% 95% (non-condensing)

**Note 1:** The CPU maximum thermal design power (TDP) is subject to chassis and heatsink cooling restrictions. For proper thermal management, please check the chassis and heatsink specifications for proper CPU TDP sizing.

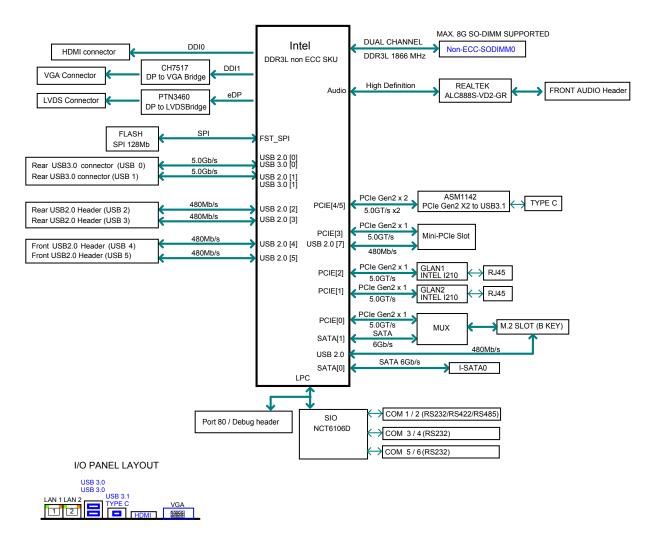


Figure 1-6. System Block Diagram

**Note:** This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.

| Model            | СРИ           | Base<br>Freq | Turbo<br>Freq | CPU<br>TDP | GbE | VGA | HDMI | LVDS | RS-232 | RS-<br>232/422/4<br>85 | USB3.0 | USB2.0 | USB3.1 | 24V out | GPIO  | Audio | TPM<br>2.0 | Temp.   | Passive<br>Heatsink      |
|------------------|---------------|--------------|---------------|------------|-----|-----|------|------|--------|------------------------|--------|--------|--------|---------|-------|-------|------------|---------|--------------------------|
| A2SAN-H          | E3940<br>(QC) | 1.6GHz       | 1.8GHz        | 9.5W       | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | 1      | N/A     | 8-bit | Yes   | Yes        | 0-60C   | SNK-<br>C0103L<br>20mm   |
| A2SAN-L          | E3930<br>(DC) | 1.3GHz       | 1.8GHz        | 6.5W       | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | N/A    | N/A     | 8-bit | Yes   | N/A        | -30-75C | SNK-<br>C0103L<br>20mm   |
| A2SAN-E          | E3940<br>(QC) | 1.6GHz       | 1.8GHz        | 9.5W       | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | N/A    | N/A     | 8-bit | Yes   | Yes        | -30-75C | SNK-<br>C0107L<br>29mm   |
| X11SAN           | N4200<br>(QC) | 1.1GHz       | 2.5GHz        | 6W         | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | 1      | N/A     | 8-bit | Yes   | Yes        | 0-60C   | SNK-<br>C0103L-1<br>19mm |
| A2SAN-H-<br>WOHS | E3940<br>(QC) | 1.6GHz       | 1.8GHz        | 9.5W       | 2   | Yes | Yes  | Yes  | 4      | 2                      | 2      | 4      | N/A    | Yes     | 8-bit | Yes   | Yes        | -30-75C | N/A                      |
| A2SAN-L-<br>WOHS | E3930<br>(DC) | 1.3GHz       | 1.8GHz        | 6.5W       | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | N/A    | N/A     | 8-bit | Yes   | N/A        | -30-75C | N/A                      |
| A2SAN-E-<br>WOHS | E3940<br>(QC) | 1.6GHz       | 1.8GHz        | 9.5W       | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | N/A    | N/A     | 8-bit | Yes   | Yes        | -30-75C | N/A                      |
| X11SAN-<br>WOHS  | N4200<br>(QC) | 1.1GHz       | 2.5GHz        | 6W         | 2   | Yes | Yes  | Yes  | 2      | 2                      | 2      | 4      | 1      | N/A     | 8-bit | Yes   | Yes        | 0-60C   | N/A                      |

Figure 1-7. A2SAN/X11SAN Series Specification Chart

**Note 1:** Audio codec operating temperature 0-60C only. Onboard TPM operating temperature -20~75C.

**Note 2:** -WOHS models do not include a heatsink. Purchase a Supermicro standard passive heatsink or provide your own thermal solution.

### **1.2 Processor Overview**

Built upon the functionality and capability of the Intel® Atom SoC series processor, the A2SAN-H/-E/-L and X11SAN motherboard offers maximum I/O expandability, energy efficiency, and data reliability in a 14-nm process architecture, and is optimized for embedded storage solutions, networking applications, or cloud-computing platforms. The A2SAN-H/-E/-L and X11SAN drastically increases system performance for a multitude of server applications.

The A2SAN-H/-E/-L and X11SAN supports the following features:

- Intel Virtualization Technology for Directed I/O (Intel VT-d)
- Enhanced Intel SpeedStep® Technology
- Video Connectors: VGA, HDMI, and LVDS
- USB3.1 Gen 2 Type-C (Only supported on X11SAN and A2SAN-H)
- Adaptive Thermal Management/Monitoring
- Mini-PCI-E slot with PCIe Gen2 x1 with transfer rates of up to 5Gb/s
- Gen3 SATA ports with transfer rates of up to 6Gb/s
- System Management Bus (SMBus) Specification, Version 2.0
- M.2 slot with B-key 2280/2242/3042 module is supported by an extender bracket
- TPM2.0 (Trusted Platform Module) onboard with Disable/Enable jumper (not supported on A2SAN-L)
- Integrated Sensor Hub (ISH)
- Intel® Identity Protection Technology

# **1.3 Special Features**

This section describes the health monitoring features of the A2SAN-H/-E/-L and X11SAN motherboard. The motherboard has an onboard System Hardware Monitor chip that supports system health monitoring.

### **Recovery from AC Power Loss**

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is Last State.

**Note:** Before setting the Recovery from AC Power Loss function in the BIOS, please adjust force power on jumper JPF1 to pins 2-3 to disable the force power-on function.

### **1.4 ACPI Features**

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a computer system including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with Windows® 10.

# **1.5 Power Supply**

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates. In areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

# 1.6 Super I/O

The Super I/O (NCT6106D chip) provides four high-speed, 16550 compatible serial communication ports (UARTs), one of which supports serial infrared communication. Each UART includes a 128 byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through a SMI or SCI function pin. It also features auto power management to reduce power consumption.

The IRQs, DMAs and I/O space resources of the Super I/O can be flexibly adjusted to meet ISA PnP requirements, which support ACPI and APM (Advanced Power Management).

# **1.7 Advanced Power Management**

The following new advanced power management features are supported by the motherboard.

### Management Engine (ME)

Intel Atom SoC only supports the TXE function, also called CSE (Converged Security Engine), which is the lite ME function.

# Chapter 2

# Installation

# 2.1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your motherboard, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

### Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of CMOS onboard battery as specified by the manufacturer. Do not install the CMOS battery upside down, which may result in a possible explosion.

### Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static protected.

# 2.2 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly.



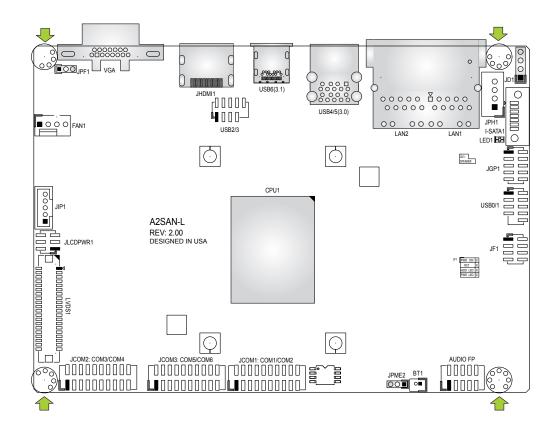




Phillips Screwdriver (1)

Phillips Screws (4)

Standoffs (4) Only if Needed



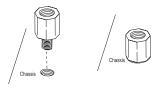
### **Location of Mounting Holes**

Note: 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation.
2) Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

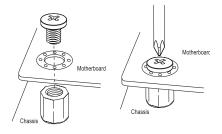
### **Tools Needed**

### Installing the Motherboard

1. Locate the mounting holes on the motherboard. See the previous page for the location.



2. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.



- 3. Install standoffs in the chassis as needed.
- 4. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.
- 5. Using the Phillips screwdriver, insert a Phillips head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
- 6. Repeat Step 5 to insert #6 screws into all mounting holes.
- 7. Make sure that the motherboard is securely placed in the chassis.

**Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

# 2.3 Memory Support and Installation

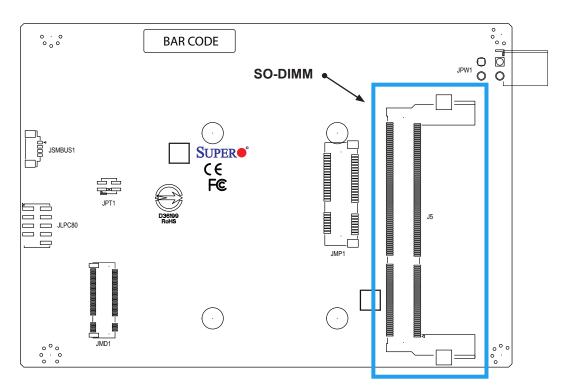
Note: Check the Supermicro website for recommended memory modules.



**Important:** Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

### **Memory Support**

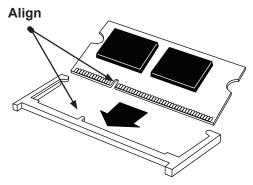
The A2SAN-H/-E/-L and X11SAN supports supports DDR3L 1333/1600/1866MHz Non-ECC SO-DIMM up to 8GB in one memory slot on the bottom side of the motherboard.



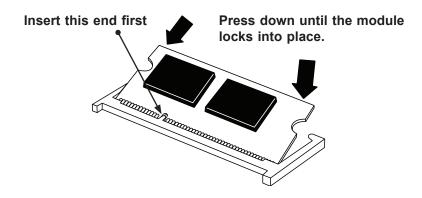
#### **Bottom Layout**

### **SO-DIMM Installation**

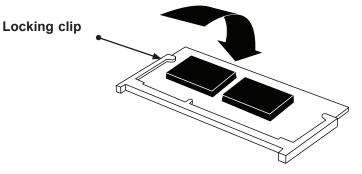
1. Position the SO-DIMM module's bottom key so it aligns with the receptive point on the slot.



2. Insert the SO-DIMM module vertically at about a 45 degree angle. Press down until the module locks into place.



3. The side clips will automatically secure the SO-DIMM module, locking it into place.



### **SO-DIMM Removal**

1. Push the side clips at the end of the slot to release the SO-DIMM module. Pull the SO-DIMM module up to remove it from the slot.

### 2.4 Rear I/O Ports

See Figure 2-1 below for the locations and descriptions of the various I/O ports on the rear of the motherboard.

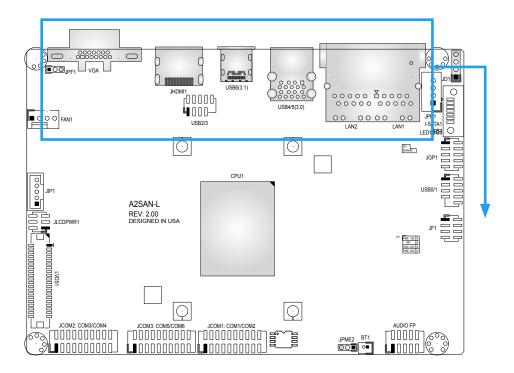
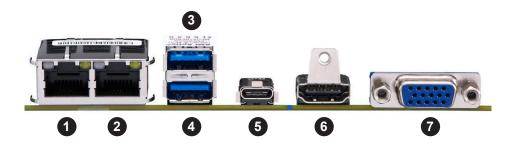


Figure 2-1. I/O Port Locations and Definitions



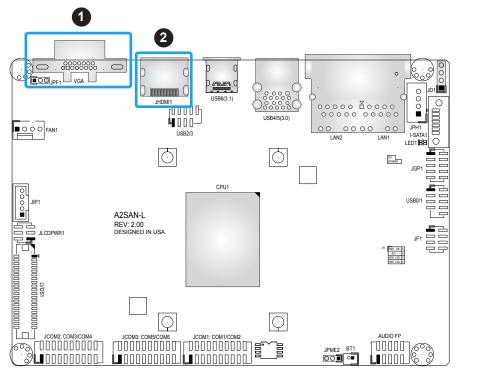
| Rear I/O Ports |             |   |             |  |  |  |  |
|----------------|-------------|---|-------------|--|--|--|--|
| #              | Description | # | Description |  |  |  |  |
| 1              | LAN1        | 5 | USB6 (3.1)  |  |  |  |  |
| 2              | LAN2        | 6 | HDMI Port   |  |  |  |  |
| 3              | USB5 (3.0)  | 7 | VGA Port    |  |  |  |  |
| 4              | USB4 (3.0)  |   |             |  |  |  |  |

### **VGA Port**

A video (VGA) port is located on the I/O back panel. Refer to the board layout below for the location.

### **HDMI** Port

The HDMI (High-Definition Multimedia Interface) port is used to display both high definition video and digital sound through an HDMI-capable display, using the same (HDMI) cable.

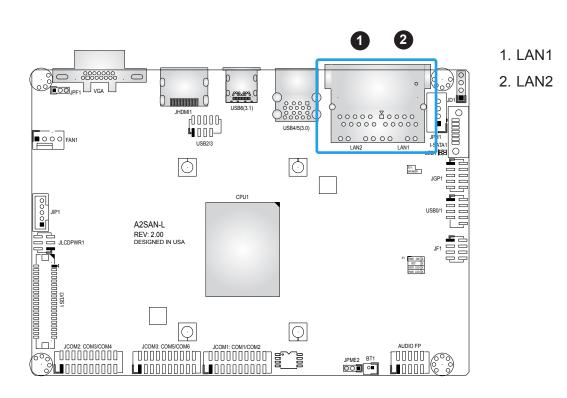


- 1. VGA Port
- 2. HDMI Port

### LAN Ports

Two LAN ports (LAN1 ~ LAN2) are located on the I/O back panel. These ports accept RJ45 type cables. Please refer to the LED Indicator section for LAN LED information. See the table below for pin definitions.

|      | LAN Port<br>Pin Definition |      |            |  |  |  |  |
|------|----------------------------|------|------------|--|--|--|--|
| Pin# | Definition                 | Pin# | Definition |  |  |  |  |
| A1   | TD1+                       | A11  | YEL-       |  |  |  |  |
| A2   | TD1-                       | A12  | YEL+       |  |  |  |  |
| A3   | TD2+                       | A13  | GRN-/ORG+  |  |  |  |  |
| A4   | TD2-                       | A14  | GRN+/ORG-  |  |  |  |  |
| A5   | CT_VCC                     | A15  |            |  |  |  |  |
| A6   | CT_VCC                     | A16  |            |  |  |  |  |
| A7   | TD3+                       | A17  |            |  |  |  |  |
| A8   | TD3-                       | A18  |            |  |  |  |  |
| A9   | TD4+                       | A19  |            |  |  |  |  |
| A10  | TD4-                       | A20  |            |  |  |  |  |



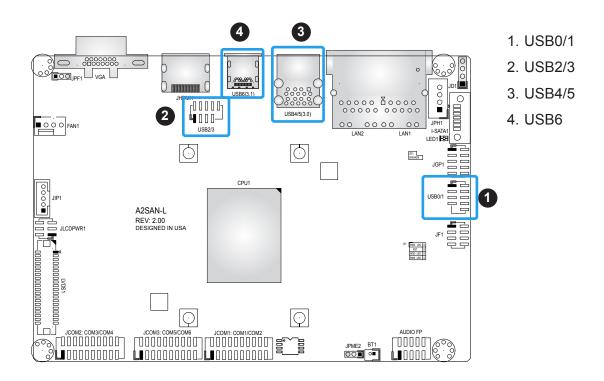
#### Universal Serial Bus (USB) Ports

There are two USB 3.0 ports (USB4/5) and one USB 3.1 Type C port (USB6) on the I/O back panel. The motherboard also has two front access USB 2.0 headers (USB0/1 and USB2/3) that provide four USB connections. The onboard headers can be used to provide front side USB access with a cable. Two USB 2.0 cables for front panel support are included with the motherboard.

|      | Back Panel USB 3.0<br>Pin Definitions |      |              |  |  |  |  |
|------|---------------------------------------|------|--------------|--|--|--|--|
| Pin# | Definition                            | Pin# | Definition   |  |  |  |  |
| A1   | VBUS                                  | B1   | VBUS         |  |  |  |  |
| A2   | D1-N                                  | B2   | D2-N         |  |  |  |  |
| A3   | D1-P                                  | B3   | D2-P         |  |  |  |  |
| A4   | GND                                   | B4   | GND          |  |  |  |  |
| A5   | Stda_SSRX1-N                          | B5   | Stda_SSRX2-N |  |  |  |  |
| A6   | Stda_SSRX1-P                          | B6   | Stda_SSRX2-P |  |  |  |  |
| A7   | GND_DRAIN                             | B7   | GND_DRAIN    |  |  |  |  |
| A8   | Stda_SSTX1-N                          | B8   | Stda_SSTX2-N |  |  |  |  |
| A9   | Stda_SSTX1-P                          | В9   | Stda_SSTX2-P |  |  |  |  |

|      | Front Panel USB 2.0 Ports 0/1<br>Header Pin Definitions |      |            |  |
|------|---|------|------------|--|
| Pin# | Definition  | Pin# | Definition |  |
| 1    | P5V_DUAL_F  | 2    | P5V_DUAL_F |  |
| 3    | USBCON_N2   | 4    | USBCON_N3  |  |
| 5    | USBCON_P2   | 6    | USBCON_P3  |  |
| 7    | Ground  | 8    | Ground     |  |
| 9    |   | 10   | NC         |  |

|      | Front Panel USB 2.0 Ports 2/3<br>Header Pin Definitions |      |            |  |
|------|---|------|------------|--|
| Pin# | Definition  | Pin# | Definition |  |
| 1    | P5V_DUAL_F  | 2    | P5V_DUAL_F |  |
| 3    | USBCON_N4   | 4    | USBCON_N5  |  |
| 5    | USBCON_P4   | 6    | USBCON_P5  |  |
| 7    | Ground  | 8    | Ground     |  |
| 9    |   | 10   | NC         |  |



# 2.5 Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro chassis. See the figure below for the descriptions of the front control panel buttons and LED indicators.

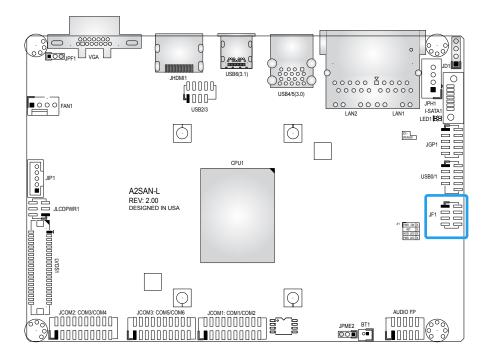
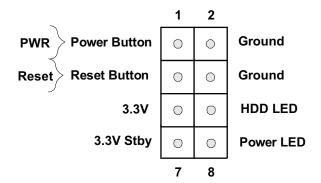


Figure 2-2. JF1 Header Pins



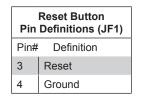
#### **Power Button**

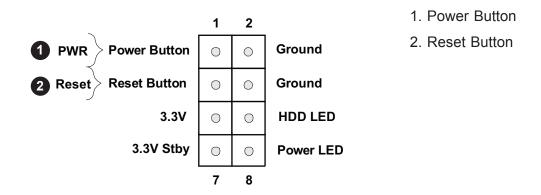
The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power in the suspend mode, press the button for at least 4 seconds. See the table below for pin definitions.

| Power Button<br>Pin Definitions (JF1) |              |  |
|---------------------------------------|--------------|--|
| Pin# Definition                       |              |  |
| 1                                     | Power Button |  |
| 2                                     | GND          |  |

#### **Reset Button**

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the computer case to reset the system. See the table below for pin definitions.





### HDD LED

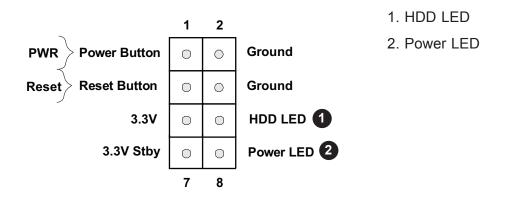
The HDD LED connection is located on pins 5 and 6 of JF1. Attach a cable here to indicate the status of HDD-related activities, including SATA activities. See the table below for pin definitions.

| Pin  | HDD LED<br>Pin Definitions (JF1) |  |
|------|----------------------------------|--|
| Pin# | Definition                       |  |
| 5    | +3.3V                            |  |
| 6    | HDD Active LOW                   |  |

### Power LED

The Power LED connection is located on pins 7 and 8 of JF1. See the table below for pin definitions.

| Power LED<br>Pin Definitions (JF1) |               |
|------------------------------------|---------------|
| Pin#                               | Definition    |
| 7                                  | +3.3VSB       |
| 8                                  | Power LED LOW |



# 2.6 Connectors

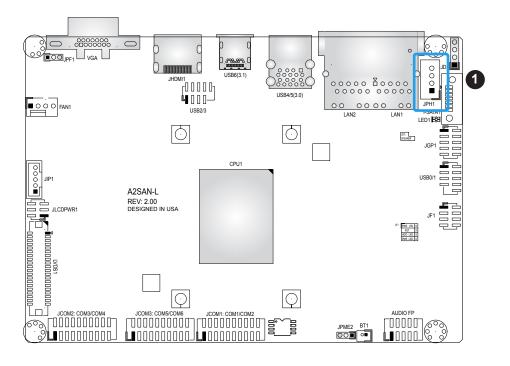
### **Power Connections**

### **SATA Power Connector**

The 4-pin SATA power connector JPH1 provides power to onboard HDD devices. See the table below for pin definitions.

| 4-pin HDD Power<br>Pin Definitions |            |
|------------------------------------|------------|
| Pin#                               | Definition |
| 1                                  | 12V        |
| 2-3                                | Ground     |
| 4                                  | 5V         |

1. SATA Power Connector

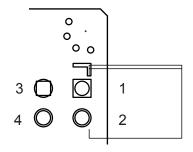


### 4-pin 12V-Standby R/A Type Power Connector

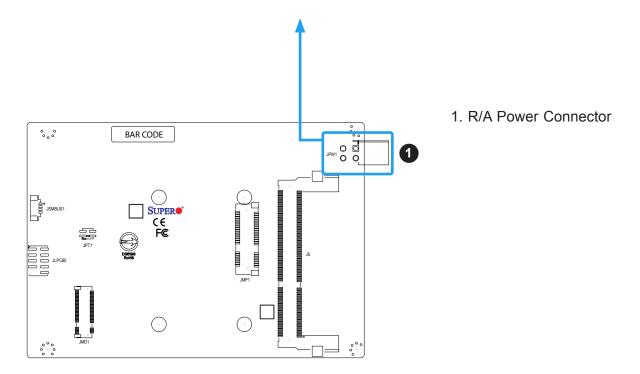
The R/A type power connector is located at JPW1 on the bottom side of the motherboard. See the table below for pin definitions.

| +12V 4-pin Power<br>Pin Definitions |            |
|-------------------------------------|------------|
| Pin#                                | Definition |
| 1                                   | +12VSB     |
| 2                                   | +12VSB     |
| 3                                   | GND        |
| 4                                   | GND        |

**Required Connection** 



Pin Layout (on the bottom side)

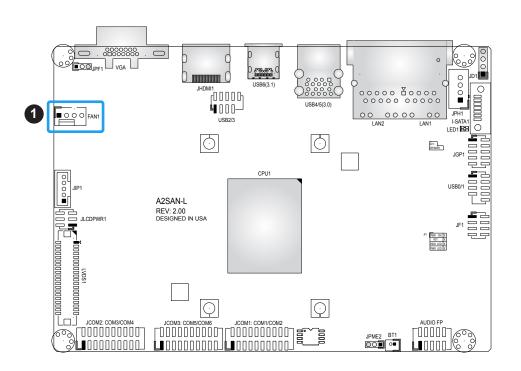


### Headers

### Fan Header

There is one fan header with 4-pins on the motherboard. Pins 1-3 are backward compatible with traditional 3-pin fans. The onboard fan speeds are controlled by Thermal Management (via Hardware Monitoring) in the BIOS. When using Thermal Management setting, <u>please</u> use all 3-pin fans or all 4-pin fans.

| Fan Header<br>Pin Definitions |                |
|-------------------------------|----------------|
| Pin#                          | Definition     |
| 1                             | Ground (Black) |
| 2                             | +12V (Red)     |
| 3                             | Tachometer     |
| 4                             | PWM Control    |

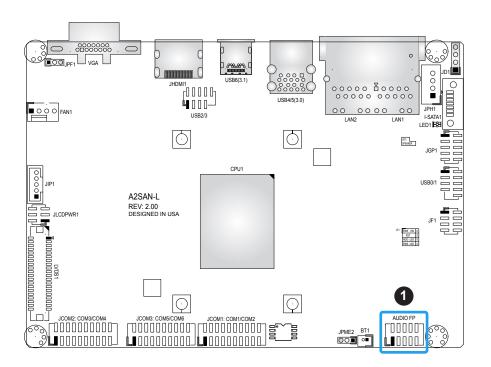


1. FAN1

# Front Accessible Audio Header

A 10-pin audio header located on the motherboard allows you to use the onboard sound for audio playback. Connect an audio cable to the this header to use this feature. See the table below for pin definitions.

|      | Audio Header<br>Pin Definitions |    |              |  |  |  |
|------|---------------------------------|----|--------------|--|--|--|
| Pin# | in# Definition Pin# Definition  |    |              |  |  |  |
| 1    | MIC_Left                        | 2  | AUDIO_GND    |  |  |  |
| 3    | MIC_Right                       | 4  | AUDIO_Detect |  |  |  |
| 5    | LINE2_Right                     | 6  | MIC2_JD      |  |  |  |
| 7    | Front AUDIO_JD                  | 8  | NC           |  |  |  |
| 9    | LINE2_Left                      | 10 | LINE2_JD     |  |  |  |



1. Audio Header

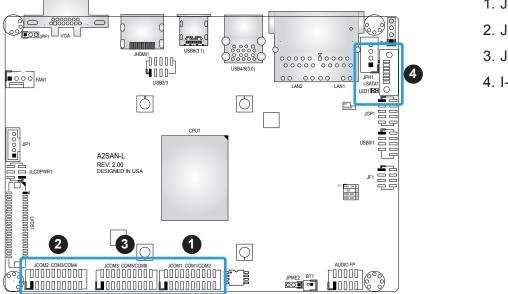
# **COM Headers**

Two COM headers (JCOM1: COM1/COM2 supports two RS232/RS422/RS485 and JCOM2/3: COM3/COM4/COM5/COM6 supports four RS232) are located on the motherboard. COM 5/6 are only supported on A2SAN-H-WOHS. See the table below for pin definitions.

| Serial COM Header (JCOM1)<br>Pin Definitions |  |      |            |   | 5   | Serial COM He<br>Pin Def | •    | ,          |
|--|--|------|------------|---|-----|--------------------------|------|------------|
| Pin#   | Definition   | Pin# | Definition | Р | in# | Definition               | Pin# | Definition |
|  | DCD1   |      | DSR1       | 1 |     | DCD3                     | 2    | DSR3       |
| 1  | or RS-485/422_COM1_TX- (Full Duplex)   | 2    |            | 3 |     | RXD3                     | 4    | RTS3       |
|  | or RS-485_COM1_Data- (Half Duplex)   |      |            | 5 |     | TXD3                     | 6    | CTS3       |
|  | RXD1   |      | RTS1       | 7 |     | DTR3                     | 8    | RI3_N      |
| 3  | or RS-485/422_COM1_TX+ (Full Duplex)   | 4    |            | 9 |     | GND                      | 10   | N/A        |
| -  | or RS-485_COM1_Data+ (Half Duplex)   | 0    | 0704       | 1 | 1   | DCD4                     | 12   | DSR4       |
| 5  | TXD1 or RS-485/422_COM1_RX+ (Full Duplex)  | 6    | CTS1       | 1 | 3   | RXD4                     | 14   | RTS4       |
| 7  | DTR1 or RS-485/422_COM1_RX- (Full Duplex)  | 8    | RI1_N      | 1 | 5   | TXD4                     | 16   | CTS4       |
| 9  | GND  | 10   | N/A        |   | 7   | DTR4                     | 18   |            |
|  | DCD2   |      | DSR2       |   | -   |                          |      | RI4_N      |
| 11   | or RS-485/422_COM2_TX- (Full Duplex)<br>or RS-485_COM2_Data- (Half Duplex)         | 12   |            | 1 | 9   | GND                      | 20   | N/A        |
| 13   | RXD2<br>or RS-485/422_COM2_TX+ (Full Duplex)<br>or RS-485_COM2_Data+ (Half Duplex) | 14   | RTS2       |   |     |                          |      |            |
| 15   | TXD2 or RS-485/422_COM2_RX+ (Full Duplex)  | 16   | CTS2       |   |     |                          |      |            |
| 17   | DTR2 or RS-485/422_COM2_RX- (Full Duplex)  | 18   | RI_N2      |   |     |                          |      |            |
| 19   | GND  | 20   | N/A        |   |     |                          |      |            |

# **SATA Ports**

The A2SAN-H/-E/-L and X11SAN has one SATA 3.0 port (I-SATA1) that is supported by the Intel® Atom SoC.



- 1. JCOM1
- 2. JCOM2
- 3. JCOM3
- 4. I-SATA1

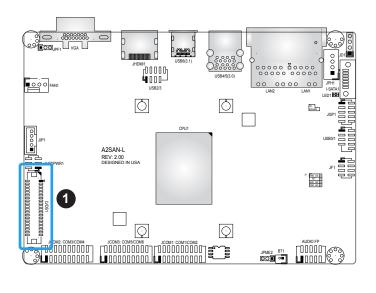
# **LVDS** Connector

LVDS1 is the LVDS connector. LVDS (low-voltage differential signaling) is a high-speed digital interface that operates at low power. It is a type of connection that is used with a LVDS LCD panel. The connector combines LCD VCC Power (pins 9-10), LVDS high speed digital interface, backlight power 3.3V (pin 7) and 12V (pins 1-5), backlight enable (pin 15), and dimming control (pin 13). Select the correct LCD VCC power according to the LCD specification by JLCDPWR1 (3.3V/5V/12V) before enabling the LVDS panel. Refer to the tables below for vendor part number, mating, and crimping contact connector information before making the LVDS/backlight cable.

|  | LVDS Connector |                      |   |  |  |
|--|----------------|----------------------|---|--|--|
| Connector Vendor Manufacture P/N Description |                | Description          |   |  |  |
| Onboard LVDS<br>Connector                    | HIROSE         | DF13E-40DP-1.25V(51) | BOX HEADER, BOARD TO WIRE, 2X20, PITCH<br>1.25MM, VERT, 1A/PIN, WHITE, 0.2UM GOLD,<br>PA9T, MATING HEIGHT 5.8MM |  |  |
| Mating Connector                             | HIROSE         | DF13-40DS-1.25C      | Headers and Wire Housings 1.25MM RECEPT<br>HSNG 40P DUAL ROW CRIMP  |  |  |
| Crimping Contact<br>Connector                | HIROSE         | DF13G-2630SCFA       | Headers and Wire Housings SOCKET CONTACT/<br>REEL AWG26-30  |  |  |

Note: Enable the LVDS Panel Support feature in the BIOS to use the LVDS panel display. Advanced-> Chipset-> System Agent (SA) Configuration-> Graphics Configuration-> LVDS Panel Support [Enabled]

1. LVDS Connector



| LVDS Connector<br>Pin Definitions |            |      |            |  |  |
|-----------------------------------|------------|------|------------|--|--|
| Pin#                              | Definition | Pin# | Definition |  |  |
| 39                                | GND        | 40   | GND        |  |  |
| 37                                | LVDSB D3N  | 38   | LVDSB D3P  |  |  |
| 35                                | LVDSB CLKN | 36   | LVDSB CLKP |  |  |
| 33                                | LVDSB D2N  | 34   | LVDSB D2P  |  |  |
| 31                                | LVDSB D1N  | 32   | LVDSB D1P  |  |  |
| 29                                | LVDSB D0N  | 30   | LVDSB D0P  |  |  |
| 27                                | GND        | 28   | GND        |  |  |
| 25                                | LVDSA D3N  | 26   | LVDSA D3P  |  |  |
| 23                                | LVDSA CLKN | 24   | LVDSA CLKP |  |  |
| 21                                | LVDSA D2N  | 22   | LVDSA D2P  |  |  |
| 19                                | LVDSA D1N  | 20   | LVDSA D1P  |  |  |
| 17                                | LVDSA D0N  | 18   | LVDSA D0P  |  |  |
| 15                                | BKLTEN     | 16   | GND        |  |  |
| 13                                | BKLTCTL    | 14   | PVCCEN     |  |  |
| 11                                | DDC CLK    | 12   | DDC DATA   |  |  |
| 9                                 | LCDVCC     | 10   | LCDVCC     |  |  |
| 7                                 | 3.3V       | 8    | GND        |  |  |
| 5                                 | 12V        | 6    | GND        |  |  |
| 3                                 | 12V        | 4    | 12V        |  |  |
| 1                                 | 12V        | 2    | 12V        |  |  |

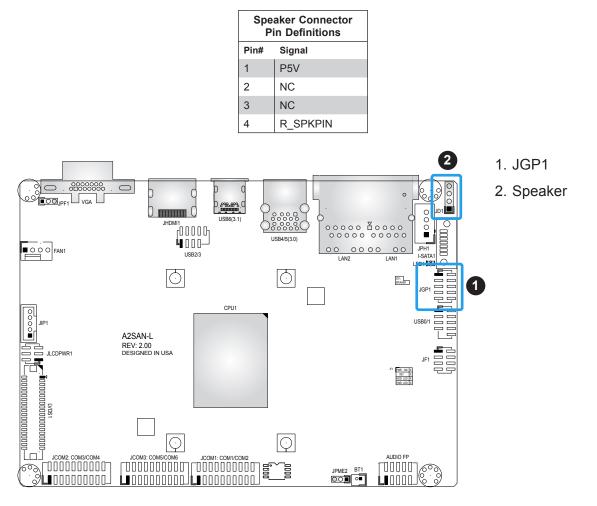
# General Purpose I/O Header

The JGP1 (General Purpose Input/Output) header is a general purpose I/O expander on a pin header via the SMBus. Each pin can be configured to be an input pin or output pin. The GPIO is controlled via the PCA9554APW 8-bit GPIO expansion from PCH SMBus. The base address is 0xF040. The expander slave address is 0x4C for WRITE and READ. See the table below for pin definitions.

| GPIO Header<br>Pin Definitions |             |      |             |  |
|--------------------------------|-------------|------|-------------|--|
| Pin#                           | Definition  | Pin# | Definition  |  |
| 1                              | P3V3SB      | 2    | GND         |  |
| 3                              | GP_P3V3_GP0 | 4    | GP_P3V3_GP4 |  |
| 5                              | GP_P3V3_GP1 | 6    | GP_P3V3_GP5 |  |
| 7                              | GP_P3V3_GP2 | 8    | GP_P3V3_GP6 |  |
| 9                              | GP_P3V3_GP3 | 10   | GP_P3V3_GP7 |  |

# Speaker

If you wish to use an external speaker, connect a cable to pins 1-4 on the JD1 header. See the table below for pin definitions.

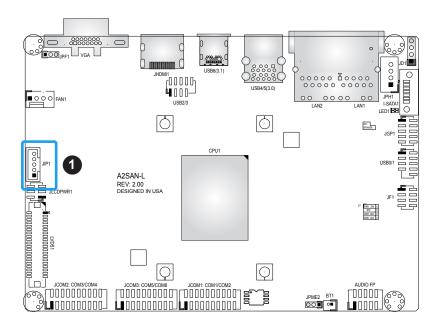


# LVDS Inverter Backlight Power

The LVDS Inverter Backlight Power header is located at JIP1 on the motherboard. It supplies 12V and 5V of backlight power to the LCD panel besides LVDS1. See the tables below for pin definitions, vendor connector part numbers, and mating housing connector information.

| LVDS        | LVDS Inverter Bicklight Power<br>Pin Definitions |  |  |
|-------------|--|--|--|
| Pin# Signal |  |  |  |
| 1           | P12V   |  |  |
| 2           | GND  |  |  |
| 3           | LVDS Backlight Enable                            |  |  |
| 4           | PWM_LVDS_P5V                                     |  |  |
| 5           | P5V  |  |  |

| Inverter Backlight Power Connector    |        |                 |   |
|---------------------------------------|--------|-----------------|---|
| Connector                             | Vendor | Manufacture P/N | Description   |
| Inverter Backlight<br>Power Connector | PONY   | 231-05-010012   | BOX/HEADER, 1X5 PIN, PITCH 2MM, DIP, VERT,<br>1A/PIN, WHITE, TIN PLATED, TL3.4MM, PA46,<br>ROHS |
| Mating Housing<br>Connector           | PONY   | HJ20-05         | Mating Connector of 231-05-010012   |

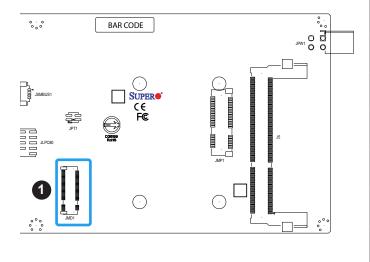


1. LVDS Inverter Backlight Power

# M.2 Slot

M.2 is formerly known as Next Generation Form Factor (NGFF) and is located at JMD1 on the bottom side of the motherboard. The M.2 slot is designed for internal mounting devices. The A2SAN-H/-E/-L and X11SAN motherboard deploys a B-KEY for SATA/PCIe SSD devices or USB/PCIe WWAN or GNSS card. The A2SAN-H/-E/-L and X11SAN deploys a 2280 screw hole location for a 2280 M.2 module. 2242 and 3042 M.2 modules are supported by an extender bracket (P/N: MCP-290-00161-0N) to fit on the 2280 screw hole location.

| 1. M.2 Slot |  |
|-------------|--|
|-------------|--|

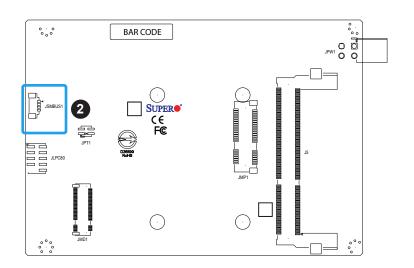


| M.2 Slot Pin Definitions |                                       |    |   |  |
|--------------------------|---------------------------------------|----|---|--|
| Pin#                     | Pin# Definition Pin# Definition       |    |   |  |
| 1                        | NC                                    | 2  | P3V3SB  |  |
| 3                        | GND                                   | 4  | P3V3SB  |  |
| 5                        | GND                                   | 6  | FULL_CARD_POW-<br>ER_OFF#(PU to<br>P1V8SB only) |  |
| 7                        | USB_D+                                | 8  | W_DISABLE1#(PU<br>to P3V3SB only)               |  |
| 9                        | USB_D-                                | 10 | NC  |  |
| 11                       | GND                                   | 12 |   |  |
| 13                       |                                       | 14 |   |  |
| 15                       |                                       | 16 |   |  |
| 17                       |                                       | 18 |   |  |
| 19                       |                                       | 20 | NC  |  |
| 21                       | NC                                    | 22 | NC  |  |
| 23                       | WWAN_<br>WAKE_N(PU to<br>P1V8SB only) | 24 | NC  |  |
| 25                       | NC                                    | 26 | RF_KILL_<br>GPS_1P8_N(PU to<br>P1V8SB only)     |  |
| 27                       | GND                                   | 28 | NC  |  |
| 29                       | NC                                    | 30 | NC  |  |
| 31                       | NC                                    | 32 | NC  |  |
| 33                       | GND                                   | 34 | NC  |  |
| 35                       | NC                                    | 36 | NC  |  |
| 37                       | NC                                    | 38 | DEVSLP<br>(reserved)                            |  |
| 39                       | GND                                   | 40 | SMB_CLK<br>(reserved)                           |  |
| 41                       | PERn0/SATARX+                         | 42 | SMB_DATA<br>(reserved)                          |  |
| 43                       | PERp0/SATARX-                         | 44 | ALERT(PU to<br>P1V8SB only)                     |  |
| 45                       | GND                                   | 46 | NC  |  |
| 47                       | PETn0/SATATX-                         | 48 | NC  |  |
| 49                       | PETn0/SATATX+                         | 50 | PERST (PLTRST)                                  |  |
| 51                       | GND                                   | 52 | CLK_REQ_N                                       |  |
| 53                       | REFCLK-                               | 54 | PE_WAKE_N                                       |  |
| 55                       | REFCLK+                               | 56 | NC  |  |
| 57                       | GND                                   | 58 | NC  |  |
| 59                       | NC                                    | 60 | NC  |  |
| 61                       | NC                                    | 62 | NC  |  |
| 63                       | NC                                    | 64 | NC  |  |
| 65                       | NC                                    | 66 | NC  |  |
| 67                       | NC                                    | 68 | SYSCLK (reserved)                               |  |
| 69                       | PE_DET                                | 70 | P3V3SB  |  |
| 71                       | GND                                   | 72 | P3V3SB  |  |
| 73                       | GND                                   | 74 | P3V3SB  |  |
| 75                       | NC                                    |    |   |  |

# System Management Bus Header

A System Management Bus header for additional slave devices or sensors is located at JSMBUS1 on the bottom side of the motherboard. See the table below for pin definitions.

| SMBus Header<br>Pin Definitions |                 |  |  |  |
|---------------------------------|-----------------|--|--|--|
| Pin#                            | Pin# Definition |  |  |  |
| 1                               | SMB_DATA        |  |  |  |
| 2                               | GND             |  |  |  |
| 3                               | SMB_CLK         |  |  |  |
| 4                               | NC              |  |  |  |



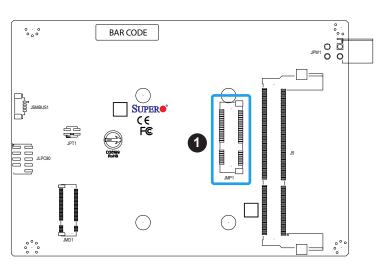
1. SMBus Header

# Mini PCI-E Slot

The Mini PCI-E slot, located at JMP1 on the bottom side of the motherboard, is used to install a compatible Mini PCI-E device. The Mini PCI-E slot supports modules which are USB or PCI-E x1 devices, such as wireless, GNSS, and bluetooth modules. See the table below for pin definitions.

| Mini PCI-E<br>Pin Definitions |            |      |                   |  |
|-------------------------------|------------|------|-------------------|--|
| Pin#                          | Definition | Pin# | Definition        |  |
| 52                            | +3.3Vaux   | 51   | NC                |  |
| 50                            | GND        | 49   | NC                |  |
| 48                            | +1.5V      | 47   | NC                |  |
| 46                            | NC         | 45   | NC                |  |
| 44                            | NC         | 43   | NC                |  |
| 42                            | NC         | 41   | +3.3Vaux          |  |
| 40                            | GND        | 39   | NC                |  |
| 38                            | USB_D+     | 37   | GND               |  |
| 36                            | USB_D-     | 35   | GND               |  |
| 34                            | GND        | 33   | PETp0             |  |
| 32                            | SMB_DATA   | 31   | PETn0             |  |
| 30                            | SMB_CLK    | 29   | GND               |  |
| 28                            | +1.5V      | 27   | GND               |  |
| 26                            | GND        | 25   | PERp0             |  |
| 24                            | +3.3Vaux   | 23   | PERn0             |  |
| 22                            | PERST#     | 21   | DET_CARD_<br>PLUG |  |
| 20                            | NC         | 19   | NC                |  |
| 18                            | GND        | 17   | NC                |  |
| 16                            | NC         | 15   | GND               |  |
| 14                            | NC         | 13   | REFCLK+           |  |
| 12                            | NC         | 11   | REFCLK-           |  |
| 10                            | NC         | 9    | GND               |  |
| 8                             | NC         | 7    | CLKREQ#           |  |
| 6                             | 1.5V       | 5    | NC                |  |
| 4                             | GND        | 3    | NC                |  |
| 2                             | 3.3Vaux    | 1    | WAKE#             |  |

1. Mini PCI-E

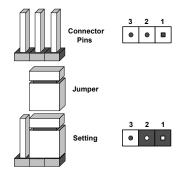


# 2.7 Jumper Settings

# **How Jumpers Work**

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the diagram below for an example of jumping pins 1 and 2. Refer to the motherboard layout page for jumper locations.

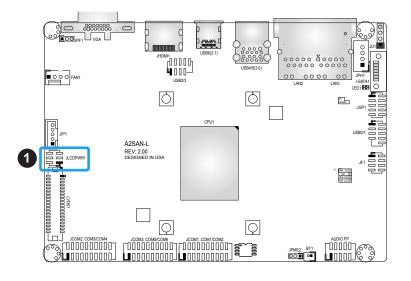
**Note:** On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.



#### JLCDPWR1

Use this jumper to select the power voltage for the LVDS panel. Make sure that the specifications of the cable is compatible with the panel to prevent damage.

| LVDS VCC Power Selection<br>Jumper Settings |                |  |  |
|---|----------------|--|--|
| Jumper Setting                              | Definition     |  |  |
| Pins 1-3                                    | 3.3V (Default) |  |  |
| Pins 3-5                                    | 5V             |  |  |
| Pins 3-4                                    | 12V            |  |  |



1. JLCDPWR1

# **Power Force On**

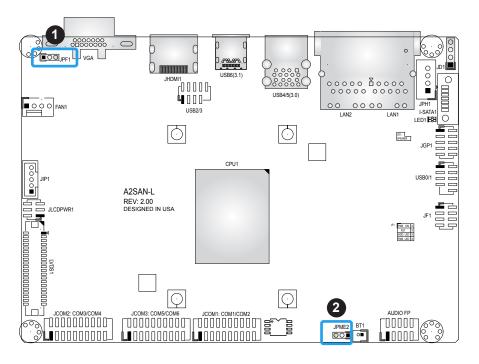
Use this jumper to select the "POWER FORCE ON" function when the AC power cord is plugged in. When enabling force power on and AC power recovery, the system will boot up automatically without pressing the power button.

| Power Force On<br>Jumper Settings |   |  |
|-----------------------------------|---|--|
| Jumper Setting                    | Definition  |  |
| Pins 1-2                          | Power Force On (Default)<br>(when AC power cord is plugged) |  |
| Pins 2-3                          | PWR BTN Power On<br>(when AC power cord is plugged)         |  |

# Manufacturing Mode Select

Close JPME2 to bypass SPI flash security and force the system to use the Manufacturing Mode, which will allow you to flash the system firmware from a host server to modify system settings. See the table below for jumper settings.

| Manufacturing Mode<br>Jumper Settings |                    |  |
|---------------------------------------|--------------------|--|
| Jumper Setting                        | Definition         |  |
| Pins 1-2                              | Normal (Default)   |  |
| Pins 2-3                              | Manufacturing Mode |  |



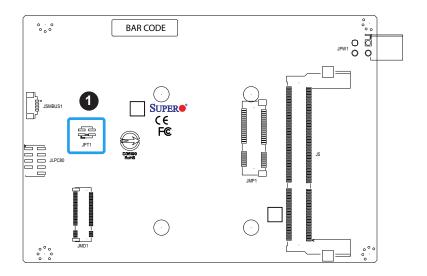
- 1. Power Force On
- 2. Manufacturing Mode

# **TPM Enable/Disable**

Use the JPT1 jumper to enable or disable the TPM feature. See the table below for jumper settings.

| TPM Enable/Disable<br>Jumper Settings |                  |  |
|---------------------------------------|------------------|--|
| Jumper Setting                        | Definition       |  |
| Pins 1-2                              | Enable (Default) |  |
| Pins 3-4                              | Disable          |  |

1. TPM Enable/Disable Header



# 2.8 LED Indicators

# LAN Port LEDs

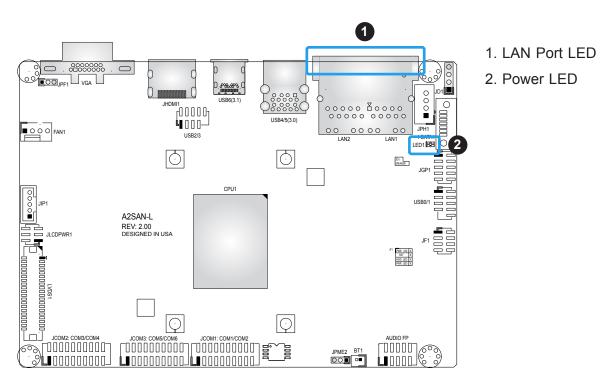
Two LAN ports (LAN 1 and LAN 2) are located on the I/O back panel of the motherboard. Each Ethernet LAN port has two LEDs. The green LED indicates activity, while the other Link LED may be green, amber, or off to indicate the speed of the connection.

| LAN1/2 LED<br>(Connection Speed<br>Indicator) |            |  |
|---|------------|--|
| LED Color                                     | Definition |  |
| Off   | 10 Mb/s    |  |
| Green   | 100 Mb/s   |  |
| Amber   | 1 Gb/s     |  |

# Power LED

LED1 is the Power LED. In S0 mode, this LED will be solid green. In S3/S4/S5 modes, this LED will be solid red.

| Onboard Power LED Indicator |                  |  |
|-----------------------------|------------------|--|
| LED Color                   | Definition       |  |
|                             | System Off       |  |
| Off                         | (power cable not |  |
|                             | connected)       |  |
| Solid Green                 | S0 mode          |  |
| Solid Red                   | S3/S4/S5 modes   |  |



# Chapter 3

# Troubleshooting

# **3.1 Troubleshooting Procedures**

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/ or 'Returning Merchandise for Service' section(s) in this chapter. <u>Always disconnect the AC power cord before adding, changing or installing any non hot-swap hardware components</u>.

# Before Power On

- 1. Check that the power LED on the motherboard is on.
- 2. Make sure that the power connector is connected to your power supply.
- 3. Make sure that no short circuits exist between the motherboard and chassis.
- 4. Disconnect all cables from the motherboard, including those for the keyboard and mouse.
- 5. Remove all add-on cards.
- 6. Install a heatsink and connect the power to the motherboard. Make sure that the heatsink is fully seated. Check all jumper settings as well.
- 7. Use the correct type of CMOS battery (CR2032) as recommended by the manufacturer.

# No Power

- 1. Make sure that no short circuits exist between the motherboard and the chassis.
- 2. Verify that all jumpers are set to their default positions.
- 3. Check that the 115V/230V switch on the power supply is properly set.
- 4. Turn the power switch on and off to test the system.
- 5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

# No Video

- 1. If the power is on but you have no video, remove all the add-on cards and cables.
- 2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.

Note: If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to Appendix B.

# System Boot Failure

If the system does not display POST (Power-On-Self-Test) or does not respond after the power is turned on, check the following:

- 1. Check for any error beep from the motherboard speaker.
- If there is no error beep, try to turn on the system without DIMM modules installed. If there is still no error beep, replace the motherboard.
- If there are error beeps, clear the CMOS settings by unplugging the power cord and removing the battery from BT1, then short pins 1 and 2 for more than 10 seconds and install the battery into BT1.
- 2. Remove all components from the motherboard, especially the DIMM modules. Make sure that system power is on and that memory error beeps are activated.

# Memory Errors

- 1. Make sure that the DIMM modules are properly and fully installed.
- 2. Confirm that you are using the correct memory. Also, it is recommended that you use the same memory type and speed for all DIMMs in the system. See Section 2.4 for memory details.
- 3. Check for bad DIMM modules or slots by swapping modules between slots and noting the results.
- Check the power supply voltage 115V/230V switch.

# Losing the System's Setup Configuration

- 1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1.5 for details on recommended power supplies.
- 2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
- 3. If the above steps do not fix the setup configuration problem, contact your vendor for repairs.

# When the System Becomes Unstable

- A. If the system becomes unstable during or after OS installation, check the following:
- 1. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.

**Note**: Click on the Tested Memory List link on the motherboard product page to see a list of supported memory.

- 2. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
- 3. Heatsink: Check that the heatsink is installed properly.
- 4. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on the minimum power requirements.
- 5. Proper software support: Make sure that the correct drivers are used.

# B. If the system becomes unstable before or during OS installation, check the following:

- 1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD/DVD and CD/DVD-ROM.
- 2. Cable connection: Check to make sure that all cables are connected and working properly.
- 3. Using the minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use the minimum configuration (but with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.

- 4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.
- 5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
- 6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

# **3.2 Technical Support Procedures**

<u>Before contacting Technical Support, please take the following steps.</u> Also, note that as a motherboard manufacturer, we do not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

- Please review the 'Troubleshooting Procedures' and 'Frequently Asked Questions' (FAQs) sections in this chapter or see the FAQs on our website at http://www. supermicro.com/FAQ/index.php before contacting Technical Support.
- 2. BIOS upgrades can be downloaded from our website at http://www.supermicro.com/ ResourceApps/BIOS\_IPMI\_Intel.html. Note: Not all BIOS can be flashed depending on the modifications to the boot block code.
- 3. If you still cannot resolve the problem, include the following information when contacting us for technical support:
- Motherboard model and PCB revision number
- BIOS release date/version (this can be seen on the initial display when your system first boots up)
- System configuration

An example of a Technical Support form is posted on our website.

<u>Distributors</u>: For immediate assistance, please have your account number ready when contacting our technical support department by e-mail.

# **3.3 Frequently Asked Questions**

# Question: What type of memory does my motherboard support?

**Answer:** The A2SAN-H/-E/-L and X11SAN motherboard supports up to 8GB of DDR3L 1333/1600/1866MHz Non-ECC SO-DIMM. See Section 2.3 for details on installing memory.

# Question: How do I update my BIOS?

**Answer:** It is recommended that you <u>do not</u> upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at http:// www.supermicro.com/ResourceApps/BIOS\_IPMI\_Intel.html. Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading.

Follow the steps below to update the BIOS:

- 1. Save this BIOS update package to your computer.
- 2. Extract the files to a USB stick. The USB stick doesn't have to be bootable, but it has to be formatted with the FAT/FAT32 file system.
- 3. Plug the USB stick into a USB port, boot to the Build-In UEFI Shell, and type FLASH. nsh BIOSname#.### to start the BIOS update:
  - Shell> fs0:
  - fs0:\> flash.nsh A2SAN#.###
- 4. Do not interrupt the process until the BIOS update is complete.
- 5. When a message indicates that the BIOS update is complete, do the A/C power cycle.
- 6. Go to the BIOS setup screen and press F3 to load the default and then press F4 to save and exit.

# Question: Why can't I turn off the power using the momentary power on/off switch?

**Answer:** The instant power off function is controlled in BIOS by the Power Button Mode setting. When the On/Off feature is enabled, the motherboard will have instant off capabilities as long as the BIOS has control of the system. When the Standby or Suspend feature is enabled or when the BIOS is not in control such as during memory count (the first screen that appears when the system is turned on), the momentary on/off switch must be held for more than four seconds to shut down the system. This feature is required to implement the ACPI features on the motherboard.

# 3.4 Battery Removal and Installation

# **Battery Removal**

To remove the battery, follow the steps below:

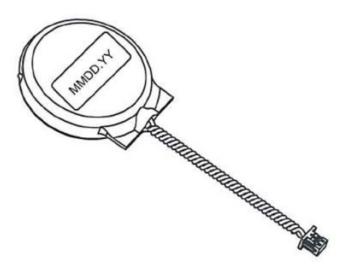
- 1. Power off your system and unplug your power cable.
- 2. Remove the battery cable at the BT1 connector on the board.
- 3. Remove the battery.

# **Proper Battery Disposal**

Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

# **Battery Installation**

- 1. Unplug the power cord.
- 2. Connect the battery cable into the battery connector (BT1) and push it down until you hear a click to ensure that the cable is securely locked.
- 3. Use the foam tape on the back side of the battery to secure the battery to a flat surface on the bottom of the motherboard or a proper location in the system. DO NOT place the battery on the heatsink.



# 3.5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (http://www.supermicro.com/ support/rma/).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

# Chapter 4

# BIOS

# 4.1 Introduction

This chapter describes the AMIBIOS<sup>™</sup> Setup utility for the A2SAN-H motherboard. The BIOS is stored on a chip and can be easily upgraded using a flash program.

**Note:** Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to BIOS that may not be reflected in this manual.

# Starting the Setup Utility

To enter the BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

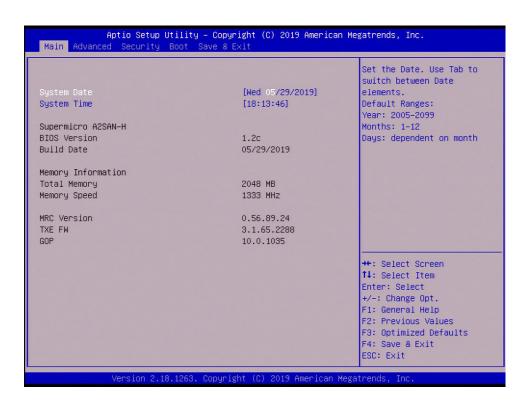
The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. "Grayed-out" options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A " ▶ " indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F10>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

# 4.2 Main

When you first enter the AMI BIOS setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below.



# System Date/System Time

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in MM/DD/YYYY format. The time is entered in HH:MM:SS format.

Note: The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00.

Supermicro A2SAN-H

**BIOS Version** 

**Build Date** 

# **Memory Information**

Total Memory: This feature displays the total size of memory available in the system.

Memory Speed: This feature displays the memory speed.

**MRC Version** 

TXE FW

GOP

# 4.3 Advanced

Use this menu to configure Advanced settings.

| <ul> <li>Boot Feature</li> <li>CPU Configuration</li> <li>Chipset Configuration</li> <li>SATA Configuration</li> <li>Trusted Computing</li> <li>ACPI Settings</li> <li>Super IO Configuration</li> <li>NCT6106D HW Monitor</li> <li>Serial Port Console Redirection</li> <li>PCIe/PCI/PnP Configuration</li> <li>ISCSI Configuration</li> <li>ISCSI Configuration</li> <li>Intel(R) I210 Gigabit Network Connection - 00:25:90:5E:90:28</li> <li>Intel(R) I210 Gigabit Network Connection - 00:25:90:5E:90:29</li> </ul> | Boot Feature Configuration<br>Page  |
|--|---|
|  | <pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre> |

**Warning:** Take caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency, or an incorrect BIOS timing setting may cause the system to malfunction. When this occurs, restore the setting to the manufacture default setting.

# ►Boot Feature

# Quiet Boot

Use this feature to select the screen to display between POST messages or the OEM logo at bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are Disabled and **Enabled**.

# AddOn ROM Display Mode

This feature sets the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and Keep Current.

# Bootup NumLock State

This feature selects the Power-on state for the Numlock key. The options are Off and **On**.

# Wait For "F1" If Error

This feature forces the system to wait until the F1 key is pressed if an error occurs. The options are **Disabled** and Enabled.

# INT19 Trap Response

Interrupt 19 is the software interrupt that handles the boot disk function. When this feature is set to Immediate, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this feature is set to Postponed, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately and allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and Postponed.

# Re-try Boot

If this feature is enabled, the BIOS will automatically reboot the system from a specified boot device after its initial boot failure. The options are **Disabled** and EFI Boot.

# **Power Configuration**

# **DeepSx Power Policies**

Use this feature to configure the Advanced Configuration and Power Interface (ACPI) settings for the system. Enable S5 to power off the whole system except the power supply unit (PSU) and keep the power button alive so that the user can wake up the system by using a USB keyboard or mouse. The options are **Disabled** and Enabled.

# Watch Dog Function

If this feature is enabled, the Watch Dog timer will allow the system to reboot when it is inactive for more than five minutes. The options are **Disabled** and Enabled.

# **Power Button Function**

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are **Instant Off** and 4 Seconds Override.

# **Restore on AC Power Loss**

Use this feature to set the power state after a power outage. Select Power Off for the system power to remain off after a power loss. Select Power On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Stay Off, Power on, and **Last State**.

# \*This feature is not available when DeepSX Power Policies is Enabled.

# ► CPU Configuration

The following CPU information will display:

- CPU model
- CPU Signature
- Microcode Patch
- Max CPU Speed
- Min CPU Speed
- Processor Cores
- Intel HT Technology
- Intel VT-x Technology
- L1 Data Cache
- L1 Code Cache
- L2 Cache
- L3 Cache
- Speed
- 64-bit

# **CPU** Power Management

# EIST

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are Disabled and **Enabled**.

# **Turbo Mode**

Select Enabled for processor cores to run faster than the frequency specified by the manufacturer. The options are Disabled and **Enabled**.

# \*This feature is available when EIST is Enabled.

# Boot performance mode

This feature allows the user to select the performance state that the BIOS will set before the operating system handoff. The options are **Max Performance** and Max Power Saving.

# Power Limit 1 Enable

Use this feature to set the power limit for the CPU. The options are Disabled and **Enabled**.

\*This feature is available when EIST is set to Enabled.

# Power Limit 1

# Power Limit 1 Clamp Mode

Use this feature to set the PL1 clamp bit. The options are Disabled and **Enabled**.

# Power Limit 1 Power

Use this feature to configure the value for Power Limit 1. The value is in milli watts and the step size is 125mW. Use the number keys on your keyboard to enter the value. The options are **Auto**, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, and 25.

# Power Limit 1 Time Window

Use this feature to indicate the time window over which the TDP value should be maintained. The default value is Auto. The options are **Auto**, 1, 2, 3, 4, 5, 6, 7, 8, 10, 12, 14, 16, 20, 24, 28, 32, 40, 48, 56, 64, 80, 96, 112, and 128.

# Active Processor Cores

This feature determines how many CPU cores will be activated for each CPU. When Enabled is selected, all cores in the CPU will be activated. Please refer to Intel's website for more information. The options are **Disabled** and Enabled.

# \*If Active Processor Cores is set to Enabled, Core 0 - Core 3 will be available for configuration:

# Core 0

This feature is set to Enabled.

# Core 1 - Core 3

Use this feature to enable or disable the CPU core. The options are Disabled and **Enabled**.

# Intel<sup>®</sup> Virtualization Technology

Select Enable to use Intel Virtualization Technology to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple virtual systems in one physical computer. The options are Disabled and **Enabled**.

# VT-d

Select Enabled to enable Intel Virtualization Technology support for Direct I/O (VT-d) by reporting the I/O device assignments to VMM through the DMAR ACPI Tables. This feature offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security, and availability in networking and data-sharing. The options are **Disabled** and Enabled.

# Monitor Mwait

Select Enabled to enable the Monitor/Mwait instructions. The Monitor instructions monitors a region of memory for writes, and MWait instructions instruct the CPU to stop until the monitored region begins to write. The options are **Disabled** and Enabled.

# **P-STATE** Coordination

This feature allows the user to change the P-State (Power-Performance State) coordination type. P-State is also known as "SpeedStep" for Intel processors. Select HW\_ALL to change the P-State coordination type for hardware components only. Select SW\_ALL to change the P-State coordination type for all software installed in the system. Select SW\_ANY to change the P-State coordination type for a software program in the system. The options are **HW\_AII**, SW\_ALL, and SW\_ANY.

# ► Chipset Configuration

**Warning:** Setting the wrong values in the following sections may cause the system to malfunction.

# ►North Bridge

The following memory information will be displayed:

• Memory Slot 0

# ► Graphics Configuration

# **GOP Configuration**

# GOP Driver

The Graphics Output Protocol (GOP) driver is a replacement for legacy video BIOS that accesses UEFI protocols. The options are **Enable** and Disable.

# **LVDS Panel Support**

Use this feature to select the supported IGFX graphics device ouput to the LVDS panel. The options are Disabled and **Enable**.

# \*If LVDS Panel Support is set to Enable, the next five features will be available for configuration:

# Panel select

Use this feature to select the panel resolution. The options are **Use EDID in SPI ROM**, 800x600, 1024x768, 1280x1024, 1366x768, 1680x1050, 1920x1080, and OEM Define.

# Panel Channel Type

Use this feature to select the Panel Channel Type. The options are **Disabled**, Odd Channel, Even Channel, and Both Channel.

# Dual LVDS mode

Use this feature to select a single or dual mode bus for the LVDS display. The options are **Disabled**, Single LVDS Bus Mode, and Dual LVDS Bus Mode,

# Panel Color Depth

Use this feature to select the panel color depth. The options are **Disabled**, VESA and JEIDA18 bpp, VESA 24 bbp, and JEIDA 24 bpp.

# Backlight Brightness

Use this feature to select the backlight brightness for the panel display. Select a range from 1 to 16.

# IGD Configuration

# Integrated Graphics Device

When enabled, the onboard graphics device will be used as the primary video display. The options are Disable and **Enable**.

# **Primary Display**

Use this feature to select the primary video display. The options are **IGD** and PCIe.

# RC6 (Render Standby)

Select Enabled to enable render standby support. This is a power saving feature for the onboard display that reduces power consumption. The options are Disable and **Enable**.

# GTT Size

Use this feature to set the memory size to be used by the graphics translation table (GTT). The options are 2MB, 4MB, and **8MB**.

# Aperture Size

Use this feature to set the Aperture size, which is the size of system memory reserved by the BIOS for graphics device use. The options are 128MB, **256MB**, and 512 MB.

# **DVMT Pre-Allocated**

Dynamic Video Memory Technology (DVMT) allows dynamic allocation of system memory to be used for video devices to ensure best use of available system memory based on the DVMT 5.0 platform. The options are **64M**, 96M, 128M, 160M, 192M, 224M, 256M, 288M, 320M, 352M, 384M, 416M, 448M, 480M, and 512M.

# **DVMT Total Gfx Mem**

Use this feature to set the total memory size to be used by internal graphics devices based on the DVMT 5.0 platform. The options are 128MB, **256MB**, and MAX.

# GT PM Support

Use this feature to enable the IGFX Power Management function. The options are **Enable** and Disable.

# **PAVP Enable**

Protected Audio Video Path (PAVP) decodes Intel integrated graphics encrypted video. The options are **Enable** and Disable.

# Max TOLUD

This feature sets the maximum TOLUD value, which specifies the "Top of Low Usable DRAM" memory space to be used by internal graphics devices, GTT Stolen Memory, and TSEG, respectively, if these devices are enabled. The options are **2 GB**, 2.25 GB, 2.5 GB, 2.75 GB, and 3 GB.

# ► South Bridge

# ► HD Audio Configuration

# **HD-Audio Configuration**

# HD-Audio Support

Use this feature to enable high-definition audio support. The options are Disable and **Enable**.

# ► PCI Express Configuration

# Advanced Erorr Reporting

Select Enabled to enable the ACPI OS to manage PCI Advanced Error Reporting. The options are Enabled and **Disabled**.

# ►ASMedia USB Controller

# ASPM

Use this feature to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are Disable and **L1**.

# PCIe Speed

Uses this feature to select the PCI speed for the device installed in the slot. The options are **Auto**, Gen1, and Gen2.

# M.2 B KEY PCIE GEN2 X1

# ASPM

Use this feature to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are Disable, L0s, L1, L0sL1, and **Auto**.

# PCIe Speed

Use this feature to select the PCI speed for the device installed in the M.2 slot. The options are **Auto**, Gen1, and Gen2.

# ►Intel I210 Gigabit LAN

# ASPM

Use this feature to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are Disable, L0s, L1, L0sL1, and **Auto**.

# PCIe Speed

Use this feature to select the PCI speed for the device installed in the slot. The options are **Auto**, Gen1, and Gen2.

# ►Intel I210 Gigabit LAN

# ASPM

Use this feature to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are Disable, L0s, L1, L0sL1, and **Auto**.

# PCIe Speed

Use this feature to select the PCI speed for the device installed in the slot. The options are **Auto**, Gen1, and Gen2.

# Mini PCle

# ASPM

Use this feature to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are **Disable**, L0s, L1, L0sL1, and Auto.

# PCIe Speed

Use this feature to select the PCI speed for the device installed in the slot. The options are **Auto**, Gen1, and Gen2.

# ► USB Configuration

# USB3.0 Support

Select Enable for USB 3.0 support. The options are **Enable** and Disable.

# **XHCI Pre-Boot Driver**

Select Enabled to enable XHCI (Extensible Host Controller Interface) support on a pre-boot drive specified by the user. The options are Enable and **Disable**.

# **XHCI Hand-Off**

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The settings are **Enabled** and Disabled.

# USB Rear I/O USB3.0

Select Enabled for rear I/O USB 3.0 support. The options are **Enabled** and Disabled.

# **USB Mass Storage Driver Support**

Select Enabled for USB mass storage device support. The options are Disabled and **Enabled**.

# SATA Configuration

# **Chipset SATA**

Use this feature to enable or disable the onboard SATA controller supported by the SoC. The options are **Enable** and Disable.

# SATA Mode Selection

Use this feature to select the mode for the installed SATA drives. The options are **AHCI** and RAID.

# Aggressive LPM (Link Power Management) Support

When this feature is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Enabled** and Disabled.

# SATA Frozen

Use this feature to enable the HDD Security Frozen Mode. The options are **Disabled** and Enabled.

# SATA Port 0 - SATA Port 1

#### Port

Use this feature to enable of disable the specified SATA port. The options are Disabled and **Enabled**.

# SATA Port Hot Plug

This feature designates the SATA port specified for hot plugging. Set this feature to Enabled for hot-plugging support, which will allow the user to replace a SATA disk drive without shutting down the system. The options are **Enabled** and Disabled.

# Spin Up Device

When the value of an edge detect or the value of an image binary (pixel) of a device is from 0 to 1, select Enabled to allow the PCH to start a COMRESET initialization sequence on this device. The options are Enabled and **Disabled**.

# SATA Device Type

Use this feature to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

# SATA Port DevSlp

DEVSLP is a signal that is sent to a SATA disk drive to tell it to enter a very low power state. The options are **Disabled** and Enabled.

# Trusted Computing

# **Security Device Support**

If this feature and the TPM on the motherboard are both set to Enabled, onbaord security devices will be enabled for TPM (Trusted Platform Module) support to enhance data integrity and network security. Please reboot the system for a change on this setting to take effect. The options are Disable and **Enable**.

# \*If Security Device Support is set to Enable, the following features will be available for configuration:

The following Platform Configuration Register information will display:

# Active PCR banks

# Available PCR banks

# SHA-1 PCR Bank

Use this feature to disable or enable the SHA-1 Platform Configuration Register (PCR) bank for the installed TPM device. The options are Disabled and **Enabled**.

# SHA256 PCR Bank

Use this feature to disable or enable the SHA256 Platform Configuration Register (PCR) bank for the installed TPM device. The options are Disabled and **Enabled**.

# Pending operation

Use this feature to schedule a TPM-related operation to be performed by a security device for system data integrity. The options are **None** and TPM Clear.

# **Platform Hierarchy**

Use this feature to disable or enable platform hierarchy for platform protection. The options are Disabled and **Enabled**.

# Storage Hierarchy

Use this feature to disable or enable storage hierarchy for cryptographic protection. The options are Disabled and **Enabled**.

# Endorsement Hierarchy

Use this feature to disable or enable endorsement hierarchy for privacy control. The options are Disabled and **Enabled**.

# TPM2.0 UEFI Spec Version

Use this feature to select the Trusted Computing Group (TCG) specification version. Version TCG\_1\_2 is compatible with Windows 8 and 10. Version TCG\_2 is compatible with Windows 10 or later. The options are TCG\_1\_2 and **TCG\_2**.

# **Physical Presence Spec Version**

Use this feature to select the Physical Presence Interface version. This interace uses the ACPI and allows the operating system and BIOS to work together to provide a platform for users to administer the TPM. The options are 1.2 and **1.3**.

# **PH Randomization**

Use this feature to disable or enable Platform Hiearchy (PH) Randomization. The options are **Disabled** and Enabled.

# **Device Select**

Use this feature to select the TPM version. TPM 1.2 will restrict support to TPM 1.2 devices. TPM 2.0 will restrict support for TPM 2.0 devices. Select Auto to enable support for both versions. The options are TPM 1.2, TPM 2.0, and **Auto**.

# ► ACPI Settings

# **ACPI Sleep State**

Use this feature to select which sleep state mode the system will enter when the Suspend button is pressed. The options are Suspend Disabled and **S3 (Suspend to RAM)**.

# **High Precision Timer**

Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback, and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Precision Event Timer is used to replace the 8254 Programmable Interval Timer. The options are Disable and **Enable**.

# **Headless Support**

Enable this feature for the system to function without a keyboard, monitor, or mouse attached. The options are **Disabled** and Enabled.

# Super IO Configuration

# Super IO Chip NCT6106D

# Serial Port 1 Configuration

# **Serial Port**

Select Enabled to enable the onboard serial port specified by the user. The options are Disabled and **Enabled**.

# **Device Settings**

This feature displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

# Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 1. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address to a serial port specified. The options are **Auto**, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

# COM Port Mode

Use this feature to select the COM port mode. The options are **RS232 Mode**, RS422 Mode/ RS485 Full Duplex Mode, and RS485 Half Duplex Mode.

# Baud Rate from different PRE-DIVIDER

Use this feature to select the Baud rate frequency. The options are **PRE-DIV: 13, 1.8461M Hz**, PRE-DIV: 12, 2M Hz, PRE-DIV: 1, 24M Hz, and PRE-DIV: 1.625, 14.769M Hz.

# Serial Port 2 Configuration

# Serial Port

Select Enabled to enable the onboard serial port specified by the user. The options are Disabled and **Enabled**.

# **Device Settings**

This feature displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

# Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 2. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address to a serial port specified. The options are **Auto**, (IO=2F8h; IRQ=3), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

# COM Port Mode

Use this feature to select the COM port mode. The options are **RS232 Mode**, RS422 Mode/ RS485 Full Duplex Mode, and RS485 Half Duplex Mode.

# Baud Rate from different PRE-DIVIDER

Use this feature to select the Baud rate frequency. The options are **PRE-DIV: 13, 1.8461M Hz**, PRE-DIV: 12, 2M Hz, PRE-DIV: 1, 24M Hz, and PRE-DIV: 1.625, 14.769M Hz.

# Serial Port 3 Configuration

# Serial Port

Select Enabled to enable the onboard serial port specified by the user. The options are Disabled and **Enabled**.

# **Device Settings**

This feature displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

#### Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 2. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address to a serial port specified. The options are **Auto**, (IO=3E8h; IRQ=7), (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

#### Baud Rate from different PRE-DIVIDER

Use this feature to select the Baud rate frequency. The options are **PRE-DIV: 13, 1.8461M Hz**, PRE-DIV: 12, 2M Hz, PRE-DIV: 1, 24M Hz, and PRE-DIV: 1.625, 14.769M Hz.

#### Serial Port 4 Configuration

#### Serial Port

Select Enabled to enable the onboard serial port specified by the user. The options are Disabled and **Enabled**.

#### **Device Settings**

This feature displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

#### Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 2. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address to a serial port specified. The options are **Auto**, (IO=2E8h; IRQ=7), (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

#### Baud Rate from different PRE-DIVIDER

Use this feature to select the Baud rate frequency. The options are **PRE-DIV: 13, 1.8461M Hz**, PRE-DIV: 12, 2M Hz, PRE-DIV: 1, 24M Hz, and PRE-DIV: 1.625, 14.769M Hz.

### ►NCT6106D Hardware Monitor

The following PC Health Status information will be displayed:

- Peripheral temperature
- System temperature

#### Fan Speed Control Mode

Use this feature to select the fan speed control mode. The options are **Standard**, Heavy I/O, and Full Speed.

- Fan1 Speed
- VCORE
- VDIMM
- 12V
- 5V
- AVSB
- 3VSB
- 3VCC
- VBAT

### Serial Port Console Redirection

#### **COM1 Console Redirection**

Select Enabled to enable console redirection support for a serial port specified by the user. The options are **Enabled** and Disabled.

\*If COM1 Console Redirection is set to Enabled, the following features will become available for configuration:

#### **COM1** Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

#### COM1 Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

#### COM1 Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

#### **COM1** Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and  $\bf 8$ .

#### **COM1** Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

#### **COM1 Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

#### COM1 Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

#### COM1 VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

#### COM1 Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

#### COM1 Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

#### **COM1 Legacy OS Redirection Resolution**

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

#### COM1 Putty KeyPad

This feature selects the settings for Function Keys and KeyPad used for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SC0, ESCN, and VT400.

#### **COM1 Redirection After BIOS POST**

Use this feature to enable legacy console redirection after BIOS POST. When set to Bootloader, legacy console redirection is disabled before booting the OS. When set to Always Enable, legacy console redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

#### **COM2** Console Redirection

Select Enabled to use the SOL port for Console Redirection. The options are Disabled and **Enabled**.

\*If COM2 Console Redirection is set to Enabled, the following features will become available for configuration:

#### ► COM2 Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user. The options are Enabled and **Disabled**.

#### COM2 Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

#### COM2 Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

#### COM2 Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8**.

#### COM2 Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

#### COM2 Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

#### COM2 Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

#### COM2 VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

#### COM2 Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

#### COM2 Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

#### COM2 Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

#### COM2 Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

#### **COM2 Redirection After BIOS POST**

Use this feature to enable or disable legacy Console Redirection after BIOS POST. When set to Bootloader, legacy Console Redirection is disabled before booting the OS. When set to Always Enable, legacy Console Redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

#### ► COM3 Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user. The options are Enabled and **Disabled**.

#### COM3 Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

#### COM3 Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

#### COM3 Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8**.

#### **COM3** Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

#### **COM3 Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

#### COM3 Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

#### COM3 VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

#### COM3 Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

#### COM3 Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

#### COM3 Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

#### COM3 Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

#### **COM3 Redirection After BIOS POST**

Use this feature to enable legacy Console Redirection after BIOS POST. When set to Bootloader, legacy Console Redirection is disabled before booting the OS. When set to Always Enable, legacy Console Redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

#### ► COM4 Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user. The options are Enabled and **Disabled**.

#### COM4 Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

#### COM4 Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

#### COM4 Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8**.

#### **COM4** Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

#### **COM4 Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

#### COM4 Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

#### COM4 VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

#### **COM4 Recorder Mode**

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

#### COM4 Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

#### COM4 Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

#### COM4 Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

#### **COM4 Redirection After BIOS POST**

Use this feature to enable legacy Console Redirection after BIOS POST. When set to Bootloader, legacy Console Redirection is disabled before booting the OS. When set to Always Enable, legacy Console Redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

# Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

#### EMS (Emergency Management Services) Console Redirection

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are Enabled and **Disabled.** 

#### \*If EMS is set to Enabled, the following features will become available for configuration:

#### **EMS Console Redirection Settings**

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

#### Out-of-Band Mgmt Port

The feature selects a serial port in a client server to be used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1** and COM2.

#### **Terminal Type**

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, VT100+, **VT-UTF8**, and ANSI.

#### Bits per second

This feature sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

#### **Flow Control**

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

Data Bits

Parity

Stop Bits

### ► PCIe/PCI/PnP Configuration

#### PCI Bus Driver Version A5.01.08

#### **PCI Devices Common Settings:**

#### Above 4G Decoding

Select Enabled for 64-bit devices to be decoded above the 4GB address space, if 64bit PCI decoding is supported by the system. The options are **Disabled** and Enabled.

#### NVMe Firmware Source

Use this feature to select the NVMe firmware source. The options are Vendor Defined Firmware and AMI Native Support.

#### M.2 B KEY PCIE GEN2 X1 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, Legacy, and **EFI**.

#### Onboard LAN Option ROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, Legacy, and **EFI**.

# \*If Onboard LAN Option ROM Type is set to Lagacy, Onboard LAN1/LAN2 Option ROM will become available for configuration:

#### Onboard LAN1 Option ROM

Use this feature to select which LAN1 firmware type to be loaded. The options are **Disabled**, PXE, and iSCSI.

#### Onboard LAN2 Option ROM

Use this feature to select which LAN2 firmware type to be loaded. The options are **Disabled**, PXE, and iSCSI.

#### Onboard Video Option ROM

Use this feature to select which onboard video firmware type to be loaded. The options are Disabled, Legacy, and **EFI**.

#### Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are Disabled and **Enabled**.

# \*If Network Stack is Enabled, the following four features will become available for configuration:

#### Ipv4 PXE Support

Select Enabled to enable Ipv4 PXE (Preboot Execution Environment) for boot support. If this feature is set to Disabled, Ipv4 PXE boot option will not be supported. The options are Disabled and **Enabled**.

#### Ipv6 PXE Support

Select Enabled to enable Ipv6 PXE (Preboot Execution Environment) for boot support. If this feature is set to Disabled, Ipv6 PXE boot option will not be supported. The options are **Disabled** and Enabled.

#### PXE boot wait time

Use this option to specify the wait time to press the ESC key to abort the PXE boot. Press "+" or "-" on your keyboard to change the value. The default setting is **0**.

#### Media detect count

Use this option to specify the number of times media will be checked. Press "+" or "-" on your keyboard to change the value. The default setting is **1**.

### ►iSCSi Configuration

#### **iSCSI** Initiator Name

This feature allows the user to enter the unique name of the iSCSI Initiator in IQN format. Once the name of the iSCSI Initiator is entered into the system, configure the proper settings for the following features.

#### Add an Attempt

Delete Attempts

Change Attempt order

# Intel® I210 Gigabit Network Connection - 00:25:90:5E:90:28 Intel® I210 Gigabit Network Connection - 00:25:90:5E:90:29

### ► NIC Configuration

#### Link Speed

This feature allows the user to specify the port speed used for the selected boot protocol. The options are **Auto Negotiated**, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, and 100 Mbps Full.

#### Wake On LAN

Select Enabled for Wake On LAN support, which will allow the system to wake up when an onboard device receives an incoming signal. The options are Disabled and **Enabled**.

#### Blink LEDs

Use this feature to identify the physical network port by blinking the associated LED. Use the keyboard to select a value.

#### **UEFI Driver**

This feature displays the UEFI driver version.

#### Adapter PBA

This feature displays the Processor Bus Adapter (PBA) model number. The PBA number is a nine digit number (i.e., 010B00-000) located near the serial number.

#### **Device Name**

This feature displays the adapter device name.

#### Chip Type

This feature displays the network adapter chipset name.

#### PCI Device ID

This feature displays the device ID number.

#### PCI Address

This feature displays the PCI address for this computer. PCI addresses are three two-digit hexadecimal numbers.

#### Link Status

This feature displays the connection status.

#### MAC Address

This feature displays the MAC address for this computer. Mac addresses are six two-digit hexadecimal numbers.

#### Virtual MAC Address

This feature displays the Virtual MAC address for this computer. Mac addresses are six twodigit hexadecimal numbers.

## 4.4 Security

Use this menu to configure Security settings.

| Setup Administrator Password<br>User Password  | Not Installed<br>Not Installed             | Setup: Check password while<br>invoking setup. Always:<br>Check password while  |
|--|--|---|
| Password Description   | invoking setup as well as<br>on each boot. |   |
| If the Administrator's / User's pas<br>then this only limits access to Set<br>asked for when entering Setup.<br>Please set Administrator's password<br>to set User's password, if clear Ad<br>password, the User's password will |  |   |
| The password length must be<br>in the following range:   |  |   |
| Minimum length<br>Maximum length   | 3<br>20                                    |   |
| Password Check<br>Setup Administrator Password   |  | ++: Select Screen<br>†↓: Select Item<br>Enter: Select   |
| ► Secure Boot  |  | +/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Exit<br>ESC: Exit |

#### **Password Check**

Use this feature to determine when a password entry is required. Select Setup to require the password only when entering setup. Select Always to require the password when entering setup and at each bootup. The options are **Setup** and Always.

#### Setup Administrator Password

Use this feature to set the administrator password, which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

#### Secure Boot

#### Secure Boot Support

Select Enable for secure boot support to ensure system security at bootup. The options are **Disabled** and Enabled.

#### Secure Boot Mode

This feature allows the user to select the desired secure boot mode for the system. The options are Standard and **Custom**.

# \*If Secure Boot Mode is set to Custom, Key Management features will be available for configuration.

#### **CSM Support**

Select Enabled for manufacturing or debugging purposes for the EFI Compatibility Support Module (CSM), which provides compatibility support for traditional legacy BIOS for system boot with limited support. The options are Disabled and Enabled. This feature is for manufacture debugging purposes.

#### Key Management

This submenu allows the user to configure the following Key Management settings.

#### **Provision Factory Default Keys**

Select Enabled to install the default Secure Boot keys set by the manufacturer. The options are **Disabled** and Enabled.

# \*If the feature above is set to Enabled, the next four features are available for configuration:

#### ► Install Factory Default Keys

Select Yes to install all factory keys to the default settings. The options are Yes and No.

#### Enroll Efi Image

Use this feature to allow the image to run in Secure Boot mode. Enroll SHA256 Hash certificate of the Image into Authorized Signature Database.

#### Save All Secure Boot Variables

This feature allows the user to decide if all secure boot variables should be saved.

#### ► Platform Key (PK)

This feature allows the user to configure the settings of the platform keys.

#### Save to File

Select Yes to save the new platform keys (PK) from the manufacturer's defaults. Select No to save the platform keys from a file. The options are **Yes** and No.

#### Set New Var

Select Yes to set the new platform keys (PK) from the manufacturer's defaults. Select No to set the platform keys from a file. The options are **Yes** and No.

#### **Delete Var**

Select Ok to delete platform keys (PK). Removing the keys will reset the system to Setup/ Audit Mode.

#### Key Exchange Keys (KEK)

#### Save to File

Select Yes to save the new key exchange keys (KEK) from the manufacturer's defaults. Select No to save the platform keys from a file. The options are **Yes** and No.

#### Set New Var

Select Yes to set the new key exchange keys (KEK) from the manufacturer's defaults. Select No to set the platform keys from a file. The options are **Yes** and No.

#### Append Key

Select Yes to add the KEK from the manufacturer's defaults list to the existing KEK database. Select No to load the KEK from a file. The options are Yes and No.

#### **Delete Var**

Select Ok to confirm the deletion of KEK from NVRAM.

#### Authorized Signatures

#### Save to File

Select Yes to save the db from the manufacturer's defaults. Select No to save the db from a file. The options are **Yes** and No.

#### Set New Var

Select Yes to load the db from the manufacturer's defaults. Select No to load the db from a file. The options are Yes and No.

#### Append Key

Select Yes to add the db from the manufacturer's defaults list to the existing db database. Select No to load the db from a file. The options are Yes and No.

#### **Delete Var**

Select Ok to confirm the deletion of the db from NVRAM.

#### Forbidden Signatures

#### Save to File

Select Yes to save the dbx from the manufacturer's defaults. Select No to save the dbx from a file. The options are **Yes** and No.

#### Set New Var

Select Yes to load the dbx from the manufacturer's defaults. Select No to load the dbx from a file. The options are Yes and No.

#### Append Key

Select Yes to add the dbx from the manufacturer's defaults list to the existing dbx database. Select No to load the dbx from a file. The options are Yes and No.

#### **Delete Var**

Select Ok to confirm the deletion of the dbx from NVRAM.

#### Authorized TimeStamps

#### Save to File

Select Yes to save the dbt from the manufacturer's defaults. Select No to save the dbx from a file. The options are **Yes** and No.

#### Set New Var

Select Yes to load the dbt from the manufacturer's defaults. Select No to load the dbt from a file. The options are Yes and No.

#### Append Key

Select Yes to add the dbt from the manufacturer's defaults list to the existing dbt database. Select No to load the dbt from a file. The options are Yes and No.

#### **Delete Var**

Select Ok to confirm the deletion of the dbt from NVRAM.

#### ► OsRecovery Signature

#### Set New Var

Select Yes to load the dbr from the manufacturer's defaults. Select No to load the dbr from a file. The options are Yes and No.

#### Append Key

Select Yes to add the dbr from the manufacturer's defaults list to the existing dbr database. Select No to load the dbr from a file. The options are Yes and No.

### 4.5 Boot

Use this menu to configure Boot settings:

|                                 |                      | Select boot mode LEGACY/UEF |
|---------------------------------|----------------------|-----------------------------|
| FIXED BOOT ORDER Priorities     |                      |                             |
| Boot Option #1                  | [UEFI Hard Disk]     |                             |
| Boot Option #2                  | [UEFI AP:UEFI: Bui]  |                             |
| Boot Option #3                  | [UEFI CD/DVD]        |                             |
| Boot Option #4                  | [UEFI USB Hard Disk] |                             |
| Boot Option #5                  | [UEFI USB CD/DVD]    |                             |
| Boot Option #6                  | [UEFI USB Key]       |                             |
| Boot Option #7                  | [UEFI USB Floppy]    |                             |
| Boot Option #8                  | [UEFI USB Lan]       |                             |
| Boot Option #9                  | [UEFI Network:UEFI]  |                             |
| • UEFI Application Boot Priorit | iec                  |                             |
| ▶ UEFI NETWORK Drive BBS Priori |                      |                             |
| ▶ Delete Boot Option            |                      | ++: Select Screen           |
|                                 |                      | ↑↓: Select Item             |
|                                 |                      | Enter: Select               |
|                                 |                      | +/-: Change Opt.            |
|                                 |                      | F1: General Help            |
|                                 |                      | F2: Previous Values         |
|                                 |                      | F3: Optimized Defaults      |
|                                 |                      | F4: Save & Exit             |
|                                 |                      | ESC: Exit                   |

#### Boot mode select

Use this feature to select the boot mode. The options are LEGACY, **UEFI**, and DUAL.

#### **Fixed Boot Order Priorities**

This option prioritizes the order of bootable devices that the system can boot from. Press <Enter> on each entry from top to bottom to select devices.

- Boot Option #1
- Boot Option #2
- Boot Option #3
- Boot Option #4
- Boot Option #5
- Boot Option #6
- Boot Option #7
- Boot Option #8
- Boot Option #9

#### ► UEFI Application Boot Priorities

This feature allows the user to specify which UEFI devices are boot devices.

Boot Option #1

#### ► UEFI NETWORK Drive BBS Priorities

This feature allows the user to specify which UEFI network drive devices are boot devices.

- Boot Option #1
- Boot Option #2

#### ► Delete Boot Option

Use this feature to remove a pre-defined boot device from which the system will boot during startup.

The settings are [any pre-defined boot device].

## 4.6 Save & Exit

Use this menu to save settings and exit the BIOS.

| Aptio Setup Utility – Copyright (C) 2019 American Megatrends, Inc.<br>Main Advanced Security Boot <mark>Save &amp; Exit</mark>   |  |  |
|--|--|--|
| Save Options<br>Discard Changes and Exit<br>Save Changes and Reset<br>Save Changes<br>Discard Changes<br>Default Options<br>Restore Defaults<br>Save as User Defaults<br>Restore User Defaults<br>Restore User Defaults<br>Boot Override<br>UEFI: Built-in EFI Shell<br>UEFI: IP4 Intel(R) 1210 Gigabit Network Connection | Exit system setup without<br>saving any changes.   |  |
| UEFI: IP4 Intel(R) I210 Gigabit Network Connection<br>Version 2.18.1263. Copyright (C) 2019 American   | ++: Select Screen<br>14: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Exit<br>ESC: Exit |  |

#### Save Options

#### **Discard Changes and Exit**

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

#### Save Changes and Reset

When you have completed the system configuration changes, select this option to save all changes made and reset the system.

#### Save Changes

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system.

#### **Discard Changes**

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS Utility Program.

#### **Default Options**

#### **Restore Defaults**

To set this feature, select Restore Defaults from the Exit menu and press <Enter>. These are factory settings designed for maximum system performance but not for maximum stability.

#### Save as User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

#### **Restore User Defaults**

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

#### **Boot Override**

This feature allows the user to override the Boot Option Priorities sequence in the Boot menu, and immediately boot the system with another device specified by the user. This is a one-time override.

#### UEFI: Built-in EFI Shell

UEFI: IP4 Intel® I210 Gigabit Network Connection

UEFI: IP4 Intel® I210 Gigabit Network Connection

# Appendix A

# **BIOS Codes**

# A.1 BIOS Error POST (Beep) Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

**Non-fatal errors** are those which, in most cases, allow the system to continue the boot-up process. The error messages normally appear on the screen.

**Fatal errors** are those which will not allow the system to continue the boot-up procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The table below lists some common errors and their corresponding beep codes encountered by users.

| BIOS Beep (POST) Codes |                                 |  |  |
|------------------------|---------------------------------|--|--|
| Beep Code              | Error Message                   | Description                                  |  |
| 1 beep                 | Refresh                         | Circuits have been reset (Ready to power up) |  |
| 5 short, 1 long        | Memory error                    | No memory detected in system                 |  |
| 5 long, 2 short        | Display memory read/write error | Video adapter missing or with faulty memory  |  |
| 1 long continuous      | System OH                       | System overheat condition                    |  |

## A.2 Additional BIOS POST Codes

The AMI BIOS supplies additional checkpoint codes, which are documented online at http:// www.supermicro.com/support/manuals/ ("AMI BIOS POST Codes User's Guide").

When BIOS performs the Power On Self Test, it writes checkpoint codes to I/O port 0080h. If the computer cannot complete the boot process, a diagnostic card can be attached to the computer to read I/O port 0080h.

For information on AMI updates, please refer to http://www.ami.com/products/.

# Appendix B

## Software Installation

## **B.1 Installing Software Programs**

The Supermicro website that contains drivers and utilities for your system is at https://www. supermicro.com/wftp/driver. Some of these must be installed, such as the chipset driver.

After accessing the website, go into the CDR\_Images (in the parent directory of the above link) and locate the ISO file for your motherboard. Download this file to create a DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.) After creating a CD/DVD with the ISO files, insert the disk into the CD/DVD drive on your

system and the display shown in Figure B-1 should appear.

Another option is to go to the Supermicro website at http://www.supermicro.com/products/. Find the product page for your motherboard here, where you may download individual drivers and utilities to your hard drive or a USB flash drive and install from there.

**Note:** To install the Windows OS, please refer to the instructions posted on our website at http://www.supermicro.com/support/manuals/.

| 🔇 SUPERMICRO A2SAN-H Motherboard Drivers & Tools (Win7) |            |               |   |  |
|---|------------|---------------|---|--|
|   | Ì          |               | Intel Chipset INF files                   |  |
| SUPERMICR   |            |               | Microsoft .Net Framework (Optional)       |  |
| Drivers & Tools   | Ì          |               | Intel Graphics Media Accelerator Driver   |  |
| Intel Atom SoC  |            |               | Realtek High Definition Audio Driver      |  |
| A2SAN-H/L/E   |            |               | Intel TXE Firmware                        |  |
|   | Ì          |               | Intel SeriallO Driver                     |  |
|   | Ì          |               | Intel USB 3.0 Drivers                     |  |
|   | Ì          |               | Intel PRO Network Connections Drivers     |  |
|   | è?         |               | Trusted Platform Module Driver (Optional) |  |
|   |            |               |   |  |
| SUPERMICRO Computer Inc.                                | È          |               | SUPERMICRO SuperDoctor 5                  |  |
|   | Ì          | P             | Build driver diskettes and manuals        |  |
|   |            | $\overline{}$ | Browse CD Auto Start Up Next Time         |  |
|   | For more i | nformation    | , please visit SUPERMICRO's web site.     |  |

Figure B-1. Driver/Tool Installation Display Screen

Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to bottom) one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

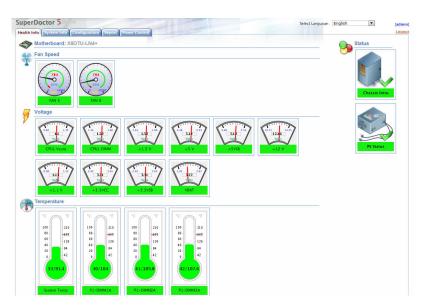
When making a storage driver disk by booting into a driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver disk, be sure to change the SATA settings back to your original settings.

## **B.2 SuperDoctor® 5**

The Supermicro SuperDoctor 5 is a hardware monitoring program that functions in a command-line or web-based interface in Windows and Linux operating systems. The program monitors system health information such as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.

Note: The default Username and Password for SuperDoctor 5 is admin / admin.



#### Figure B-2. SuperDoctor 5 Interface Display Screen (Health Information)

**Note:** The SuperDoctor 5 program and user's manual can be downloaded from the Supermicro website at http://www.supermicro.com/products/nfo/sms\_sd5.cfm.

# Appendix C

# **Standardized Warning Statements**

The following statements are industry standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components.

These warnings may also be found on our website at http://www.supermicro.com/about/ policies/safety\_information.cfm.

### **Battery Handling**



**Warning!** There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推 奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

#### 警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电 池。请按制造商的说明处理废旧电池。

#### 警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按 照製造商的說明指示處理廢棄舊電池。

#### Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

#### Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

#### ¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

אזהרה!

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת. סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة اسحبذال البطارية بطريقة غير صحيحة فعليل اسحبذال البطارية فقط بنفس النىع أو ما يعادلها مما أوصث به الشرمة المصنعة جخلص من البطاريات المسحعملة وفقا لحعليمات الشرمة الصانعة

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

#### Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

### **Product Disposal**



**Warning!** Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר אזהרה! סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القيانين والليائح البطنية

#### 경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.