

GIGABYTE™

R181-Z90

R181-Z91

R181-Z92

AMD EPYC™ 7003 DP Server System - 1U 4-Bay

AMD EPYC™ 7003 DP Server System - 1U 10-Bay

AMD EPYC™ 7003 DP Server System - 1U 10-Bay All NVMe

User Manual

Rev. B00

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Documentation Classifications

In order to assist in the use of this product, GIGABYTE provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use of this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents.

For More Information

For related product specifications, the latest firmware and software, and other information please visit our website at <http://www.gigabyte.com>




For GIGABYTE distributors and resellers, additional sales & marketing materials are available from our reseller portal: <http://reseller.b2b.gigabyte.com>

For further technical assistance, please contact your GIGABYTE representative or visit <https://esupport.gigabyte.com/> to create a new support ticket

For any general sales or marketing enquiries, you may also message GIGABYTE server directly by email: server.grp@gigabyte.com

Conventions

The following conventions are used in this user's guide:

	NOTE! Gives bits and pieces of additional information related to the current topic.
	CAUTION! Gives precautionary measures to avoid possible hardware or software problems.
	WARNING! Alerts you to any damage that might result from doing or not doing specific actions.

Server Warnings and Cautions

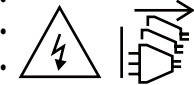
Before installing a server, be sure that you understand the following warnings and cautions.



WARNING!

To reduce the risk of electric shock or damage to the equipment:

- Do not disable the power cord grounding plug. The grounding plug is an important safety feature.
- Plug the power cord into a grounded (earthed) electrical outlet that is easily accessible at all times.
- Unplug the power cord from the power supply to disconnect power to the equipment.



- Shock Hazard! Disconnect all power supply cords before servicing.
- Do not route the power cord where it can be walked on or pinched by items placed against it. Pay particular attention to the plug, electrical outlet, and the point where the cord extends from the server.



WARNING!

To reduce the risk of personal injury from hot surfaces, allow the drives and the internal system components to cool before touching them.



WARNING!

This server is equipped with high speed fans. Keep away from hazardous moving fan blades during servicing.



WARNING!

This equipment is not suitable for use in locations where children are likely to be present.



WARNING!

This equipment is intended to be used in Restrict Access Location. The access can only be gained by Skilled person.

Only authorized by well trained professional person can access the restrict access location.



CAUTION!

- Do not operate the server for long periods with the access panel open or removed. Operating the server in this manner results in improper airflow and improper cooling that can lead to thermal damage.
- Danger of explosion if battery is incorrectly replaced.
- Replace only with the same or equivalent type recommended by the manufacturer.
- Dispose of used batteries according to the manufacturer's instructions.

Electrostatic Discharge (ESD)



CAUTION!

ESD CAN DAMAGE DRIVES, BOARDS, AND OTHER PARTS. WE RECOMMEND THAT YOU PERFORM ALL PROCEDURES AT AN ESD WORKSTATION. IF ONE IS NOT AVAILABLE, PROVIDE SOME ESD PROTECTION BY WEARING AN ANTI-STATIC WRIST STRAP ATTACHED TO CHASSIS GROUND -- ANY UNPAINTED METAL SURFACE -- ON YOUR SERVER WHEN HANDLING PARTS.

Always handle boards carefully. They can be extremely sensitive to ESD. Hold boards only by their edges without any component and pin touching. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

System power on/off: To remove power from system, you must remove the system from rack. Make sure the system is removed from the rack before opening the chassis, adding, or removing any non hot-plug components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the system and disconnect the cables attached to the system before servicing it. Otherwise, personal injury or equipment damage can result.

Electrostatic discharge (ESD) and ESD protection: ESD can damage drives, boards, and other parts. We recommend that you perform all procedures in this chapter only at an ESD workstation. If one is not available, provide some ESD protection by wearing an antistatic wrist strap attached to chassis ground (any unpainted metal surface on the server) when handling parts.

ESD and handling boards: Always handle boards carefully. They can be extremely sensitive to electrostatic discharge (ESD). Hold boards only by their edges. After removing a board from its protective wrapper or from the system, place the board component side up on a grounded, static free surface. Use a conductive foam pad if available but not the board wrapper. Do not slide board over any surface.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that can be gripped with fingertips or with a pair of fine needle nosed pliers. If the jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can damage the contacts inside the jumper, causing intermittent problems with the function controlled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool used to remove a jumper, or the pins on the board may bend or break.

**CAUTION!**

Risk of explosion if battery is replaced incorrectly or with an incorrect type. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

Table of Contents

Chapter 1 Hardware Installation	11
1-1 Installation Precautions	11
1-2 Product Specifications	12
1-3 System Block Diagram	17
Chapter 2 System Appearance	19
2-1 Front View	19
2-2 Rear View	19
2-3 Front Panel LED and Buttons	20
2-4 Rear System LAN LEDs	23
2-5 Hard Disk Drive LEDs	24
2-6 Power Supply Unit (PSU) LED	25
Chapter 3 System Hardware Installation	27
3-1 Removing Chassis Cover	28
3-2 Removing and Installing the Fan Duct	29
3-3 Installing the CPU and Heat Sink	30
3-4 Installing the Memory	32
3-4-1 Eight Channel Memory Configuration	32
3-4-2 Installing a Memory	33
3-4-3 Processor and Memory Module Matrix Table	33
3-4-4 DIMM Population Table	34
3-5 Installing the PCI Expansion Card	36
3-6 Installing the Hard Disk Drive	37
3-6-1 R181-Z90	37
3-6-2 R181-Z91 / R181-Z92	39
3-7 Installing the Mezzanine Card	40
3-8 Replacing the FAN Assembly	41
3-9 Replacing the Power Supply	42
3-10 Cable Routing (R181-Z90)	43
3-11 Cable Routing (R181-Z91/Z92)	46
Chapter 4 Motherboard Components	51
4-1 Motherboard Components	51
4-2 Jumper Setting	53
Chapter 5 BIOS Setup	55

5-1	The Main Menu	56
5-1-1	Trusted Computing	58
5-1-2	Legacy Video Select	59
5-1-3	Serial Port Console Redirection	61
5-2	Advanced Menu	65
5-2-1	PSP Firmware Versions	66
5-2-2	AST2500 Super IO Configuration	67
5-2-3	S5 RTC Wake Settings	68
5-2-4	CPU Configuration	70
5-2-5	PCI Subsystem Settings	71
5-2-6	USB Configuration	73
5-2-7	Network Stack Configuration	75
5-2-8	NVMe Configuration	76
5-2-9	Offboard SATA Controller Configuration	77
5-2-10	SATA Configuration	78
5-2-11	Graphic Output Configuration	79
5-2-12	AMD Mem Configuration Status	80
5-2-13	Tls Auth Configuration	81
5-2-14	iSCSI Configuration	82
5-2-15	Intel(R) I350 Gigabit Network Connection	83
5-2-16	VLAN Configuration	85
5-2-17	MAC IPv4 Network Configuration	87
5-2-18	MAC IPv6 Network Configuration	88
5-3	AMD CBS Menu	89
5-3-1	CPU Common Options	90
5-3-2	DF Common Options	93
5-3-3	UMC Common Options	96
5-3-4	NBIO Common Options	98
5-3-5	FCH Common Options	103
5-3-6	NTB Common Options	106
5-3-7	SOC Miscellaneous Control	107
5-4	AMD PBS Option Menu	108
5-4-1	RAS	109
5-5	Chipset Setup Menu	111
5-6	Server Management Menu	112
5-6-1	System Event Log	114
5-6-2	View FRU Information	115
5-6-3	BMC Network Configuration	116
5-6-4	IPv6 BMC Network Configuration	117
5-7	Security Menu	118

5-7-1	Secure Boot.....	119
5-8	Boot Menu.....	121
5-8-1	UEFI NETWORK Drive BBS Priorities	123
5-8-2	UEFI Application Boot Priorities.....	124
5-9	Save & Exit Menu.....	125
5-10	BIOS POST Beep code (AMI standard).....	126
5-10-1	PEI Beep Codes.....	126
5-10-2	DXE Beep Codes	126

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Chapter 1 Hardware Installation







1-1 Installation Precautions




The motherboard/system contain numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the service guide and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications

	<p>CPU</p> <ul style="list-style-type: none"> ◆ AMD EPYC™ 7003 series processor family ◆ Dual processors, 7nm, Socket SP3 ◆ Up to 64-core, 128 threads per processor ◆ TDP up to 225W, cTDP up to 240W(Conditional support 280W) <p>Compatible with AMD EPYC™ 7002 series processor family</p> <p>NOTE: If only 1 CPU is installed, some PCIe or memory functions might be unavailable</p>
	<p>Chipset</p> <ul style="list-style-type: none"> ◆ System on Chip
	<p>Memory</p> <ul style="list-style-type: none"> ◆ 32 x DIMM slots ◆ DDR4 memory supported only ◆ 8-Channel memory architecture ◆ RDIMM modules up to 128GB supported ◆ LRDIMM modules up to 128GB supported ◆ 3DS RDIMM/LRDIMM modules up to 256GB supported ◆ Memory speed: Up to 3200*/ 2933 MHz <p>Note: * Follow BIOS setting and memory QVL list if running 3200 Mhz with 2DPC</p>
	<p>LAN</p> <ul style="list-style-type: none"> ◆ 2 x 1GbE LAN ports (1 x Intel® I350-AM2) ◆ 1 x 10/100/1000 management LAN
	<p>Expansion Slot</p> <ul style="list-style-type: none"> ◆ Riser Card CRS1021: <ul style="list-style-type: none"> ◆ - 2 x PCIe x8 slots (Gen3 x8), Low profile half-length ◆ Riser Card CRS1015: <ul style="list-style-type: none"> ◆ - 1 x PCIe x16 slot (Gen3 x16), Low profile half-length ◆ 2 x OCP mezzanine slots <ul style="list-style-type: none"> ◆ - PCIe Gen3 x16 ◆ - Type1, P1, P2, P3, P4, K2, K3 ◆ 1 x M.2 slot: <ul style="list-style-type: none"> ◆ - M-key ◆ - PCIe Gen3 x4 ◆ - Supports NGFF-2242/2260/2280 cards ◆ - CPU TDP is limited to 180W if using M.2 device
	<p>Video</p> <ul style="list-style-type: none"> ◆ Integrated in Aspeed® AST2500 ◆ 2D Video Graphic Adapter with PCIe bus interface ◆ 1920x1200@60Hz 32bpp

	Storage (R181-Z90)	<ul style="list-style-type: none"> ◆ 4 x 3.5" hot-swappable HDD bays ◆ 2.5" HDD/SSD supported ◆ SAS card is required for SAS devices support
	(R181-Z91)	<ul style="list-style-type: none"> ◆ 2 x 2.5" U.2, 8 x 2.5" SATA/SAS hot-swappable HDD/SSD bays ◆ SAS card is required for SAS devices support
	(R181-Z92)	<ul style="list-style-type: none"> ◆ 10 x 2.5" NVMe hot-swappable SSD bays
	SATA	<ul style="list-style-type: none"> ◆ 1 x 7-pin SATA III 6Gb/s with SATA DOM support
	SAS	<ul style="list-style-type: none"> ◆ Supported via add-on SAS Card
	Internal Connectors	<ul style="list-style-type: none"> ◆ 6 x SlimSAS connectors ◆ 1 x M.2 slot ◆ 2 x CPU fan headers ◆ 1 x USB 3.0 header ◆ 1 x TPM header ◆ 5 x PCIe expansion slots ◆ 2 x OCP mezzanine slots ◆ 2 x Power supply connectors ◆ 1 x Front panel header ◆ 1 x Back plane board header ◆ 1 x IPMB connector ◆ 1 x Clear CMOS jumper ◆ 1 x BIOS recovery jumper
	Front Panel LED/Buttons	<ul style="list-style-type: none"> ◆ 2 x USB 3.0 ◆ 1 x Power button with LED ◆ 1 x ID button with LED ◆ 1 x NMI button ◆ 1 x Reset button ◆ 2 x LAN activity LEDs ◆ 1 x HDD activity LED ◆ 1 x System status LED
	Rear Panel I/O	<ul style="list-style-type: none"> ◆ 2 x USB 3.0 ◆ 1 x VGA ◆ 1 x COM (RJ45 type) ◆ 2 x RJ45 ◆ 1 x MLAN ◆ 1 x ID button with LED

	Backplane I/O (R181-Z90)	<ul style="list-style-type: none"> ◆ Backplane P/N: 9CBP1042NR-00 ◆ Speed and bandwidth: ◆ SAS 12Gb/s, SATA 6Gb/s
	(R181-Z91)	<ul style="list-style-type: none"> ◆ Backplane P/N: 9CBP10A4NR-00 ◆ Speed and bandwidth: ◆ 2 x ports for PCIe x4, 8 x ports for SAS 12Gb/s, SATA 6Gb/s
	(R181-Z92)	<ul style="list-style-type: none"> ◆ Backplane P/N: 9CBP10A3NR-00 ◆ Speed and bandwidth: ◆ 10 x ports for PCIe x4
	TPM	<ul style="list-style-type: none"> ◆ 1 x TPM header with LPC interface ◆ Optional TPM2.0 kit: CTM000
	System Management	<ul style="list-style-type: none"> ◆ Aspeed® AST2500 management controller ◆ GIGABYTE Management Console (AMI MegaRAC SP-X) web interface ◆ ◆ Dashboard ◆ JAVA Based Serial Over LAN ◆ HTML5 KVM ◆ Sensor Monitor (Voltage, RPM, Temperature, CPU Status ...etc.) ◆ Sensor Reading History Data ◆ FRU Information ◆ SEL Log in Linear Storage / Circular Storage Policy ◆ Hardware Inventory ◆ Fan Profile ◆ System Firewall ◆ Power Consumption ◆ Power Control ◆ LDAP / AD / RADIUS Support ◆ Backup & Restore Configuration ◆ Remote BIOS/BMC/CPLD Update ◆ Event Log Filter ◆ User Management ◆ Media Redirection Settings ◆ PAM Order Settings ◆ SSL Settings ◆ SMTP Settings



Power Supply
(R181-Z90)

- ◆ 2 x 1200W redundant PSUs
- ◆ 80 PLUS Platinum
- ◆ AC input:
 - 100-240V/ 12-7A, 50-60Hz
 - 200-240V/ 7A, 50-60Hz
- ◆ DC output:
 - 1200W
 - 12V, 80.5A (100-240V)
 - 12V, 97A (200-240V)
 - 12Vsb, 3A

- ◆ **Optional** power supply: DPS-850AB-3A
 - 2 x 850W redundant PSUs
 - 80 PLUS Titanium
 - ◆ AC input:
 - 100-127V/ 11A, 50-60Hz
 - 200-240V/ 5A, 50-60Hz
 - ◆ DC output:
 - 12V, 70.8A(100~240V)
 - 12Vsb, 2.1A

(R181-Z91)

- ◆ 2 x 1200W redundant PSUs
- ◆ 80 PLUS Platinum
- ◆ AC input:
 - 100-240V/ 12-7A, 50-60Hz
 - 200-240V/ 7A, 50-60Hz
- ◆ DC output:
 - 1200W
 - 12V, 80.5A (100-240V)
 - 12V, 97A (200-240V)
 - 12Vsb, 3A

(R181-Z92)

- ◆ 2 x 1200W redundant PSUs
- ◆ 80 PLUS Platinum
- ◆ AC input:
 - 100-240V/ 12-7A, 50-60Hz
 - 200-240V/ 7A, 50-60Hz
- ◆ DC output:
 - 1200W
 - 12V, 80.5A (100-240V)
 - 12V, 97A (200-240V)
 - 12Vsb, 3A
- ◆ NOTE:
 - * Select 1600W power supply if using 280W CPU with full loading configuration



Operating
Properties

- ◆ Operating temperature: 10°C to 35°C
- ◆ Non-operating temperature: -40°C to 60°C
- ◆ Operating humidity: 8-80% (non-condensing)
- ◆ Non-operating humidity: 20%-95% (non-condensing)
- ◆ Ambient temperature limited to 30°C if using 280W CPU



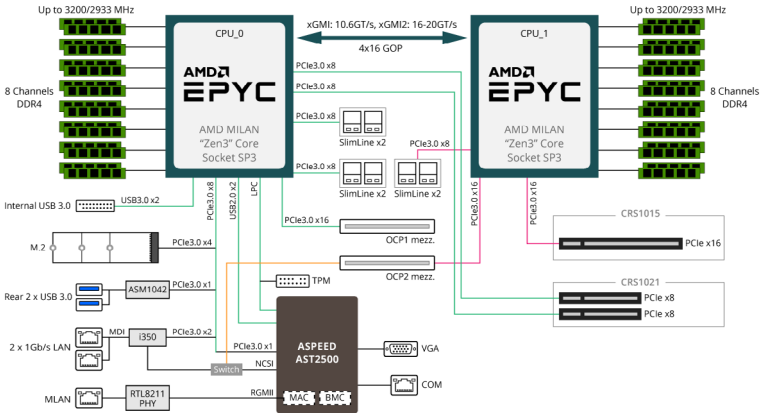
System
Dimension

- ◆ 1U
- ◆ 438mm (W) x 43.5mm (H) x 730mm (D)

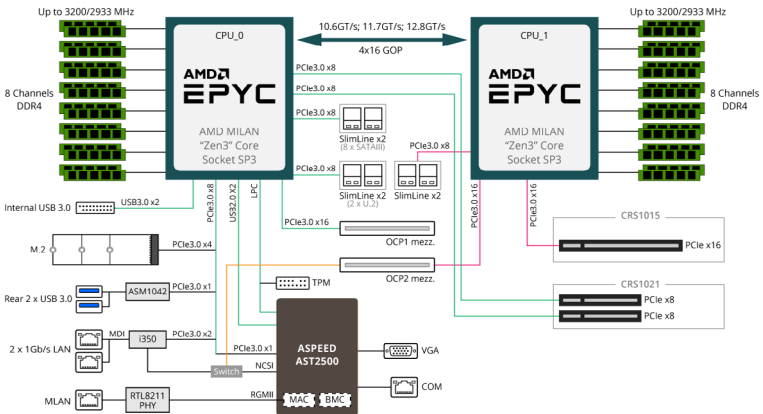
* We reserves the right to make any changes to the product specifications and product-related information without prior notice.

1-3 System Block Diagram

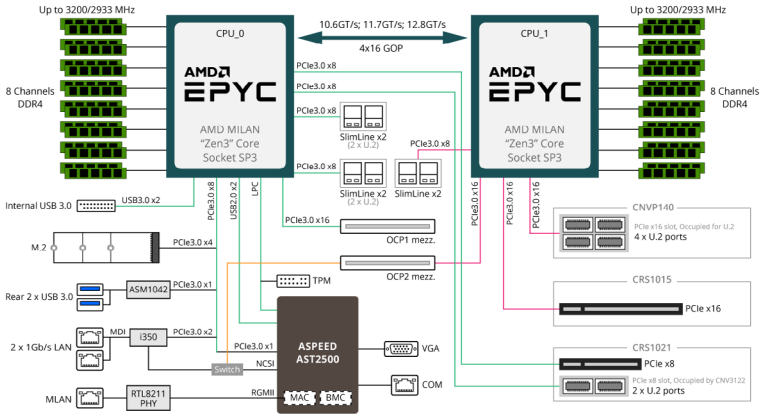
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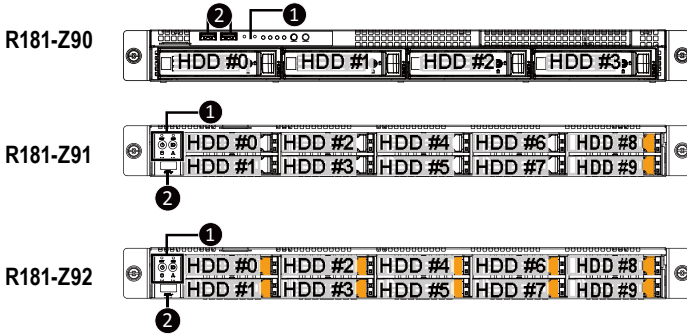


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Chapter 2 System Appearance

2-1 Front View



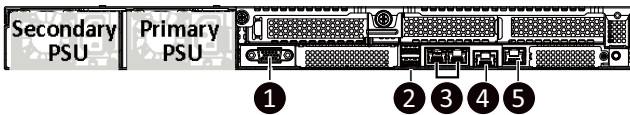
No.	Description
1.	Front Panel LEDs and buttons
2.	Front USB 3.0 ports

NOTE! The Orange Latche Supports NVMe



• Please Go to Chapter 2-3 **Front Panel LED** and Buttons for detail description of function LEDs.

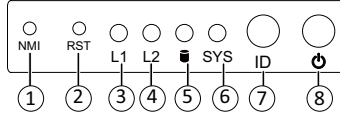
2-2 Rear View



No.	Description
1.	VGA port
2.	USB 3.0 ports
3.	GbE LAN ports
4.	Serial Port
5.	10/100/1000 Server management LAN port

2-3 Front Panel LED and Buttons

R181-Z90

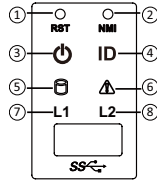


No.	Name	Color	Status	Description
1.	NMI button			Press the button server generates a NMI to the processor if the multiple-bit ECC errors occur, which effectively halt the server.
2.	Reset Button			Press the button to reset the system.
3/4	LAN 1/2 Active/Link LEDs	Green	Solid On	Link between system and network or no access.
		Green	Blink	Data transmission or receiving is occurring
		N/A	Off	No data transmission or receiving is occurring
5.	HDD Status LED	Green	On	HDD locate
			Blink	HDD access
		Amber	On	HDD fault
		Green/Amber	Blink	HDD rebuilding
		N/A	Off	No HDD access or no HDD fault.
6.	System Status LED ^(Note)	Green	Solid On	System is operating normally.
			Solid On	Critical condition, may indicate: System fan failure System temperature
		Amber	Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue Chassis intrusion
			N/A	Off
7.	ID Button ^(Note)			Press the button to activate system identification
8.	Power button with LED	Green	On	System is powered on
		Green	Blink	System is in ACPI S1 state (sleep mode)
		N/A	Off	<ul style="list-style-type: none"> System is not powered on or in ACPI S5 state (power off) System is in ACPI S4 state (hibernate mode)

(Note) If your server features RoT function, please see the following section for detail LED behavior.

R181-Z91

R181-Z92

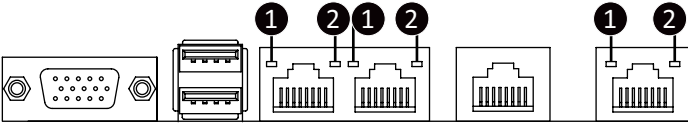


No.	Name	Color	Status	Description
1.	Reset Button			Press the button to reset the system.
2.	NMI button			Press the button server generates a NMI to the processor if the multiple-bit ECC errors occur, which effectively halt the server.
3.	Power button with LED	Green	On	System is powered on
		Green	Blink	System is in ACPI S1 state (sleep mode)
		N/A	Off	<ul style="list-style-type: none"> System is not powered on or in ACPI S5 state (power off) System is in ACPI S4 state (hibernate mode)
4.	ID Button ^(Note)			Press the button to activate system identification
5.	HDD Status LED	Green	On	HDD locate
			Blink	HDD access
		Amber	On	HDD fault
		Green/Amber	Blink	HDD rebuilding
		N/A	Off	No HDD access or no HDD fault.
6.	System Status LED ^(Note)	Green	Solid On	System is operating normally.
			Solid On	Critical condition, may indicate: System fan failure System temperature
		Amber	Blink	Non-critical condition, may indicate: Redundant power module failure Temperature and voltage issue Chassis intrusion
			N/A	Off
7/8.	LAN 1/2 Active/Link LEDs	Green	Solid On	Link between system and network or no access.
		Green	Blink	Data transmission or receiving is occurring
		N/A	Off	No data transmission or receiving is occurring

NOTE!

1. EC FW is broken or not exited result in Microchip CEC1702 cannot load EC FW for authentication.
2. (1) Authentication fail include below scenarios
 - Configuration table is missing or modified
 - Public key is missing or modified
 - Protected area or signature is modified
 - Flash empty
3. If active flash is still authentication failed after recovery sequence, Microchip CEC1702 stop the process and showing LED behavior.
4. If backup flash authentication is failed cause by configuration table, public key or protected area is broken. Microchip CEC1702 stop the process and showing LED behavior.
5. Front panel LED is controlled by BMC or Microchip CEC1702. Once Microchip CEC1702 is working(Auth or recovery), the front panel LED is controlled by Microchip CEC1702 and vice versa.

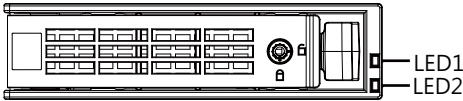
2-4 Rear System LAN LEDs



No.	Name	Color	Status	Description
1.	1GbE Speed LED	Yellow	On	1 Gbps data rate
		Green	On	100 Mbps data rate
		N/A	Off	10 Mbps data rate
2.	1GbE Link/Activity LED	Green	On	Link between system and network or no access
			Blink	Data transmission or receiving is occurring
		N/A	Off	No data transmission or receiving is occurring

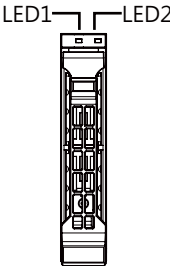
2-5 Hard Disk Drive LEDs

R181-Z90



R181-Z91

R181-Z92



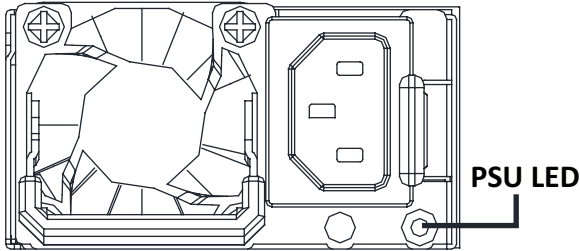
RAID SKU	LED1	Locate	HDD Fault	Rebuilding	HDD Access	HDD Present (No Access)
No RAID configuration (via HBA, ICH)	Disk LED (LED on Back Panel)	Green	ON(*1)	OFF	Green	OFF
		Amber	OFF	OFF	Amber	OFF
	Removed HDD Slot (LED on Back Panel)	Green	ON(*1)	OFF	Green	--
		Amber	OFF	OFF	Amber	--
RAID configuration (via HW RAID Card or SW RAID Card)	Disk LED	Green	ON	OFF	Alternately	OFF
		Amber	OFF	ON	(Low Speed: 2 Hz)	OFF
	Removed HDD Slot	Green	ON(*1)	OFF	(*3)	--
		Amber	OFF	ON	(*3)	--

LED 2	HDD Present	No HDD
Green	ON	OFF

NOTE:

- *1: Depends on HBA/Utility Spec.
- *2: Blink cycle depends on HDD's activity signal.
- *3: If HDD is pulled out during rebuilding, the disk status of this HDD is regarded as faulty.

2-6 Power Supply Unit (PSU) LED



State	Description
OFF	Indicates no AC power to all power supplies
0.5Hz Blink GREEN (R181-Z90)	Indicates AC present/ only standby on/ Cold redundant mode
1Hz Blink GREEN (R181-Z92)	Indicates AC present/ only standby on/ Cold redundant mode
2Hz Blink GREEN	Indicates power supply firmware in updating mode
Amber	Indicates AC cord unplugged or AC power lost; with a second power supply in parallel still with AC input power
	Indicates power supply critical event causing shut down: failure, OCP, OVP, Fan Fail, UVP
0.5Hz Blink Amber	Indicates power supply warning events where the power supply continues to operate: high temp, high power, high current, slow fan

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Chapter 3 System Hardware Installation



Pre-installation Instructions

Computer components and electronic circuit boards can be damaged electrostatic discharge. Working on computers that are still connected to a power supply can be extremely dangerous. Follow the simple guidelines below to avoid damage to your computer or injury to yourself.

- Always disconnect the computer from the power outlet whenever you are working inside the computer case.
- If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal system of the computer case, or the bare metal body of any other grounded appliance.
- Hold electronic circuit boards by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
- Leave all components inside the static-proof packaging until you are ready to use the component for the installation.

3-1 Removing Chassis Cover

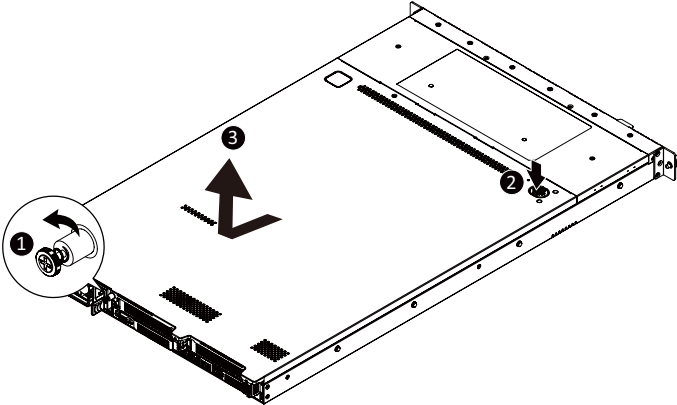


Before you remove or install the system cover

- Make sure the system is not turned on or connected to AC power.

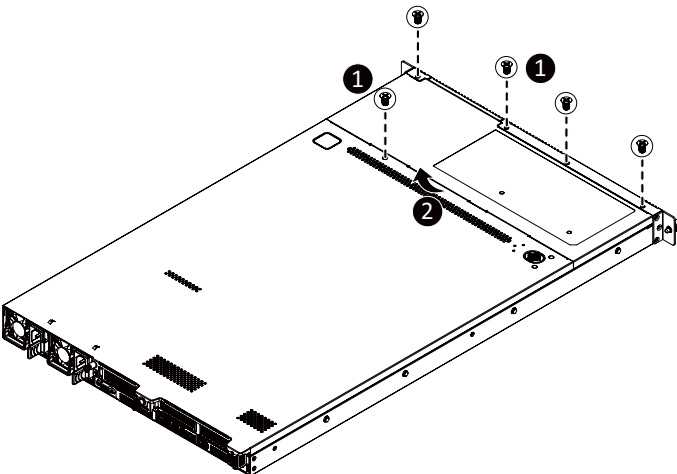
Follow these instructions to remove the rear system cover:

1. Loosen and remove the thumbscrew securing the back cover.
2. Push down the indentation located at the side of the back chassis
3. Slide the cover horizontally to the back and remove the cover in the direction of the arrow.



Follow these instructions to remove the front system cover:

1. Remove the five screws securing the front system cover to the system.
2. Flip open the front system cover.

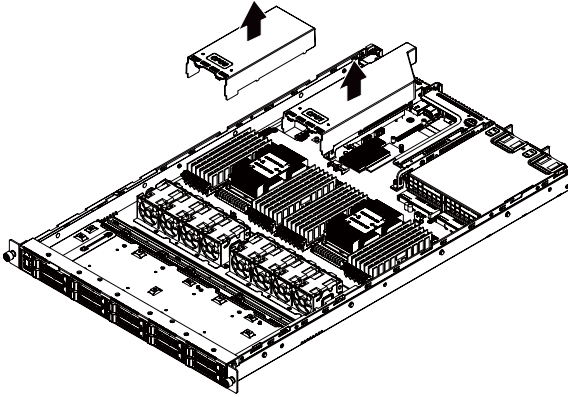


3-2 Removing and Installing the Fan Duct

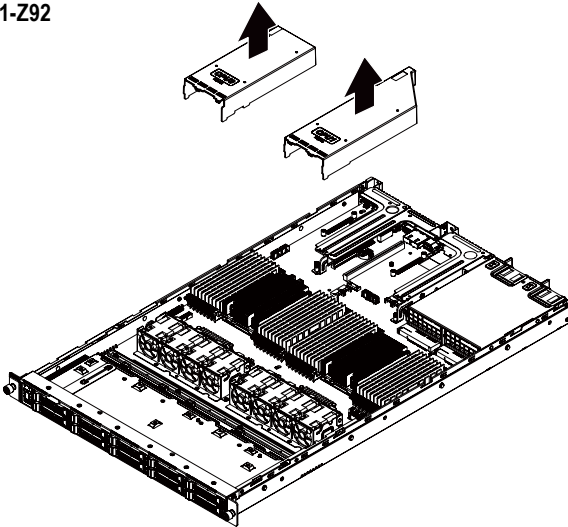
Follow these instructions to remove/install the fan duct:

1. Lift up to remove the two fan ducts
2. To install the fan duct, align the fan duct with the guiding groove. Push down the fan duct into chassis until its firmly seats

R181-Z90



R181-Z91/R181-Z92



3-3 Installing the CPU and Heat Sink



Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.



WARNING!

Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

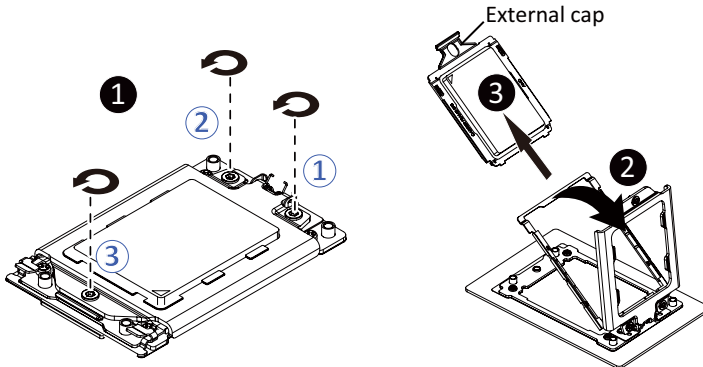
Follow these instructions to install the CPU:

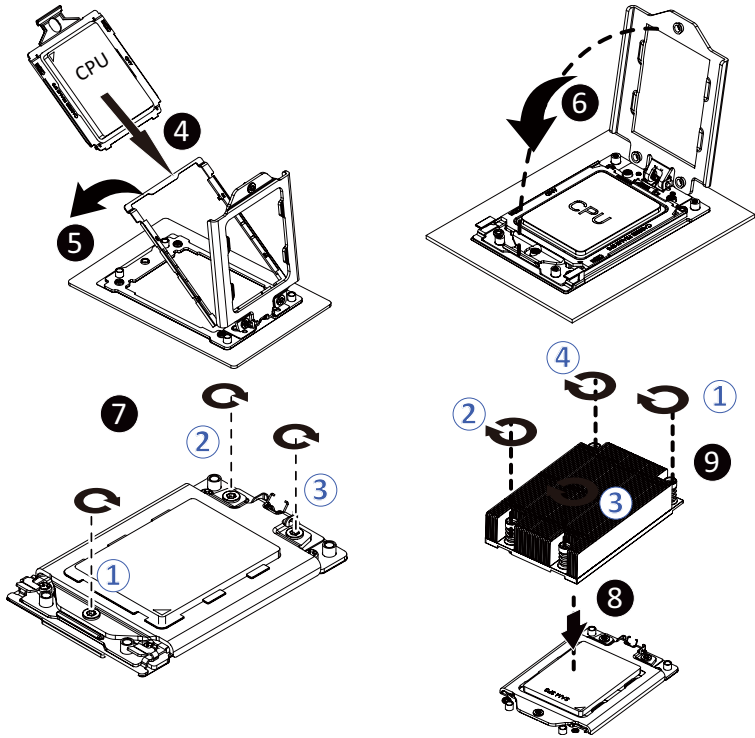
1. Align and install the processor on the carrier, making sure to line up the triangle markers on the corner of the CPU to the triangle mark on the corner of the CPU carrier.
2. Slide the carrier assembly into the channels of the carrier bracket.
3. Close the carrier bracket so that it firmly latches on to the CPU socket.
4. Close the CPU socket cover.
5. Tighten and secure the CPU socket cover screws in the following order (3→2→1).

NOTE: When removing the CPU socket cover, loosen the screws in reverse order (1→2→3).

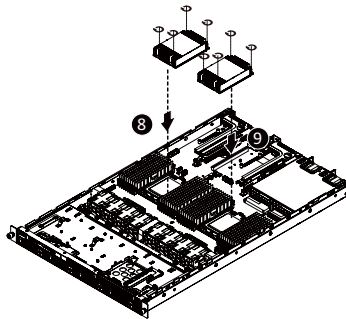
NOTE: Apply thermal compound evenly on the top of the CPU. Remove the protective cover from the underside of the heat sink.

6. Align and place the heatsink onto the top of the CPU socket.
7. To secure the heatsink, tighten the four screws to the CPU socket.
8. Repeat steps 1-7 for the second CPU and heatsink.
9. To remove the heatsinks and CPUs, follow steps 1-7 in reverse order.





- Tighten the CPU cover screws in sequential order (1 → 2 → 3).
- The screw tightening torque: 16.1 ± 1.2 kgf-cm



3-4 Installing the Memory

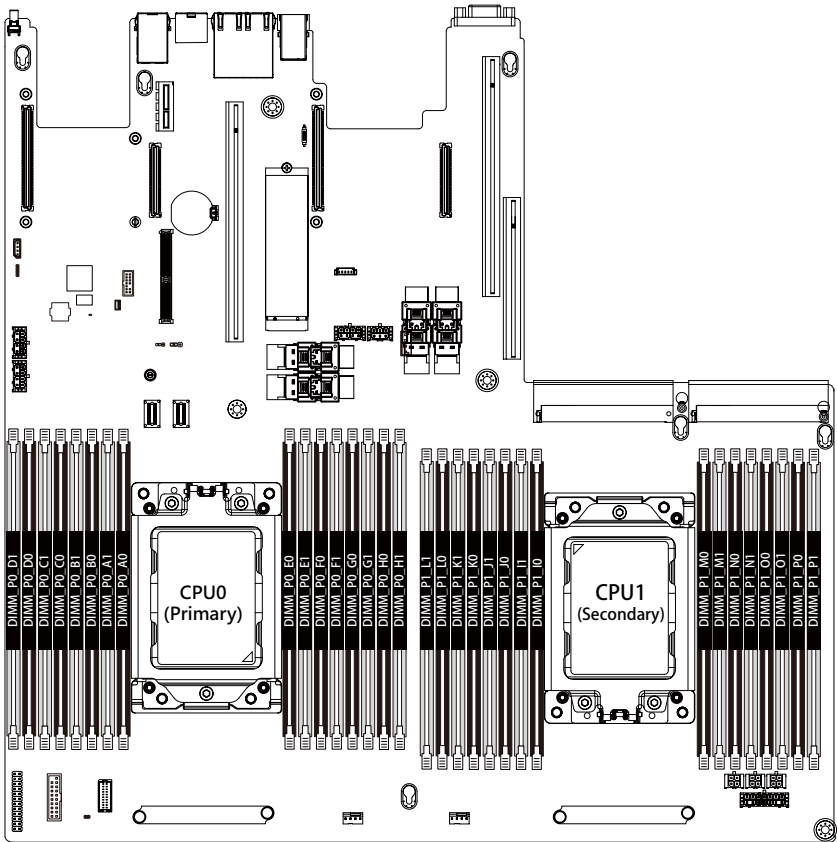


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

3-4-1 Eight Channel Memory Configuration

This motherboard provides 32 DDR4 memory sockets and supports Eight Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory. Enabling Four Channel memory mode will be four times of the original memory bandwidth.



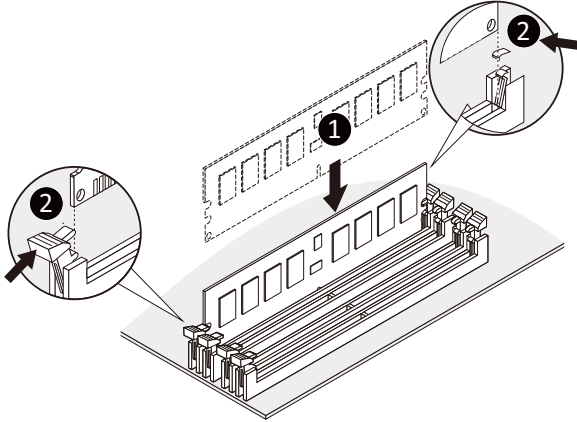
3-4-2 Installing a Memory

! Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.

Be sure to install DDR4 DIMMs on this motherboard.

Follow these instructions to install the Memory:

1. Insert the DIMM memory module vertically into the DIMM slot, and push it down.
2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
3. Reverse the installation steps when you want to remove the DIMM module.



3-4-3 Processor and Memory Module Matrix Table

Processor and Memory Module Matrix Table														
CPU#	Channel A/J		Channel B/I		Channel C/K		Channel D/L		Channel E/M		Channel F/N		Channel G/O	
8 DIMMs														
CPU0		A1		B1		C1		D1		E1		F1		G1
16 DIMMs														
CPU0	A0	A1	B0	B1	C0	C1	D0	D1	E0	E1	F0	F1	G0	G1
16 DIMMs														
CPU0		A1		B1		C1		D1		E1		F1		G1
CPU1		I1		J1		K1		L1		M1		N1		O1
32 DIMMs														
CPU0	A0	A1	B0	B1	C0	C1	D0	D1	E0	E1	F0	F1	G0	G1
CPU1	I0	I1	J0	J1	K0	K1	L0	L1	M0	M1	N0	N1	O0	O1

3-4-4 DIMM Population Table

EPYC Memory Speed based on DIMM Population (One DIMM per Channel)

DIMM Type	DIMM Population		Max EPYC 7003 DDR Frequency (MHz)
	DIMM 0		
RDIMM	1R (1 Rank)		3200
	2R or 2DR (2 Ranks)		3200
LRDIMM	4DR (4 Ranks)		3200
	2S2R (4 Ranks)		3200
	2S4R (8 Ranks)		3200
3DS	2S2R (4 Ranks)		3200
	2S4R (8 Ranks)		3200

EPYC Memory Speed based on DIMM Population (Two DIMM per Channel)

DIMM Type	DIMM Population		Max EPYC 7003 DDR Frequency (MHz)
	DIMM 0	DIMM 1	
RDIMM	--	1R	3200
	1R	1R	2933
	--	2R or 2DR	3200
	1R	2R or 2DR	2933
	2R or 2DR	2R or 2DR	2933
LRDIMM	--	4DR	3200
	4DR	4DR	2933
	--	2S2R (4 Ranks)	3200
	--	2S4R (8 Ranks)	3200
	2S2R (4 Ranks)	2S2R (4 Ranks)	2933
	2S4R (8 Ranks)	2S4R (8 Ranks)	2933
3DS	--	2S2R (4 Ranks)	2933
	2S2R (4 Ranks)	2S2R (4 Ranks)	2666
	--	2S4R (8 Ranks)	2933
	2S4R (8 Ranks)	2S4R (8 Ranks)	2666

NOTE!

- 1R: 1 package rank of SDP DRAMs
- 2R: 2 package rank of SDP DRAMs
- 2DR: 2 package rank of DDP DRAMs
- 4DR: 4 package rank of DDP DRAMs
- 2S2R/2S4R/2S8R: 2 package rank of 2/4/8 high 3DS DRAMs
- DIMM must be populated in sequential alphabetic order, starting with bank A.
- When only one DIMM is used, it must be populated in memory slot A1.

3-5 Installing the PCI Expansion Card



- Voltages can be present within the server whenever an AC power source is connected. This voltage is present even when the main power switch is in the off position. Ensure that the system is powered-down and all power sources have been disconnected from the server prior to installing a PCI card.

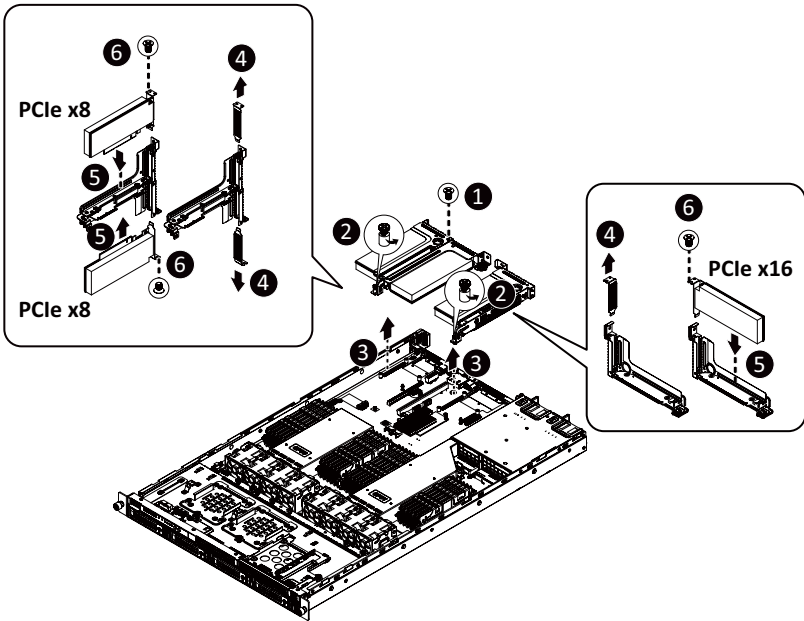
Failure to observe these warnings could result in personal injury or damage to equipment.



- The PCI riser assembly does not include a riser card or any cabling as standard. To install a PCI card, a riser card must be installed.

Follow these instructions to PCI Expansion card:

1. Remove the securing special screw on the riser bracket.
2. Remove the thumbscrew on the riser bracket
3. Lift up the riser bracket out of system.
4. Remove the slot covers from the riser bracket.
5. Orient the PCIe card with the riser guide slot and push in the direction of the arrow until the PCIe card sits in the PCI card connector.
6. Secure the PCIe card with the screw.
7. Reverse the steps 3 - 1 to install the riser bracket.



3-6 Installing the Hard Disk Drive



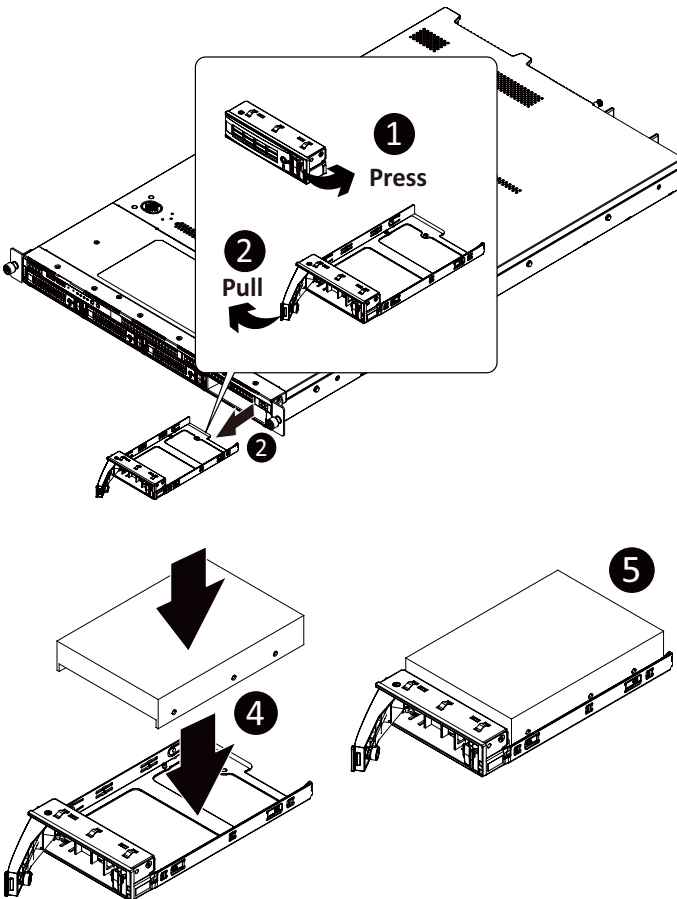
Read the following guidelines before you begin to install the Hard disk drive:

- Take note of the drive tray orientation before sliding it out.
- The tray will not fit back into the bay if inserted incorrectly.
- Make sure that the HDD is connected to the HDD connector on the backplane.

3-6-1 R181-Z90

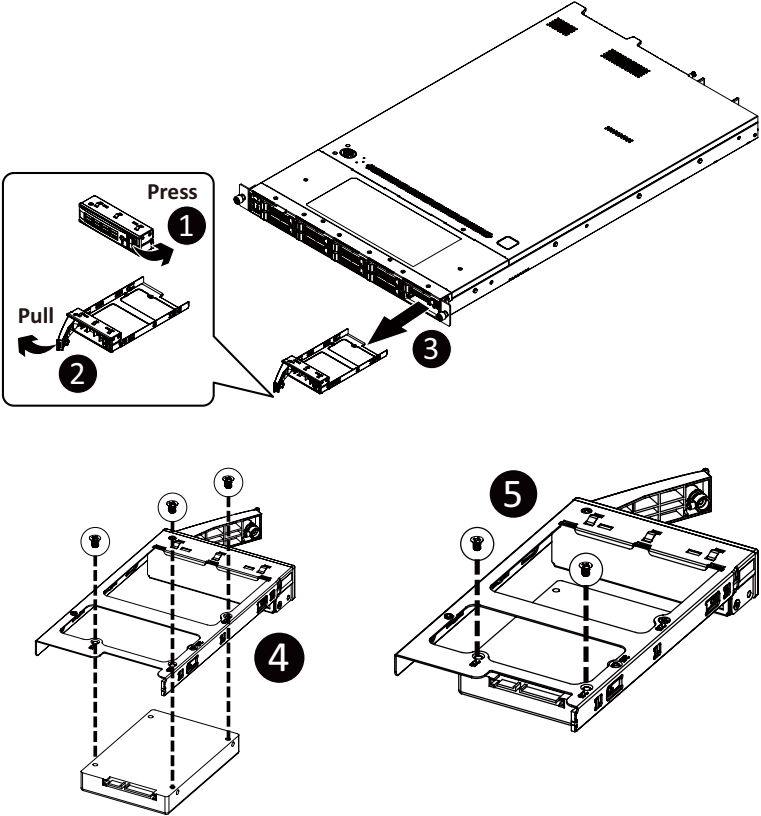
Follow these instructions to install a 3.5" hard disk drive:

1. Press the release button.
2. Extend the locking lever and pull the locking lever to remove the HDD tray.
3. Place the hard disk drive into the HDD tray.
4. Secure the hard disk drive to the HDD tray with four screws.



Follow these instructions to install a 2.5" hard disk drive:

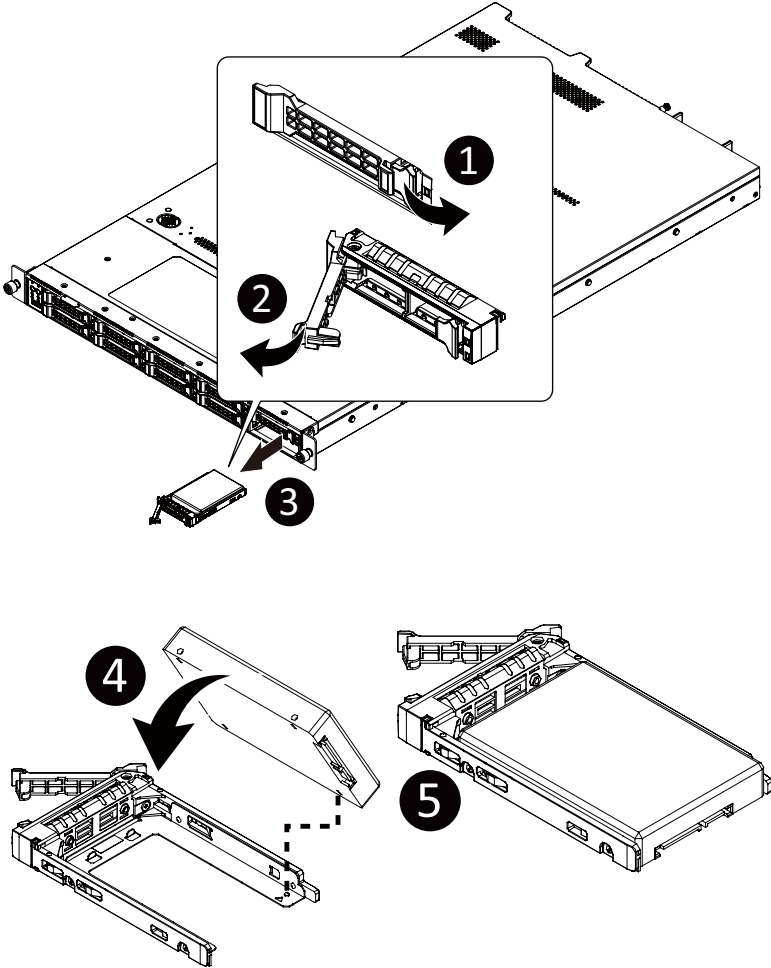
1. Press the release button.
2. Extend the locking lever.
3. Pull the locking lever in the direction indicated to remove the HDD tray.
4. Align the hard disk drive with the positioning screw on the HDD tray.
5. Secure the hard disk drive with five screws.
6. Reinsert the HDD tray into the slot and close the locking lever.



3-6-2 R181-Z91 / R181-Z92

Follow these instructions to install a 2.5" hard disk drive:

1. Press the release button.
2. Extend the locking lever.
3. Pull the locking lever to remove the HDD tray.
4. Align the hard disk drive with the positioning stop on the HDD tray.
5. Slide the hard disk drive into the HDD tray.
6. Reinsert the HDD tray into the slot and close the locking lever.

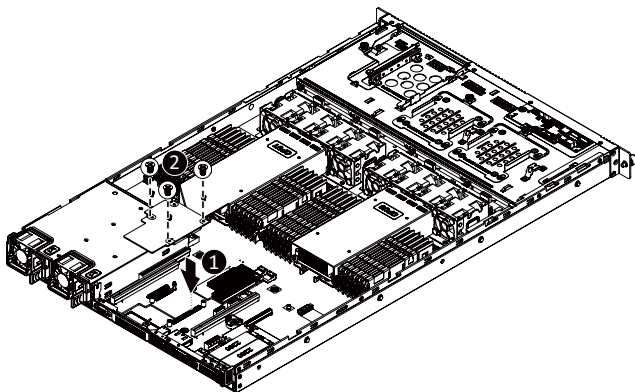


3-7 Installing the Mezzanine Card

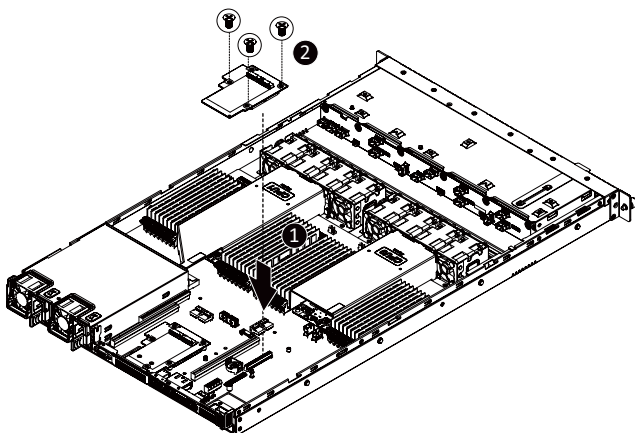
Follow these instructions to install a mezzanine card:

1. Insert the mezzanine card into the system ensuring that the connector on the mezzanine card connects to the connector on the motherboard.
2. Secure the mezzanine card to the system with three screws.

R181-Z90



R181-Z91/R181-Z92

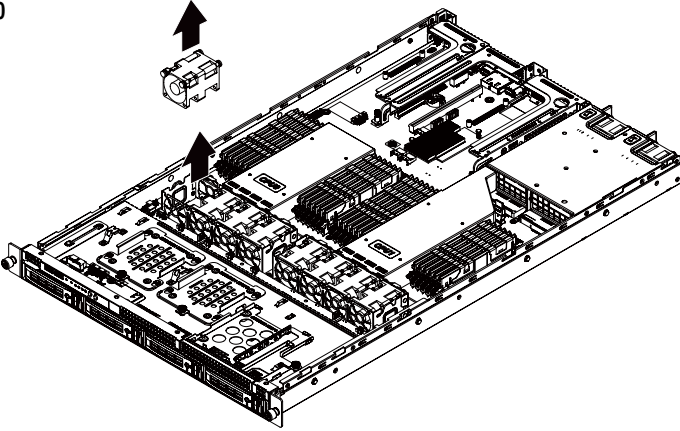


3-8 Replacing the FAN Assembly

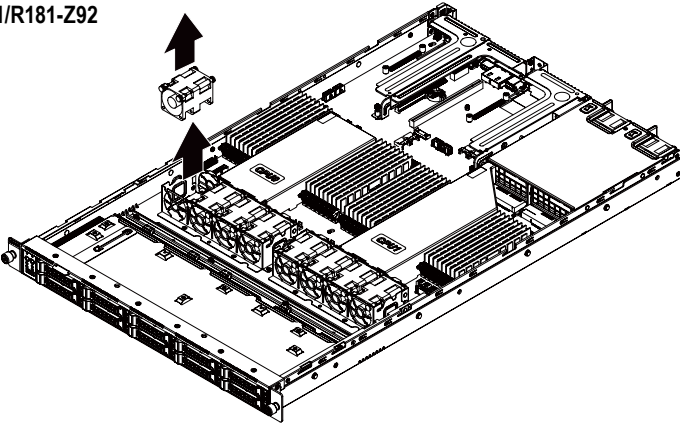
Follow these instructions to replace the fan assembly:

1. Lift up the fan assembly from the chassis.
2. Reverse the previous steps to install the replacement fan assembly.

R181-Z90



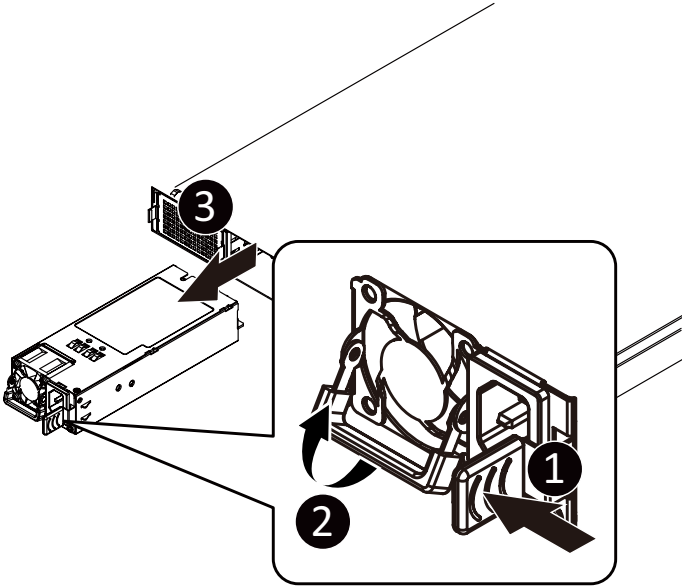
R181-Z91/R181-Z92



3-9 Replacing the Power Supply

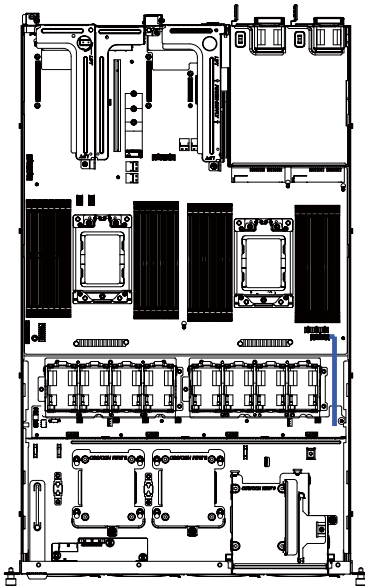
Follow these instructions to replace the power supply:

1. Press the retaining clip on the right side of the power supply along the direction of the arrow.
2. Pull up the power supply handle at the same time and pull out the power supply.
3. Insert the replacement power supply firmly into the chassis. Connect the AC power cord to the replacement power supply.

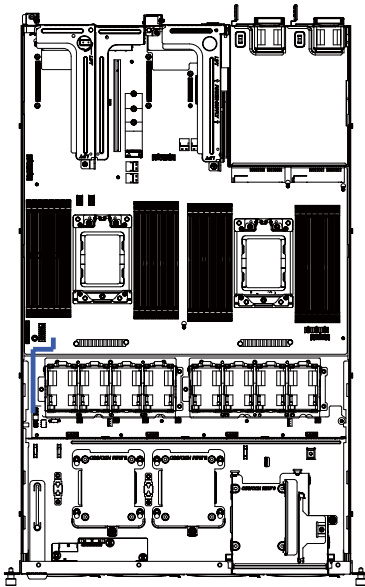


3-10 Cable Routing (R181-Z90)

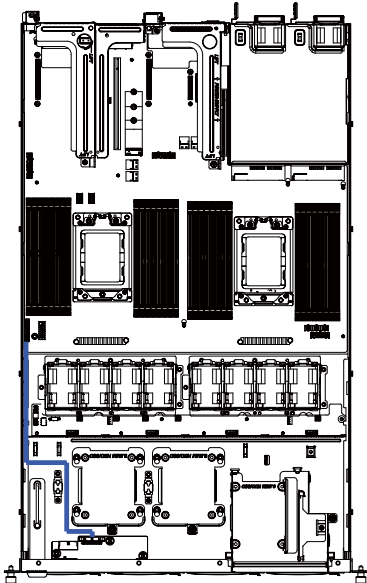
HDD Back Plane Board Power Cable



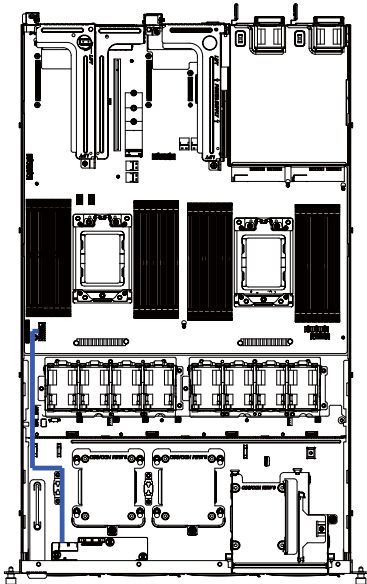
HDD Back Plane Board Signal Cable



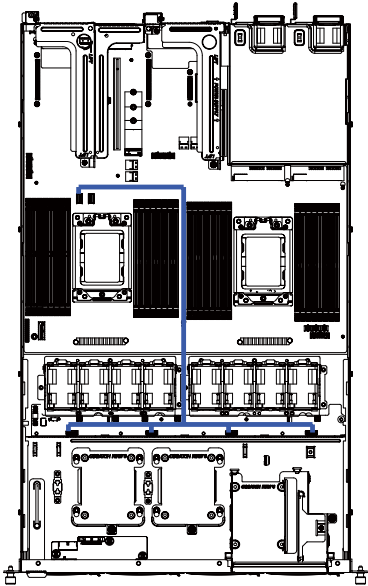
Front IO Board Cable



Front Panel USB 3.0 Cable

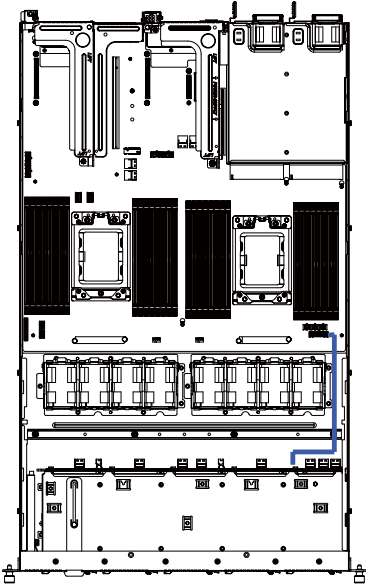


On-Board SATA to HDD Back Plane Board Cable

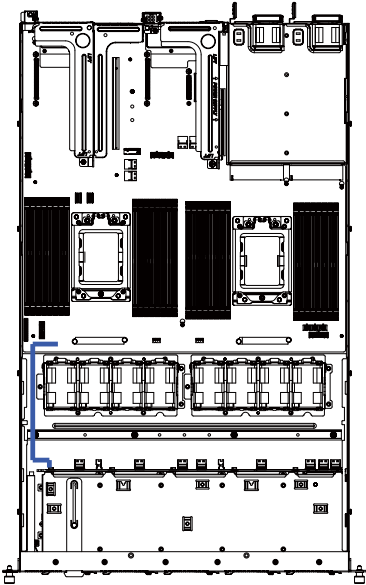


3-11 Cable Routing (R181-Z91/Z92)

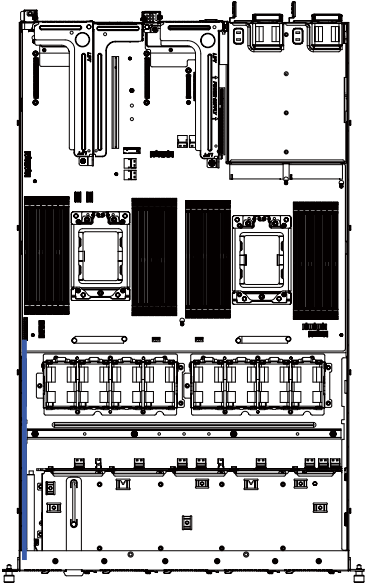
HDD Back Plane Board Power Cable



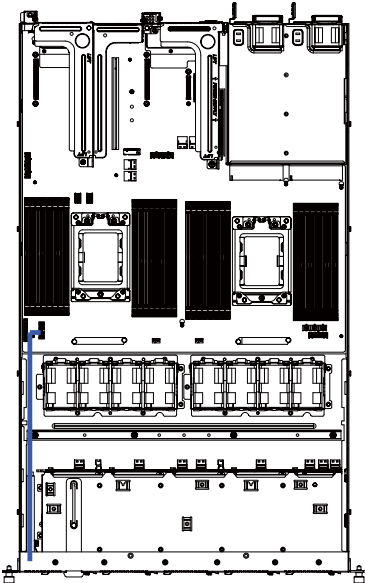
HDD Back Plane Board Signal Cable



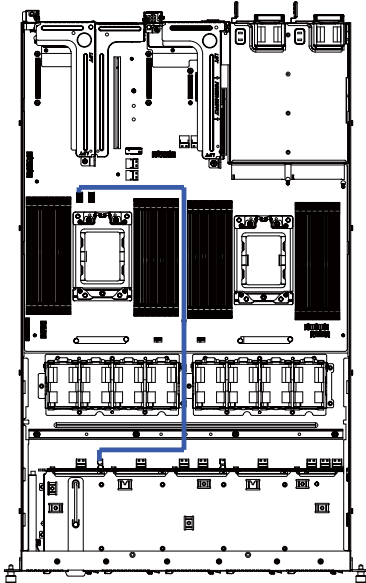
Front IO Board Cable



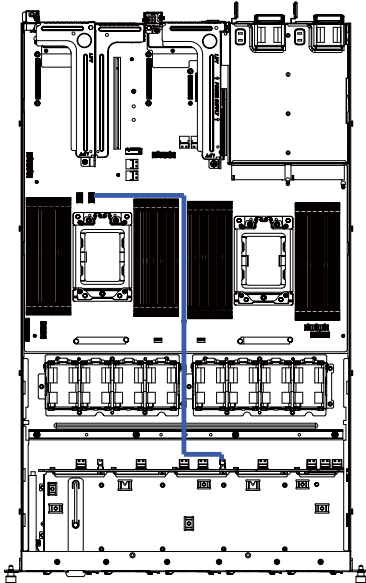
Front Panel USB 3.0 Cable



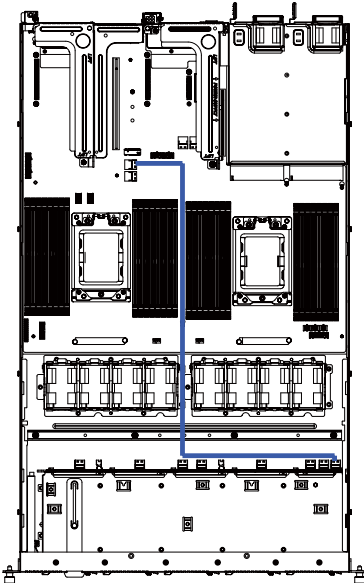
On-Board SATA to HDD Back Plane Board Cable (SATA0)



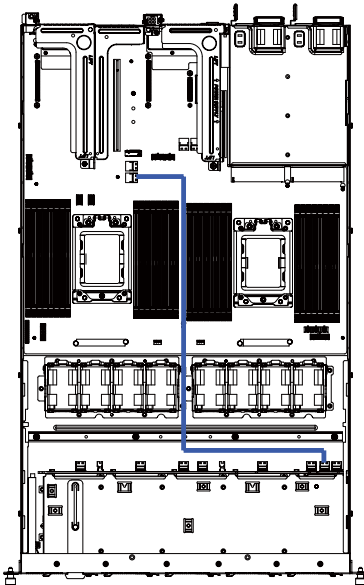
On-Board SATA to HDD Back Plane Board Cable (SATA1)



U.2 NMVe to HDD Back Plane Board Cable (NMVe0)



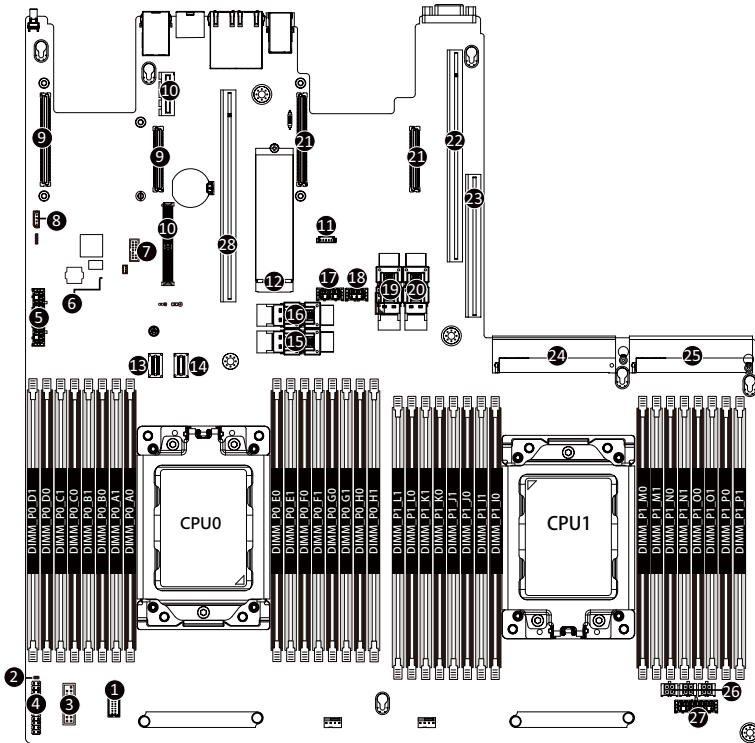
U.2 NMVe to HDD Back Plane Board Cable (NMVe1)



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Chapter 4 Motherboard Components

4-1 Motherboard Components

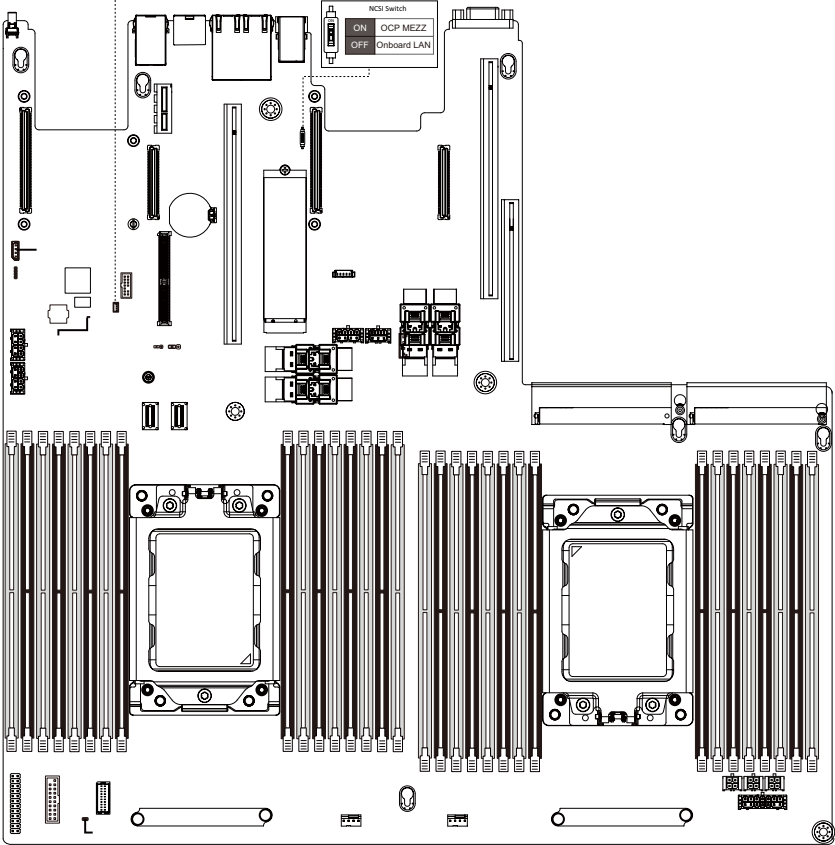


Item	Description
1	HDD Back Plane Board Connector
2	Case Open Intrusion Header
3	Front Panel USB 3.0 Connector
4	Front Panel Connector
5	2 x 4 Pin GPGPU Power Connectors
6	BMC Firmware Readiness LED
7	TPM Module Connector
8	IPMB Connector
9	OCPI Mezzanine Connector #1
10	Riser Slot Connector #1
11	SGPIO Connector
12	M.2 Slot (PCIe Gen3 x4, Support NGFF-2280, M-Key)
13	SlimLine Connector #0 (PCIe/SATA/Configurable and define SKUs)

14	SlimLine Connector #1 (PCIe/SATA/Configurable and define SKUs)
15	SlimLine Connector #3 (PCIe/SATA/Configurable and define SKUs)
16	SlimLine Connector #2 (PCIe/SATA/Configurable and define SKUs)
17	2 x 4 Pin GPGPU Power Connector
18	2 x 3 Pin Rear Back Plane Board Power Connector
19	SlimLine Connector #4 (PCIe/SATA/Configurable and define SKUs)
20	SlimLine Connector #5 (PCIe/SATA/Configurable and define SKUs)
21	OCP Mezzanine Connector#2 (Support NCSI)
22	Riser Slot Connector #3
23	Riser Slot Connector #4 for Proprietary NVMe Small Card (R181-Z91/R181-Z92 Only)
24	Power Supply Connector#1 (Primary)
25	Power Supply Connector#2 (Secondary)
26	2 x 2 Pin Extention Card Power Connectors
27	2 x 7 Pin HDD Back Plane Board Power Connector
28	Riser Slot Connector #2

4-2 Jumper Setting

J1		ON	OFF
1	HOST_SMBUS_SEL	BIOS defined	
2	PMBUS_SEL	BIOS defined	
3	BIOS_PWD	Clear supervisor password	Normal [Default]
4	DB_PLD	CPLD debug mode	Normal [Default]



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Chapter 5 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters and loading operating system, etc. BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter problems of using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the **Exit** section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 4 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

<<-><->>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<Enter>	Execute command or enter the submenu
<Esc>	Main Menu: Exit the BIOS Setup program Submenus: Exit current submenu
<F1>	Show descriptions of general help
<F3>	Restore the previous BIOS settings for the current submenus
<F9>	Load the Optimized BIOS default settings for the current submenus
<F10>	Save all the changes and exit the BIOS Setup program

5-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

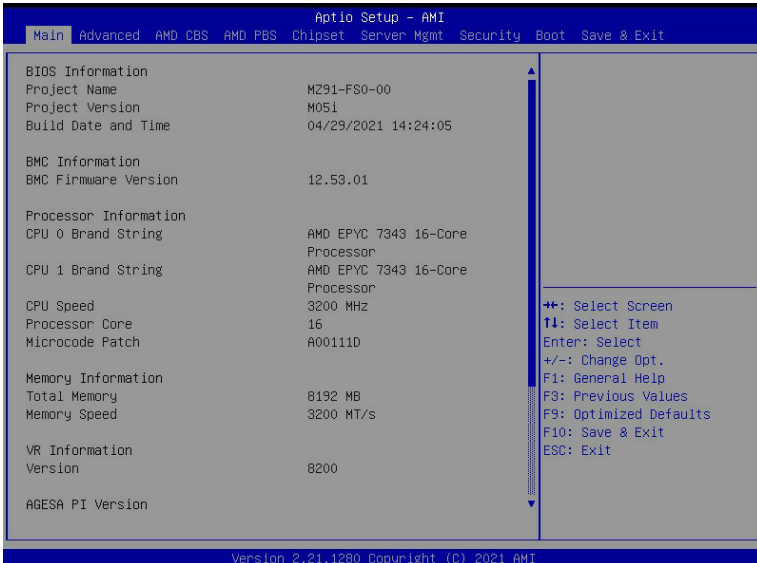
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.

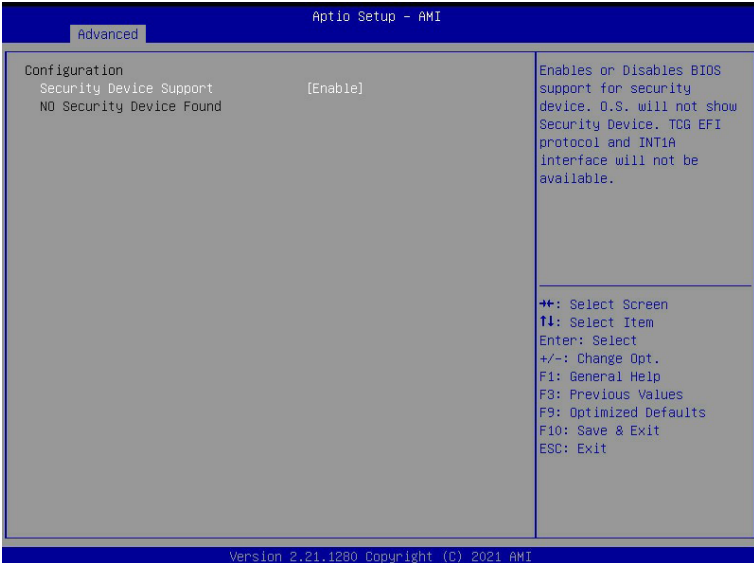


Parameter	Description
Total Memory ^(Note1)	Displays the total memory size of the installed memory.
Memory Speed ^(Note1)	Displays the frequency information of the installed memory.
VR Information	
Version	Displays VR version information.
AGESA PI Version	
PI Version	Displays AGESA PI version information.
Onboard LAN Information	
LAN1 MAC Address ^(Note2)	Displays LAN MAC address information.
LAN2 MAC Address ^(Note2)	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

(Note1) The number of LAN ports listed will depend on the motherboard / system model.

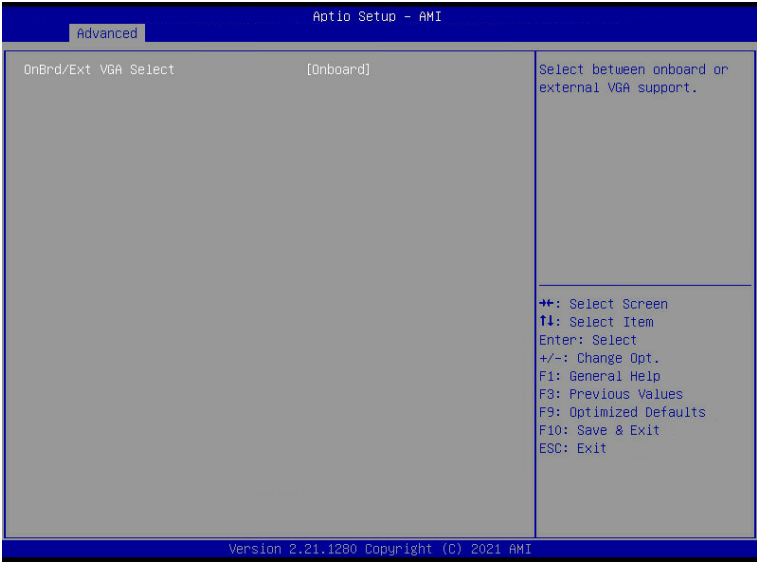
(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

5-1-1 Trusted Computing



Parameter	Description
Configuration	
Security Device Support	Select Enable to activate TPM support feature. Options available: Enable/Disable. Default setting is Enable .
SPI TPM Support	Options available: Enabled/Disabled. Default setting is Enabled

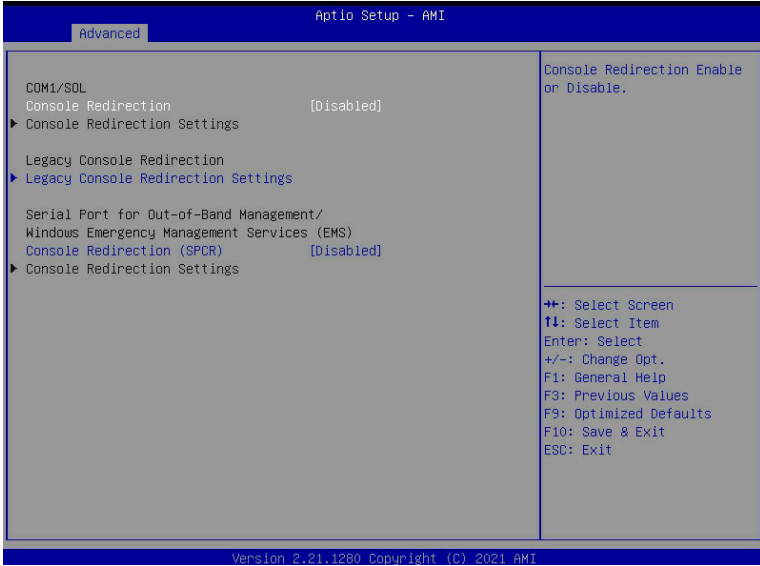
5-1-2 Legacy Video Select



Parameter	Description
OnBrd/Ext VGA Select	Select between onboard or external VGA support. Options available: Auto/Onboard/External. Default setting is Onboard .

Parameter	Description
Serial Port 1/2 Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Serial Port^(Note1): <ul style="list-style-type: none"> – Enable/Disable the Serial Port (COM). When set to Enabled allows you to configure the Serial port 1/2 settings. When set to Disabled, displays no configuration for the serial port. – Options available: Enabled/Disabled. Default setting is Enabled. ◆ Devices Settings^(Note2): <ul style="list-style-type: none"> – Displays the serial port 1/2 device settings. ◆ Change Settings^(Note2): <ul style="list-style-type: none"> – Select an optimal setting for the Super I/O device: – Options available for Serial Port 1: <ul style="list-style-type: none"> Auto IO=3F8h; IRQ=4; IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; Default setting is Auto. Options available for Serial Port 2: <ul style="list-style-type: none"> Auto IO=2F8h; IRQ=3; IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; Default setting is Auto.
	(Note1) Advanced items will appear when this item is set to Enabled .
	(Note2) This item will appear when Serial Port is set to Enabled .

5-1-3 Serial Port Console Redirection



Parameter	Description
COM1/SOL / COM2 Console Redirection ^(Note)	Select whether to enable console redirection for specified device. Console redirection enables the users to manage the system from a remote location. Options available: Enabled/Disabled. Default setting is Disabled .
Legacy Console Redirection	Selects a COM port for Legacy serial redirection. The options are dependent on the available COM ports.
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note)	Selects a COM port for EMS console redirection. EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management. Options available: Enabled/Disabled. Default setting is Disabled .
COM1/SOL / COM2 Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p>Please note that this item is configurable when COM1/SOL / COM2 Console Redirection is set to Enabled.</p> <ul style="list-style-type: none"> ◆ Terminal Type <ul style="list-style-type: none"> – Selects a terminal type to be used for console redirection. – Options available: VT100/VT100+/ANSI /VT-UTF8. Default setting is ANSI.

(Note) Advanced items prompt when this item is set to Enabled.

Parameter	Description
Legacy Console Redirection Settings	<ul style="list-style-type: none"> ◆ Redirection COM Port <ul style="list-style-type: none"> – Selects a COM port to display redirection of Legacy OS and Legacy OPROM Messages. – Options available: COM1/SOL / COM2. Default setting is COM1/SOL. ◆ Resolution <ul style="list-style-type: none"> – On Legacy OS, the number of rows and columns supported in redirection. – Options available: 80x24/80x25. Default setting is 80x24. ◆ Redirection After BIOS POST <ul style="list-style-type: none"> – This item allows user to enable console redirection after OS has loaded. – Options available: Always Enable/Boot Loader. Default setting is Always Enable.
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection Settings	<ul style="list-style-type: none"> ◆ Out-of-Band Mgmt Port <ul style="list-style-type: none"> – Selects a serial port to remotely manage a Windows server OS. – Options available: COM1/SOL / COM2. Default setting is COM1/SOL. ◆ Terminal Type <ul style="list-style-type: none"> – Selects a terminal type to be used for console redirection. – Options available: VT100/VT100+/ANSI /VT-UTF8. Default setting is VT-UTF8. ◆ Bits per second <ul style="list-style-type: none"> – Selects the transfer rate for console redirection. – Options available: 9600/19200/38400/57600/115200. Default setting is 115200. ◆ Flow Control <ul style="list-style-type: none"> – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None/Hardware RTS/CTS. Default setting is None.

■ **Main**

This setup page includes all the items in standard compatible BIOS.

■ **Advanced**

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ **AMD CBS**

This setup page includes the common items for configuration of AMD motherboard-related information.

■ **AMD PBS Option**

This setup page includes the common items for configuration of AMD CPM RAS related settings.

■ **Chipset**

This setup page includes all the submenu options for configuring the function of processor, network, North Bridge, South Bridge, and System event logs.

■ **Server Management**

Server additional features enabled/disabled setup menus.

■ **Security**

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

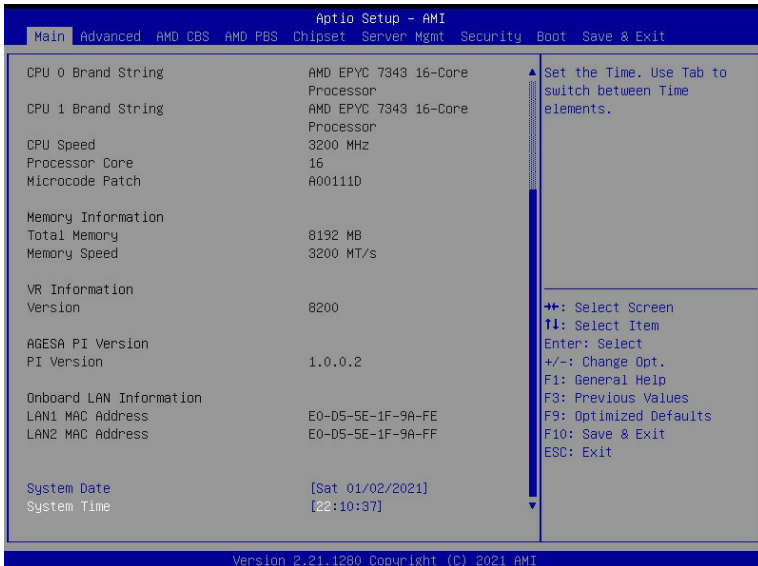
■ **Boot**

This setup page provides items for configuration of boot sequence.

■ **Save & Exit**

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

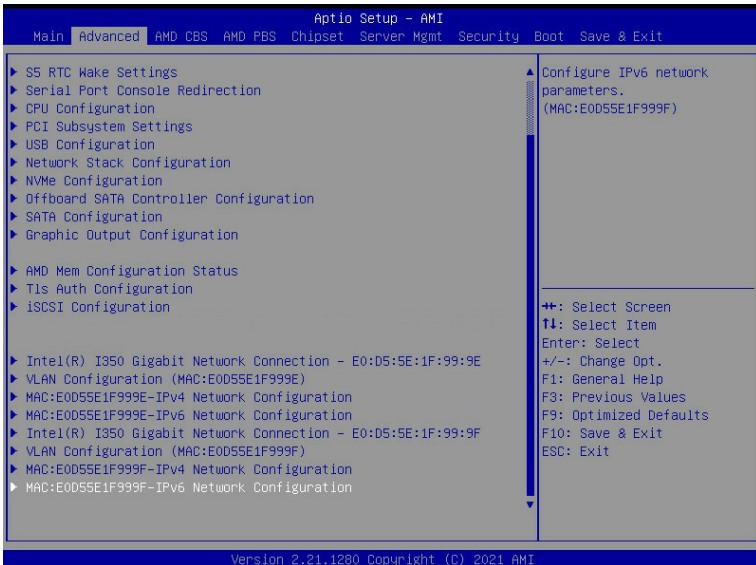
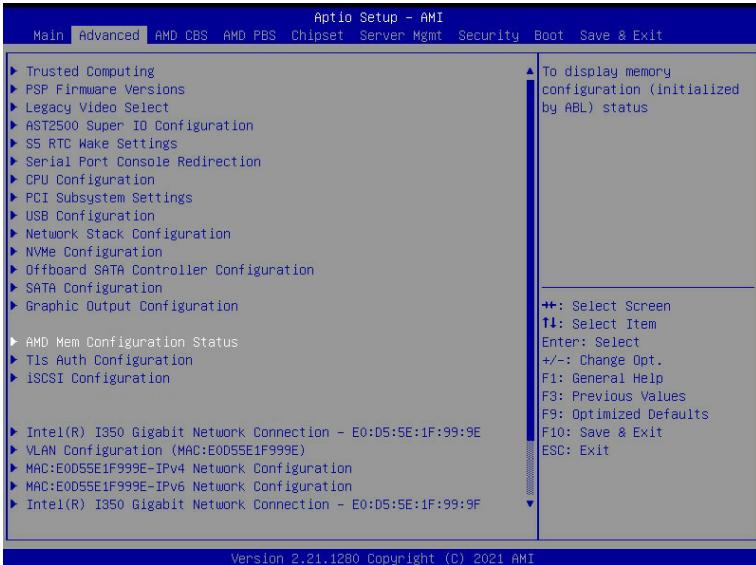
Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)



Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information	
BMC Firmware Version	Displays version number of the BIOS setup utility.
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information	
BMC Firmware Version	Displays version number of the BIOS setup utility.
Processor Information	
CPU 0 Brand String / CPU 1 Brand String / CPU Speed / Processor Core / Microcode Patch	Displays the technical information for the installed processor(s).

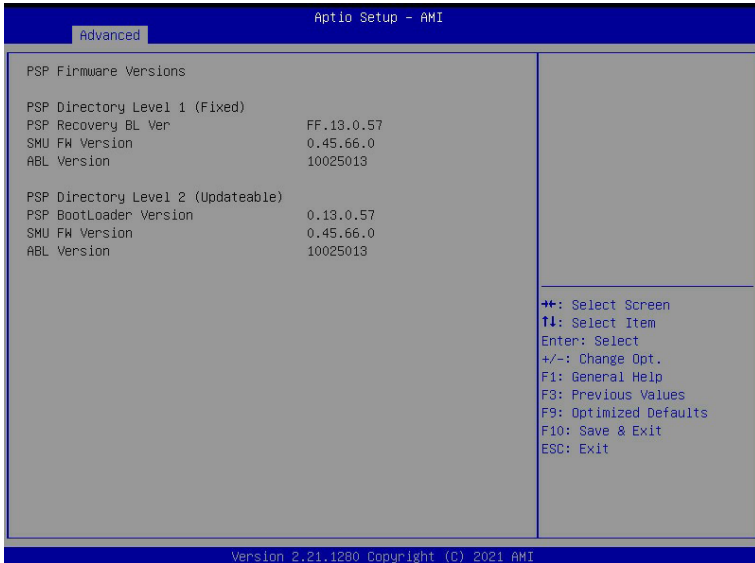
5-2 Advanced Menu

The Advanced menu display submenu options for configuring the function of various hardware components. Select a submenu item, then press [Enter] to access the related submenu screen.

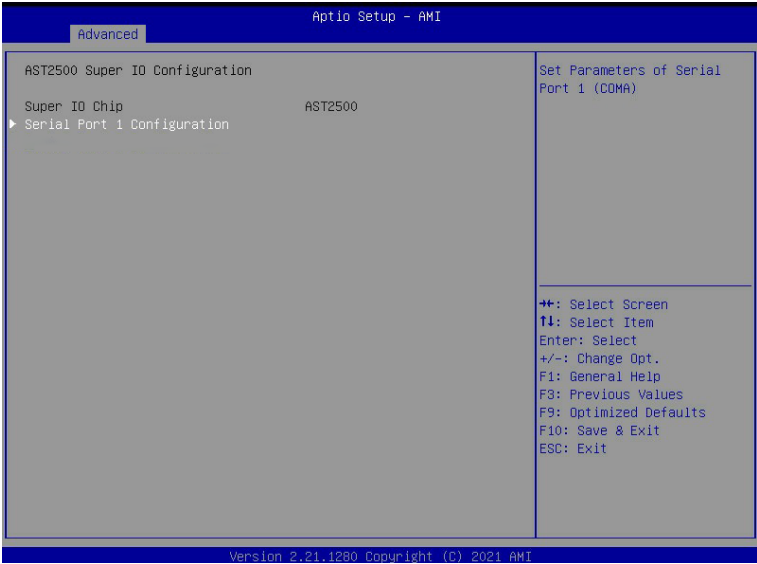


5-2-1 PSP Firmware Versions

The PSP Firmware Versions page displays the basic PSP firmware version information. Items on this window are non-configurable.

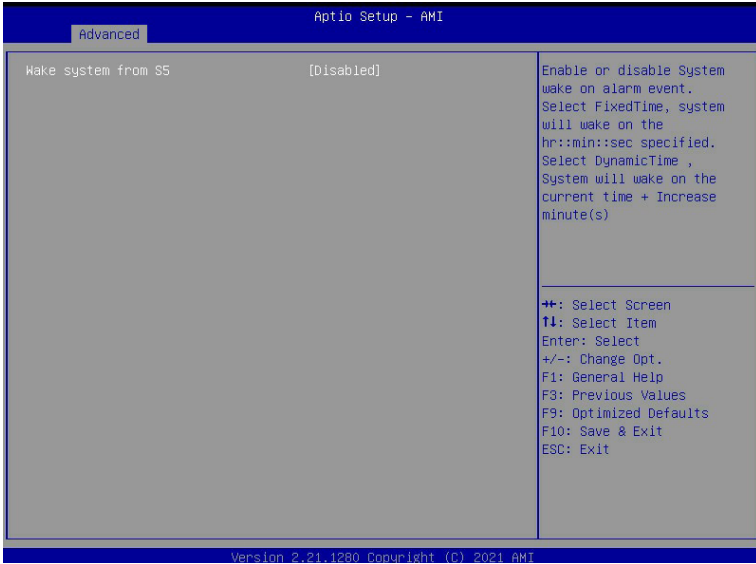


5-2-2 AST2500 Super IO Configuration



Parameter	Description
AST2500 Super IO Configuration	
Super IO Chip	Displays the super IO chip information.

5-2-3 S5 RTC Wake Settings

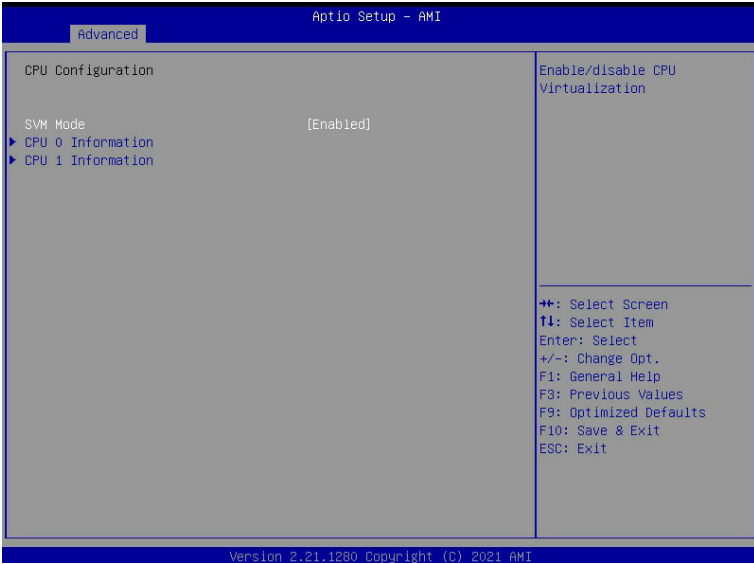


Parameter	Description
Wake system from S5	Enable or disable system wake on alarm event. Select Fixed Time, system will wake on the time (HH:MM:SS) specified. Select Dynamic Time and the system will wake at the current time plus an increase in minute(s). Options available: Disabled/Fixed Time. Default setting is Disabled .

Parameter	Description
COM1/SOL / COM2 Console Redirection Settings (continued)	<ul style="list-style-type: none"> ◆ Bits per second <ul style="list-style-type: none"> – Selects the transfer rate for console redirection. – Options available: 9600/19200/38400/57600/115200. Default setting is 115200. ◆ Data Bits <ul style="list-style-type: none"> – Selects the number of data bits used for console redirection. – Options available: 7/8. Default setting is 8. ◆ Parity <ul style="list-style-type: none"> – A parity bit can be sent with the data bits to detect some transmission errors. – Even: parity bit is 0 if the num of 1's in the data bits is even. – Odd: parity bit is 0 if num of 1's in the data bits is odd. – Mark: parity bit is always 1. Space: Parity bit is always 0. – Mark and Space Parity do not allow for error detection. – Options available: None/Even/Odd/Mark/Space. Default setting is None. ◆ Stop Bits <ul style="list-style-type: none"> – Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. – Options available: 1/2. Default setting is 1. ◆ Flow Control <ul style="list-style-type: none"> – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None/Hardware RTS/CTS. Default setting is None. ◆ VT-UTF8 Combo Key Support <ul style="list-style-type: none"> – Enable/Disable the VT-UTF8 Combo Key Support. – Options available: Enabled/Disabled. Default setting is Enabled. ◆ Recorder Mode^(Note) <ul style="list-style-type: none"> – When this mode enabled, only texts will be send. This is to capture Terminal data. – Options available: Enabled/Disabled. Default setting is Disabled. ◆ Resolution 100x31^(Note) <ul style="list-style-type: none"> – Enable/Disable extended terminal resolution. – Options available: Enabled/Disabled. Default setting is Enabled. ◆ Putty KeyPad^(Note) <ul style="list-style-type: none"> – Selects FunctionKey and KeyPad on Putty. – Options available: T100/LINUX/XTERM/R6/SCO/ESCN/VT400. – Default setting is VT100.

(Note) Advanced items prompt when this item is defined.

5-2-4 CPU Configuration



Parameter	Description
CPU Configuration	
SVM Mode	Enable/disable the CPU Virtualization. Options available: Enabled/Disabled. Default setting is Enabled .
SMEE	Controls the Secure Memory Encryption Enable (SMEE) function. Options available: Enabled/Disabled. Default setting is Enabled .
CPU 0 Information	Press [Enter] to view more information related to CPU 0.
CPU 1 Information	Press [Enter] to view more information related to CPU 1.

5-2-5 PCI Subsystem Settings

Aptio Setup - AMI

Advanced		
PCI Bus Driver Version	A5.01.24	▲ Enable/Disable SLSAS Port 0 I/O ROM.
SLSAS Port 0 I/O ROM	[Enabled]	
SLSAS Port 1 I/O ROM	[Enabled]	
SLSAS Port 2 I/O ROM	[Enabled]	
SLSAS Port 3 I/O ROM	[Enabled]	
SLSAS Port 4 I/O ROM	[Enabled]	
SLSAS Port 5 I/O ROM	[Enabled]	
Riser 1 or 2 x16	[Auto]	
Riser 1 or 2 x16 I/O ROM	[Enabled]	
Riser 1 or 2 x16 Max Link Speed	[Auto]	↔: Select Screen
Riser 1 or 2 x8	[Auto]	↑↓: Select Item
Riser 1 or 2 x8 I/O ROM	[Enabled]	Enter: Select
Riser 1 or 2 x8 Max Link Speed	[Auto]	+/-: Change Opt.
Riser_3 x16	[Auto]	F1: General Help
Riser 3 x16 I/O ROM	[Enabled]	F3: Previous Values
Riser_3 x16 Max Link Speed	[Auto]	F9: Optimized Defaults
Riser_3 x8	[Auto]	F10: Save & Exit
Riser 3 x8 I/O ROM	[Enabled]	ESC: Exit

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Advanced		
Riser_3 x16	[Auto]	▲ Enables or Disables 64bit capable Devices to be Decoded in Above 4G Address Space (Only if System Supports 64 bit PCI Decoding).
Riser 3 x16 I/O ROM	[Enabled]	
Riser_3 x16 Max Link Speed	[Auto]	
Riser_3 x8	[Auto]	
Riser 3 x8 I/O ROM	[Enabled]	
Riser_3 x8 Max Link Speed	[Auto]	
Riser_4	[Auto]	
Riser 4 I/O ROM	[Enabled]	
Riser_4 Max Link Speed	[Auto]	
ME22_1	[Auto]	↔: Select Screen
ME22 1 I/O ROM	[Enabled]	↑↓: Select Item
ME22_2	[Auto]	Enter: Select
ME22 2 I/O ROM	[Enabled]	+/-: Change Opt.
Onboard LAN Controller	[Enabled]	F1: General Help
Onboard LAN1 I/O ROM	[Enabled]	F3: Previous Values
Onboard LAN2 I/O ROM	[Enabled]	F9: Optimized Defaults
PCI Devices Common Settings:		F10: Save & Exit
Above 4G Decoding	[Enabled]	ESC: Exit

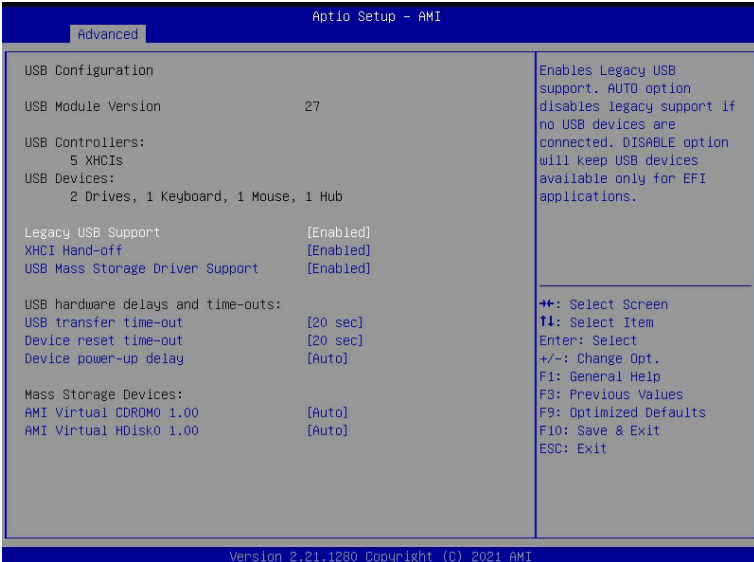
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Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
SLOT1_F / SLOT1_R / SLOT2_F / SLOT2_R / SLOT3 / OCP1 / OCP2 Lanes ^(Note1)	Change the PCIe lanes. Options available: Auto / x16 / x8 x8 / x8 x4 x4 / x4 x4 x8 / x4 x4 x4 x4 (OCP2 Lanes only features Auto / x8 / x4 x4.) Disabled. Default setting is Auto .
SLOT1_F / SLOT1_R / SLOT2_F / SLOT2_R / SLOT3 / OCP1 / OCP2 I/O ROM ^(Note1)	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Enabled/Disabled. Default setting is Enabled .
Onboard LAN Controller ^(Note2)	Enable/Disable the onboard LAN devices. Options available: Enabled/Disabled. Default setting is Enabled .
Onboard LAN I/O ROM ^(Note2)	Enable/Disable the onboard LAN devices and initializes device expansion ROM. Options available: Enabled/Disabled. Default setting is Enabled .
PCI Devices Common Settings	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Enabled/Disabled. Default setting is Enabled .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Enabled/Disabled. Default setting is Enabled .

(Note1) This section is dependent on the available PCIe Slot.

(Note2) This section is dependent on the available LAN controller.

5-2-6 USB Configuration

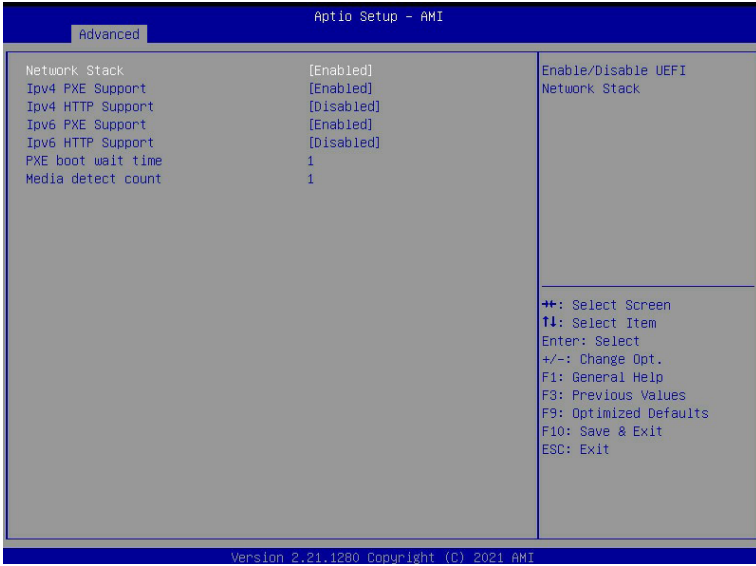


Parameter	Description
USB Configuration	
USB Module Version	Displays the USB version.
USB Controllers	Displays the supported USB controllers.
USB Devices	Displays the USB devices connected to the system.
Legacy USB Support	Enable/disable the Legacy USB support function. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications. Options available: Auto/Enabled/Disabled. Default setting is Enabled .
XHCI Hand-off	Enable/Disable the XHCI (USB 3.0) Hand-off support. Options available: Enabled/Disabled. Default setting is Enabled .
USB Mass Storage Driver Support ^(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Enabled/Disabled. Default setting is Enabled .
Port 60/64 Emulation	Enables the I/O port 60h/64h emulation support. This should be enabled for the complete USB Keyboard Legacy support for non-USB aware OS. Options available: Enabled/Disabled. Default setting is Enabled .
USB hardware delays and time-outs	
USB transfer time out	The time-out value for Control, Bulk, and Interrupt transfers. Options available: 1 sec/5 sec/10 sec/20 sec. Default setting is 20 sec .

(Note) This item is present only if you attach USB devices.

Parameter	Description
Device reset time-out	USB mass storage device Start Unit command time-out. Options available: 10 sec/20 sec/30 sec/40 sec. Default setting is 20 sec.
Device power-up delay	Maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor. Options available: Auto/Manual. Default setting is Auto.
Mass Storage Devices	
AMI Virtual CDROM0 1.00 / HDisk0 1.00	Mass storage device emulation type. AUTO enumerates devices according to their media format. Optical drives are emulated as CDROM, drives with no media will be emulated according to a drive type. Options available: Auto/Floppy/Forced FDD/Hard Disk/CD-ROM. Default setting is Auto.

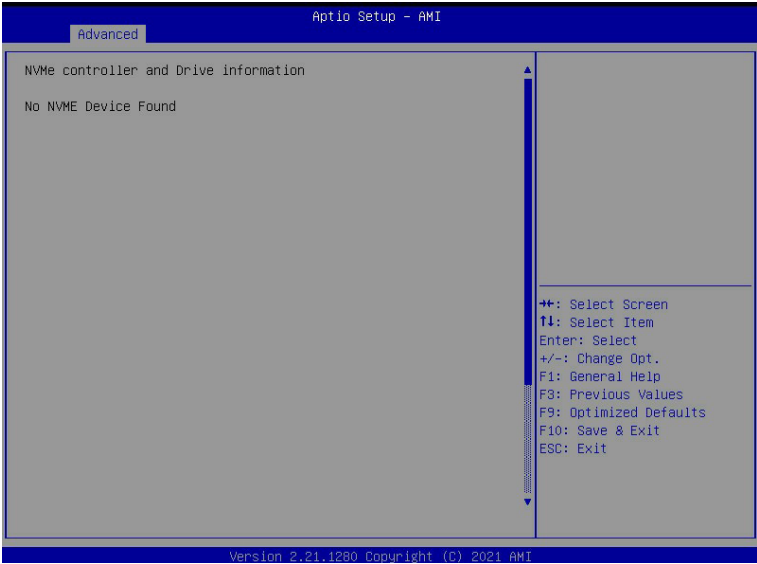
5-2-7 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled/Disabled. Default setting is Enabled .
Ipv4 PXE Support ^(Note)	Enable/Disable the Ipv4 PXE feature. Options available: Enabled/Disabled. Default setting is Enabled .
Ipv4 HTTP Support ^(Note)	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled/Disabled. Default setting is Disabled .
Ipv6 PXE Support ^(Note)	Enable/Disable the Ipv6 PXE feature. Options available: Enabled/Disabled. Default setting is Disabled .
Ipv6 HTTP Support ^(Note)	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled/Disabled. Default setting is Disabled .
IPSEC Certificate ^(Note)	Enable/Disable the IPSEC Certificate feature.
PXE boot wait time ^(Note)	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count ^(Note)	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

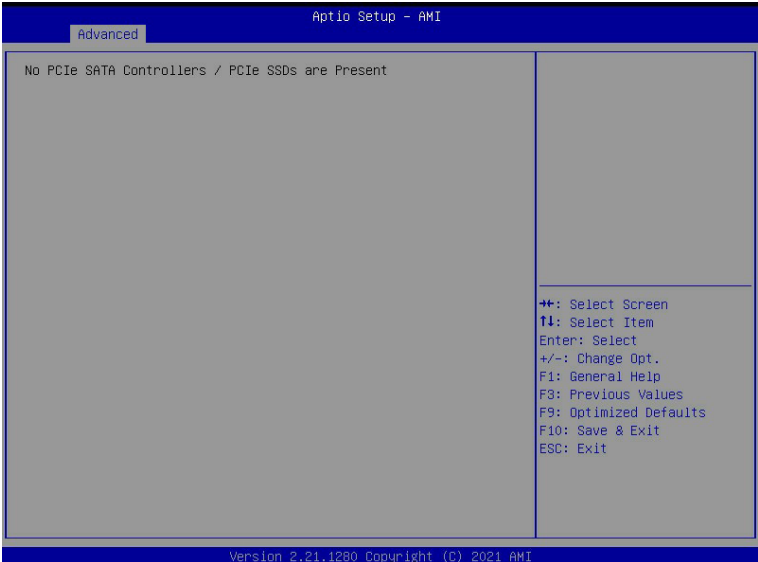
(Note) This item appears when **Network Stack** is set to **Enabled**.

5-2-8 NVMe Configuration

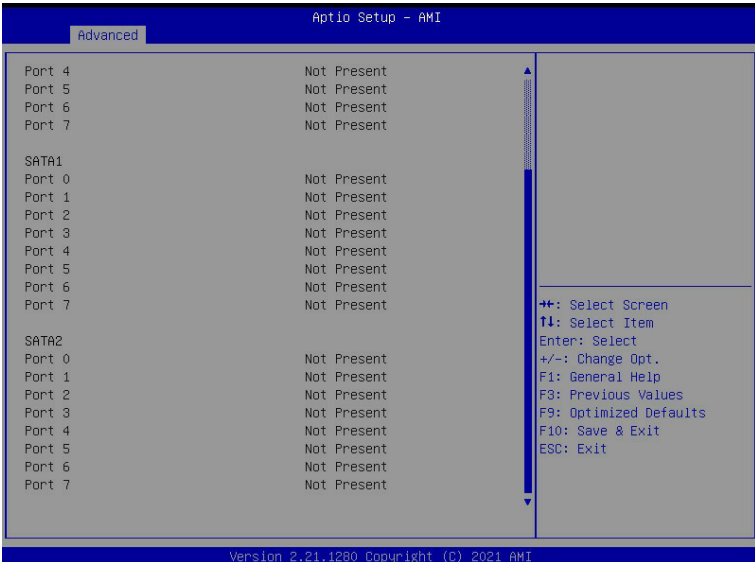
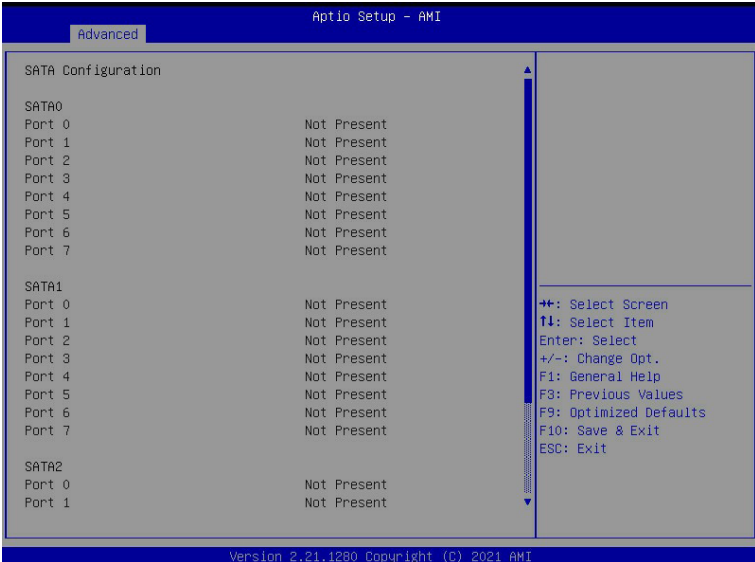


Parameter	Description
NVMe controller and Drive Information	Displays the NVMe devices connected to the system.

5-2-9 Offboard SATA Controller Configuration



5-2-10 SATA Configuration

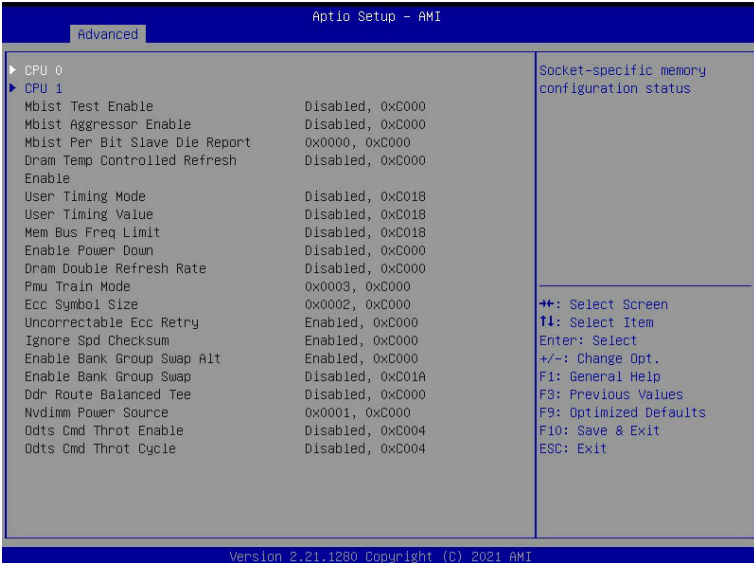


5-2-11 Graphic Output Configuration



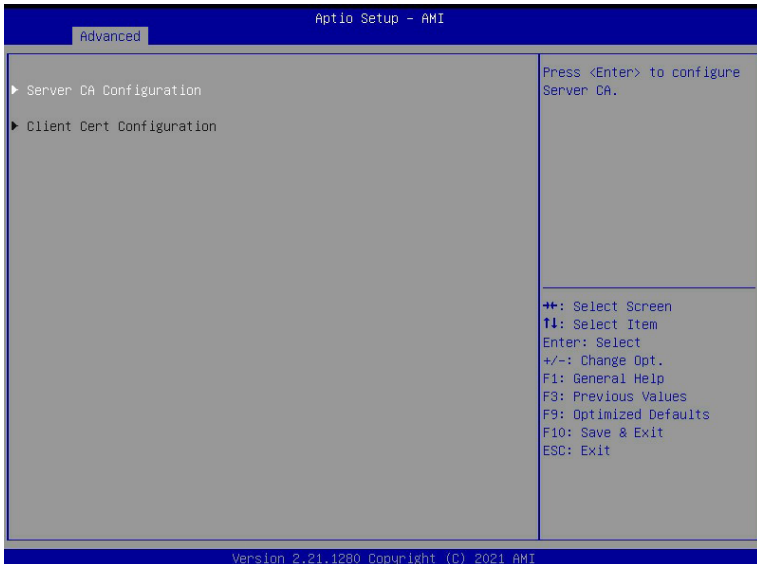
Parameter	Description
Output Device Type	Option: First loaded Device/Onboard Device/External Device/Specific Device
OS graphics output	Option: Control by OS/ Onboard VGA

5-2-12 AMD Mem Configuration Status



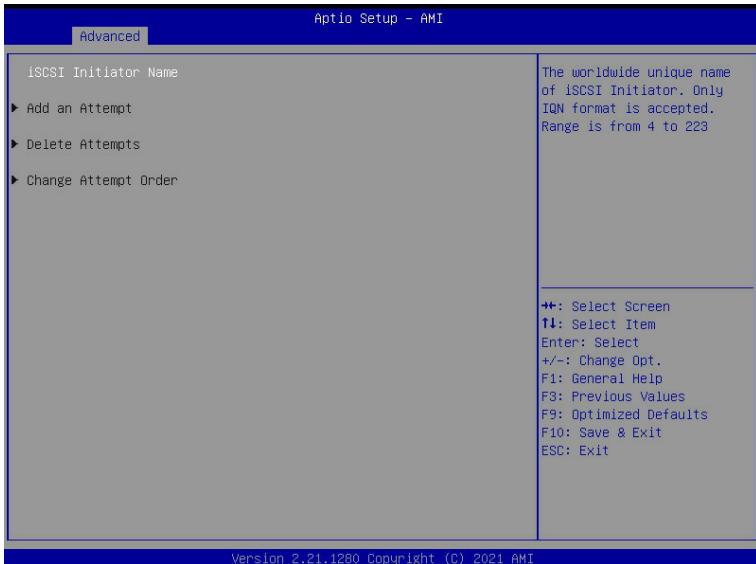
Parameter	Description
CPU 0	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> ◆ Channel A/BC/D/E/F/G/H <ul style="list-style-type: none"> - DIMM0 Presence - DIMM1 Presence - Chipset/Bank Interleave ◆ Dram EC ◆ Dram Parity ◆ Dimm Sensor Fine Grain Mode
CPU 1	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> ◆ Channel I/J/K/L/M/N/O/P <ul style="list-style-type: none"> - DIMM0 Presence - DIMM1 Presence - Chipset/Bank Interleave ◆ Dram EC ◆ Dram Parity ◆ Dimm Sensor Fine Grain Mode

5-2-13 Tls Auth Configuration



Parameter	Description
Server CA Configuration	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> ◆ Enroll Cert <ul style="list-style-type: none"> – Press [Enter] to enroll a certificate <ul style="list-style-type: none"> • Enroll Cert Using File • Cert GUID Input digit character in 1111111-2222-3333-4444-1234567890ab format. – Commit Changes and Exit – Discard Changes and Exit ◆ Delete Cert
Client Cert Configuration	N/A

5-2-14 iSCSI Configuration



Parameter	Description
iSCSI Initiator Name	Press [Enter] and name iSCSI Initiator. Only IQN format is accepted. Range: from 4 to 223
Add Attempt	Press [Enter] for configuration of advanced items.
Delete Attempt	Press [Enter] for configuration of advanced items.
Change Attempt Order	Press [Enter] for configuration of advanced items.

5-2-15 Intel(R) I350 Gigabit Network Connection

Aptio Setup - AMI

Advanced

<p>▶ NIC Configuration</p> <p>Blink LEDs 0</p> <p>UEFI Driver Intel(R) PRO/1000 7.5.11 PCI-E</p> <p>Adapter PBA 140422-008</p> <p>Device Name Intel(R) I350 Gigabit Network Connection</p> <p>Chip Type Intel i350</p> <p>PCI Device ID 1521</p> <p>PCI Address 61:00:00</p> <p>Link Status [Disconnected]</p> <p>MAC Address E0:05:5E:1F:9A:FE</p> <p>Virtual MAC Address 00:00:00:00:00:00</p>		<p>Click to configure the network device port.</p>
		<p>→+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F8: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</p>

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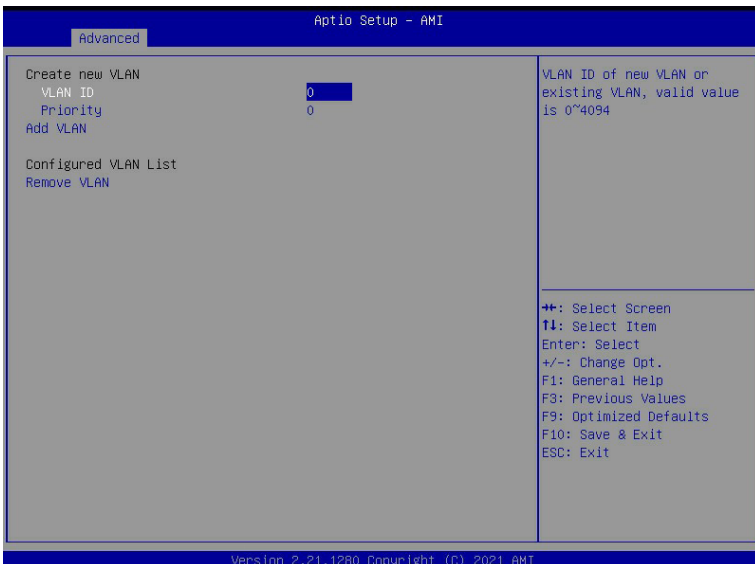
Advanced

<p>Link Speed [Auto Negotiated]</p> <p>Wake On LAN [Enabled]</p>		<p>Specifies the port speed used for the selected boot protocol.</p>
		<p>→+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F8: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit</p>

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Parameter	Description
NIC Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Link Speed <ul style="list-style-type: none"> – Allows for automatic link speed adjustment. – Options available: Auto Negotiated/10 Mbps Half/10 Mbps Full/100 Mbps Half/100 Mbps Full. Default setting is Auto Negotiated. ◆ Wake On LAN <ul style="list-style-type: none"> – Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. – Options available: Enabled/Disabled. Default setting is Enabled.
Blink LEDs	Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values.
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

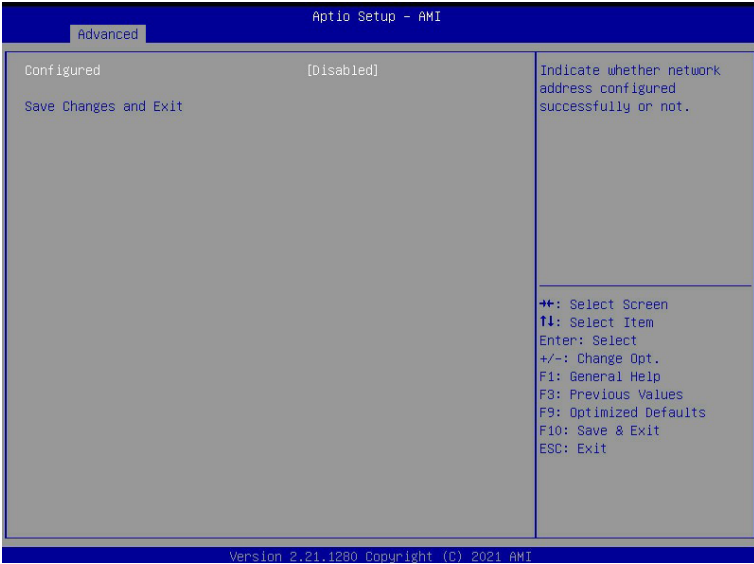
5-2-16 VLAN Configuration



Parameter	Description
Enter Configuration Menu	<p data-bbox="338 145 668 164">Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> <li data-bbox="338 174 517 192">◆ Create new VLAN <li data-bbox="338 203 934 310">◆ VLAN ID <ul style="list-style-type: none"> <li data-bbox="373 232 802 250">– Sets VLAN ID for a new VLAN or an existing VLAN. <li data-bbox="373 261 934 310">– Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 4094. <li data-bbox="338 321 934 428">◆ Priority <ul style="list-style-type: none"> <li data-bbox="373 349 849 368">– Sets 802.1Q Priority for a new VLAN or an existing VLAN. <li data-bbox="373 379 934 428">– Press the <+> / <-> keys to increase or decrease the desired values. The valid range is from 0 to 7. <li data-bbox="338 439 900 487">◆ Add VLAN <ul style="list-style-type: none"> <li data-bbox="373 467 900 487">– Press [Enter] to create a new VLAN or update an existing VLAN. <li data-bbox="338 498 884 575">◆ Configured VLAN List <ul style="list-style-type: none"> <li data-bbox="373 526 602 545">– Enable/Disable the VLAN. <li data-bbox="373 556 884 575">– Options available: Enable/Disable. Default setting is Disabled. <li data-bbox="338 586 727 631">◆ Remove VLAN <ul style="list-style-type: none"> <li data-bbox="373 614 727 631">– Press [Enter] to remove an existing VLAN.

(Note) Only Supported when **Configured VLAN List** is set to **Enabled**.

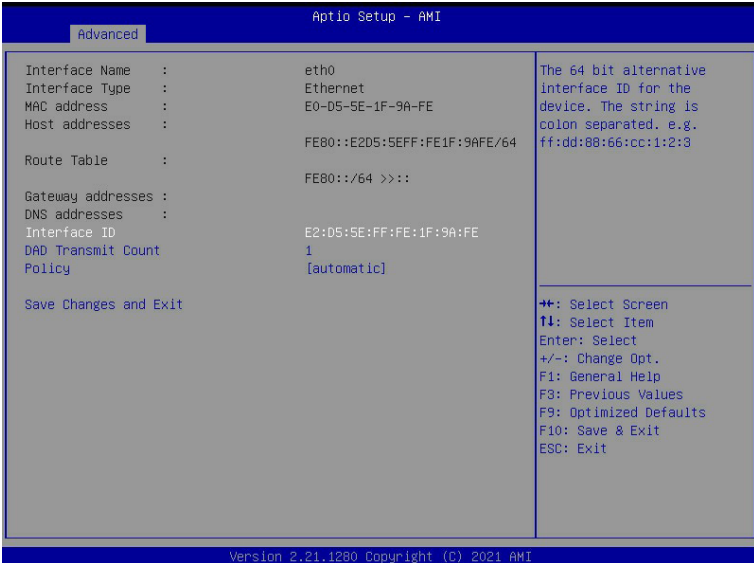
5-2-17 MAC IPv4 Network Configuration



Parameter	Description
Configured	Indicates whether network address is configured successfully or not. Options available: Disabled/Enabled. Default setting is Disabled .
Enable DHCP ^(Note)	Options available: Enabled/Disabled. Default setting is Enabled .
Local IP Address ^(Note)	Press [Enter] to configure local IP address.
Local NetMask ^(Note)	Press [Enter] to configure local NetMask.
Local Gateway ^(Note)	Press [Enter] to configure local Gateway
Local DNS Servers ^(Note)	Press [Enter] to configure local DNS servers
Save Changes and Exit	Press [Enter] and then choose to save or discard the changes made.

(Note) This item will appear on the screen when **Configured** is set to **Enabled**.

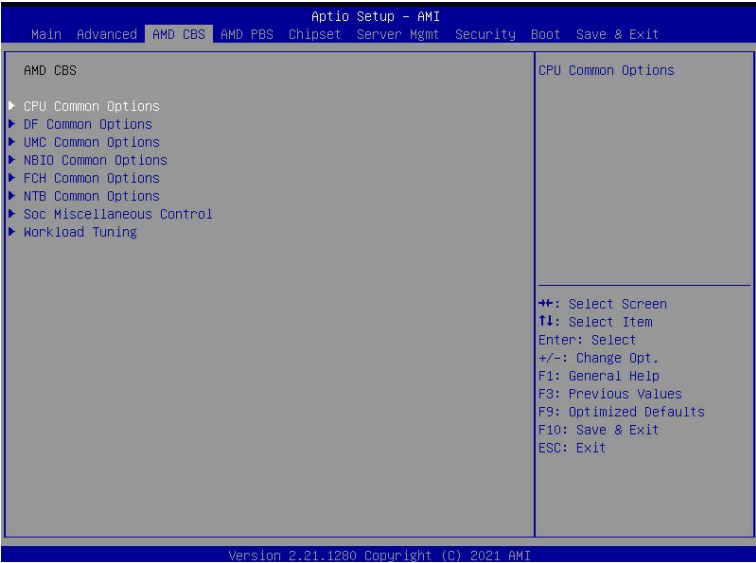
5-2-18 MAC IPv6 Network Configuration



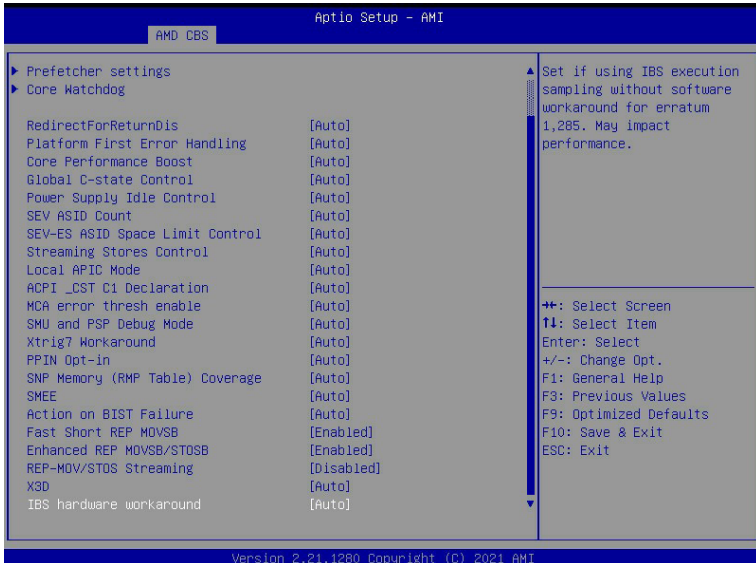
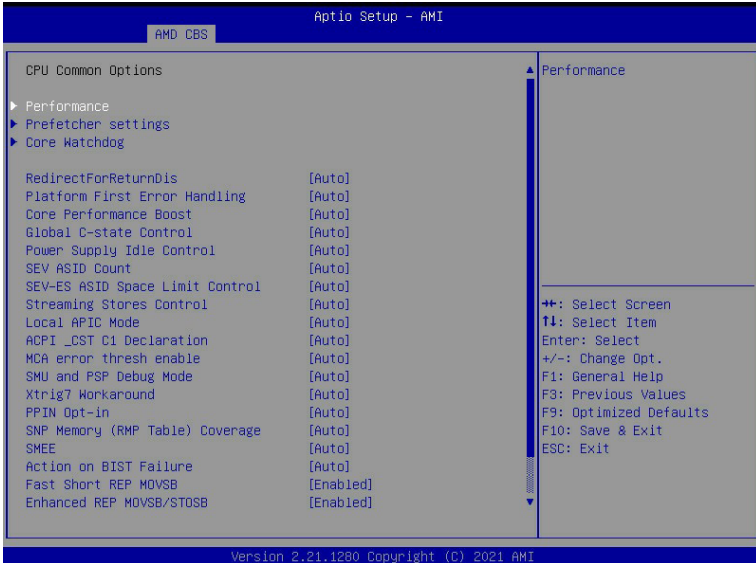
Parameter	Description
Enter Configuration Menu	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> ◆ Interface Name ◆ Interface Type ◆ MAC address ◆ Host address ◆ Route Table ◆ Gateway addresses ◆ DNS addresses ◆ Interface ID <ul style="list-style-type: none"> – The 64-bit alternative interface ID for the device. The string is colon separated e.g. ff:dd:88:66:cc:1:2:3. ◆ DAD Transmit Count <ul style="list-style-type: none"> – The number of consecutive Neighbor Solicitation messages sent while performing Duplicate Address Detection on a tentative address. A value of zero indicates that Duplicate Address Detection is not performed. ◆ Policy ◆ Save Changes and Exit

5-3 AMD CBS Menu

AMD CBS menu displays submenu options for configuring the CPU-related information that the BIOS automatically sets. Select a submenu item, then press [Enter] to access the related submenu screen.



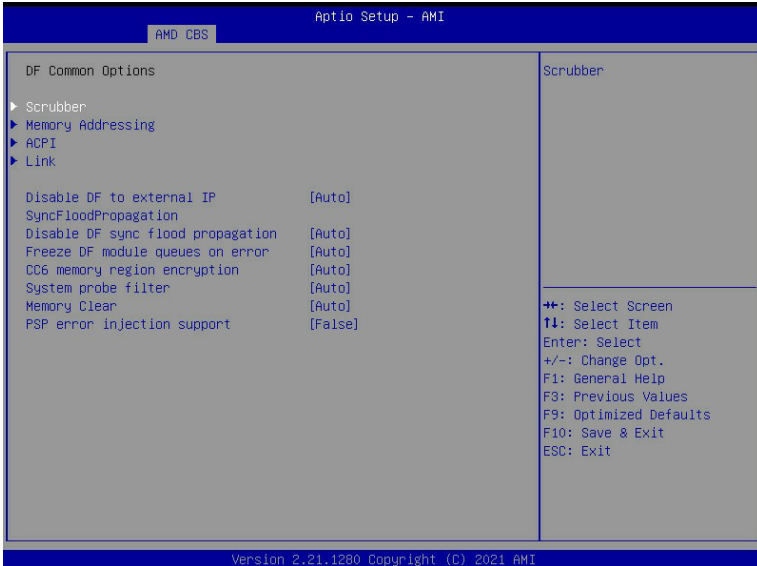
5-3-1 CPU Common Options



Parameter	Description
CPU Common Options	
Performance	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ Custom Core Pstates <ul style="list-style-type: none"> – Allows you to accept or decline custom core pstates. When accepted you can disable or customize certain pstates. ◆ CCD/Core/Thread Enablement <ul style="list-style-type: none"> – Allows you to accept or decline enabling CCDs, processor cores and threads. When accepted you can control the number of CCDs to be used, the number of cores to be used, and whether to enable or disable symmetric multithreading.
Prefetcher settings	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ L1 Stream HW Prefetcher <ul style="list-style-type: none"> – Option to enable or disable L1 Stream HW Prefetcher – Options available: Disable/Enable/Auto. Default option is Auto. ◆ L2 Stream HW Prefetcher <ul style="list-style-type: none"> – Option to enable or disable L2 Stream HW Prefetcher – Options available: Disable/Enable/Auto. Default option is Auto.
Core Watchdog	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ Core Watchdog Timer Enable <ul style="list-style-type: none"> – Enable or disable CPU watchdog timer. – Options available: Disable/Enable/Auto. Default option is Auto.
RedirectForReturnDis	<p>From a workaround for GCC/C000005 issue for XV Core on CZ A0, setting MSRC001_1029 Decode Configuration (DE_CFG) bit 14 [DecfgNoRdrctForReturns] to 1.</p> <p>Options available: Auto/1/0. Default option is Auto.</p>
Platform First Error Warning	<p>Enable/Disable PFEH, cloak individual banks, and mask deferred error interrupts from each bank.</p> <p>Options available: Enabled/Disabled/Auto. Default option is Enabled.</p>
Core Performance Boost	<p>Allows you to disable CPB.</p> <p>Options available: Disabled/Auto. Default option is Auto.</p>
Global C-State Control	<p>Controls the IO based C-state generation and DF C-states.</p> <p>Options available: Disabled/Enabled/Auto. Default option is Auto.</p>
Power Supply Idle Control	<p>Configures the power supply idle control.</p> <p>Options available: Low Current Idle/Typical current Idle/Auto. Default option is Auto.</p>
Opcache Control	<p>Enables or disables the Opcache.</p> <p>Options available: Disabled/Enabled/Auto. Default option is Auto.</p>
SEV ASID Count	<p>This field specifies the max. valid ASID, which affects the maximum system physical address space. 16TB of physical address space is available for systems that support 253 ASIDs, while 8TB of physical address space is available for systems that support 509 ASIDs.</p> <p>Options available: 253 ASIDs/509 ASIDs/Auto. Default option is Auto.</p>

Parameter	Description
SEV-ES ASID Space Limit Control	Space limit control for SEV-ES ASIDs. Options available: Auto/Manual. Default option is Auto .
Streaming Stores Control	Enables or disables the streaming stores functionality. Options available: Disabled/Enabled/Auto. Default option is Auto .
Local APIC Mode	Sets the Local APIC mode. Options available: xAPIC/x2APIC/Auto. Default option is Auto .
ACPI_CST C1 Decaration	Determines whether or not to declare the C1 state to the OS. Options available: Disabled/Enabled/Auto. Default option is Auto .
MCA error thresh enable	Enable MCA error thresholding. Options available: False/True/Auto. Default option is Auto .
SMU and PSP Debug Mode	When this option is enabled, specific uncorrected errors detected by the PSP FW or SMU FW will hand and not reset the system. Options available: Disabled/Enabled/Auto. Default option is Auto .
Xtrig7 Workaround	By default (Auto) the bronze workaround is applied. Bronze workaround: DbReq and PDM function as expected, breakpoint redirect capability compromised. Silver workaround: DbReq, PDM, and breakpoint redirect function as expected, SCAN capability compromised. Options available: Auto/No Workaround/Bronze Workaround/Silver Workaround. Default option is Auto .
PPIN Opt-in	Turns on PPIN feature. Options available: Disabled/Enabled/Auto. Default option is Auto .

5-3-2 DF Common Options

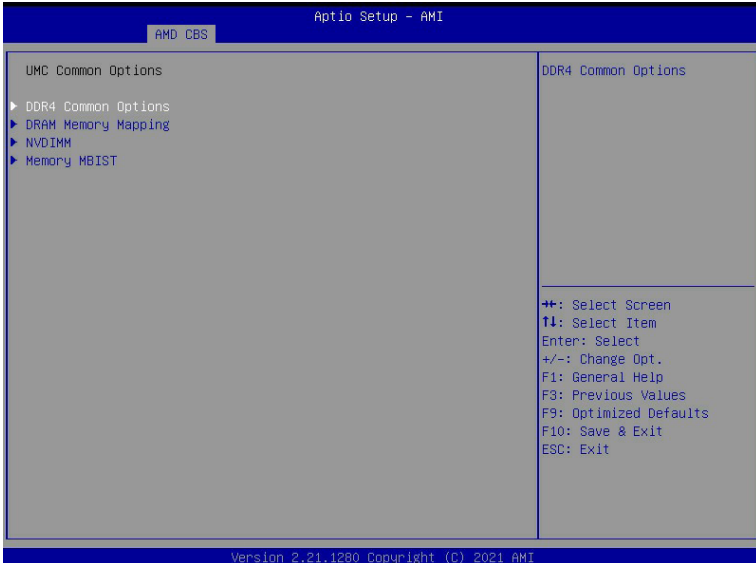


Parameter	Description
Scrubber	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> ◆ DRAM scrub time <ul style="list-style-type: none"> – Provides a value that is the number of hours to scrub memory. – Options available: Disabled/1 hour/4 hours/8 hours/16 hours/24 hours/48 hours/Auto. Default option is Auto. ◆ Poison scrubber control <ul style="list-style-type: none"> – Allows you to enable or disable poison scrubber control. – Options available: Disabled/Enabled/Auto. Default option is Auto. ◆ Redirect scrubber control <ul style="list-style-type: none"> – Allows you to enable or disable redirect of scrubber control. – Options available: Disabled/Enabled/Auto. Default option is Auto. ◆ Redirect scrubber limit <ul style="list-style-type: none"> – Allows you to set the redirect scrubber limit. – Options available: 2/4/8/Infinite/Auto. Default option is Auto.

Parameter	Description
Memory Addressing	<p data-bbox="362 142 610 166">Press [Enter] for more options.</p> <ul style="list-style-type: none"> <li data-bbox="362 170 953 341">◆ NUMA nodes per socket <ul style="list-style-type: none"> <li data-bbox="405 202 953 279">– Specifies the number of desired NUMA (Non-uniform Memory Access) nodes per socket. Zero will attempt to interleave the two sockets together. <li data-bbox="405 286 953 341">– Options available: NPS0/NPS1/NPS2/NPS4/Auto. Default option is Auto. <li data-bbox="362 349 953 459">◆ Memory interleaving <ul style="list-style-type: none"> <li data-bbox="405 381 953 428">– Allows for disabling memory interleaving. Note that NUMA nodes per socket will be honored regardless of this setting. <li data-bbox="405 435 953 459">– Options available: Disabled/Auto. Default option is Auto. <li data-bbox="362 467 953 631">◆ Memory interleaving size <ul style="list-style-type: none"> <li data-bbox="405 498 953 577">– Controls the memory interleaving size. The valid value are AUTO, 256 bytes, 512 bytes, 1Kbytes or 2Kbytes. This determines the starting address of the interleave (bit 8, 9, 10 or 11). <li data-bbox="405 584 953 631">– Options available: 256 Bytes/512 Bytes/1 KB/2KB/Auto. Default setting is Auto. <li data-bbox="362 639 953 804">◆ 1TB remap <ul style="list-style-type: none"> <li data-bbox="405 671 953 749">– Attempt to remap DRAM out of the space just below the 1TB boundary. The ability to remap depends on DRAM configuration, NPS, and interleaving selection, and may not always be possible. <li data-bbox="405 757 953 804">– Options available: Do not remap/Attempt to remap/Auto. Default option is Auto. <li data-bbox="362 812 953 929">◆ DRAM map inversion <ul style="list-style-type: none"> <li data-bbox="405 843 953 890">– Inverting the map will cause the highest memory channels to get assigned the lowest addresses in the system. <li data-bbox="405 898 953 929">– Options available: Disabled/Enabled/Auto. Default option is Auto.
ACPI	<p data-bbox="362 937 610 961">Press [Enter] for more options.</p> <ul style="list-style-type: none"> <li data-bbox="362 965 953 1130">◆ ACPI SRAT L3 Cache as NUMA Domain <ul style="list-style-type: none"> <li data-bbox="405 997 953 1044">– Enabled: Each CCX in the system will be declared as a separate NUMA domain. <li data-bbox="405 1052 953 1099">– Disabled: Memory Addressing \ NUMA nodes per socket will be declared. <li data-bbox="405 1107 953 1130">– Options available: Disable/Enable/Auto. Default option is Auto. <li data-bbox="362 1138 953 1216">◆ ACPI SLIT Distance Control <ul style="list-style-type: none"> <li data-bbox="405 1169 953 1193">– Determines how the SLIT distances are declared. <li data-bbox="405 1201 953 1216">– Options available: Manual/Auto. Default option is Auto. <li data-bbox="362 1224 953 1334">◆ ACPI SLIT remote relative distance <ul style="list-style-type: none"> <li data-bbox="405 1255 953 1303">– Set the remote socket distance for 2P systems as near (2.8) or far (3.2). <li data-bbox="405 1310 953 1334">– Options available: Near/Far/Auto. Default option is Auto.

Parameter	Description
Link	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ GMI encryption control <ul style="list-style-type: none"> – Control GMI link encryption. – Options available: Disable/Enable/Auto. Default option is Auto. ◆ xGMI encryption control <ul style="list-style-type: none"> – Control xGMI link encryption. Options available: Disable/Enable/Auto. Default option is Auto. ◆ CAKE CRC perf bounds control <ul style="list-style-type: none"> – Control CAKE CRC perf bounds – Options available: Auto/Manual. Default option is Auto. ◆ 4-link xGMI max speed <ul style="list-style-type: none"> – Set 4-link xGMI max speed. – Options available: 10.667Gbps/13Gbps/16Gbps/18Gbps/Auto. Default option is Auto. ◆ 3-link xGMI max speed <ul style="list-style-type: none"> – Set 3-link xGMI max speed. – Options available: 10.667Gbps/13Gbps/16Gbps/18Gbps/Auto. Default option is Auto. ◆ xGMI TXEQ Mode <ul style="list-style-type: none"> – Select XGMI TXEQ/RX vetting Mode. – Options available: TXEQ_Disabled/TXEQ_LAnet/TXEQ_Link/TXEQ_RX_Vet/Auto. Default option is Auto.
Disable DF to external IP Sync Flood Propagation	<p>Disable SyncFlood to UMC & downstream slaves. Options available: Sync flood disabled/Sync flood enabled/Auto. Default option is Auto.</p>
Disable DF sync flood propagation	<p>Enable/Disable DF SyncFlood. Options available: Sync flood disabled/Sync flood enabled/Auto. Default option is Auto.</p>
Freeze DF module queues on error	<p>Controls DF PIE Config. Disabling this options sets DF:PIEConfig. Options available: Disable/Enable/Auto. Default option is Auto.</p>
CC6 memory region encryption	<p>Control whether or not the CC6 save/restore memory is encrypted. Options available: Disable/Enable/Auto. Default option is Auto.</p>
System probe filter	<p>Controls whether or not the probe filter is enabled. Has no effect on parts where the probe filter is fuse disabled. Options available: Disable/Enable/Auto. Default option is Auto.</p>
Memory Clear	<p>When this feature is disabled, BIOS does not implement MemClear after memory training (only if non-ECC DIMMs are used). Options available: Disable/Enable/Auto. Default option is Auto.</p>
PSP error injection support	<p>Select True to enable error injection. Options available: False/True. Default option is False.</p>

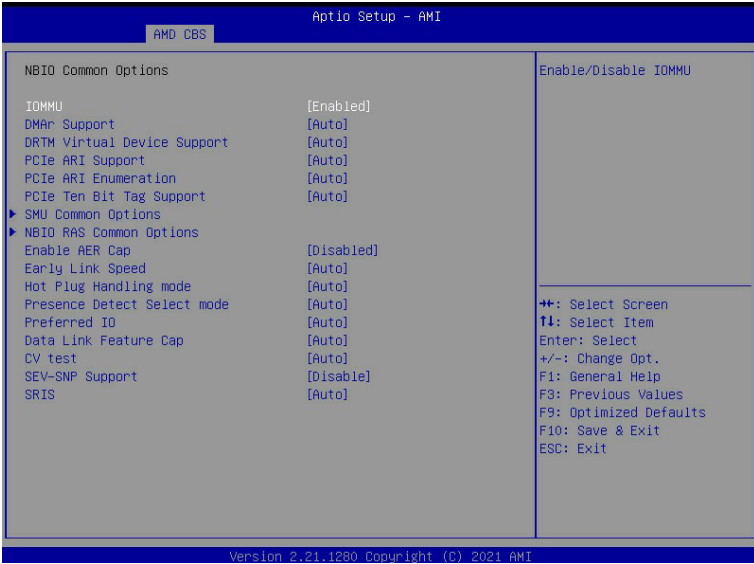
5-3-3 UMC Common Options



Parameter	Description
DDR4 Common Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ Enforce POR <ul style="list-style-type: none"> – Press [Enter] to configure the enforcement of Plan Of Record (POR) which enables enforcement of POR restrictions for DDR4 frequency and voltage programming. Memory speeds will be capped at Intel guidelines. ◆ DRAM Controller Configuration <ul style="list-style-type: none"> – Press [Enter] to configure DRAM controller options. ◆ CAD Bus Configuration <ul style="list-style-type: none"> – Press [Enter] to configure CAD Bus options. ◆ Data Bus configuration <ul style="list-style-type: none"> – Press [Enter] to configure Data Bus options. ◆ Common RAS <ul style="list-style-type: none"> – Press [Enter] to configure Common RAS options. ◆ Security <ul style="list-style-type: none"> – Press [Enter] to configure UMC security options.

Parameter	Description
DRAM Memory Mapping	<p>Press [Enter] for more options</p> <ul style="list-style-type: none"> ◆ Chipselect Interleaving <ul style="list-style-type: none"> – Interleave memory blocks across the DRAM chip selects for node 0 – Options available: Disabled/Auto. Default option is Auto. ◆ BankGroupSwap <ul style="list-style-type: none"> – Configures the BankGroupSwap. BankGroupSwap (BGS) is a memory mapping option in AGESA that alters how applications get assigned to physical locations within the memory modules. When this option sets to Auto, it is null. – Options available: Enabled/Disabled/Auto. Default option is Auto. ◆ BankGroupSwapAlt <ul style="list-style-type: none"> – Configures the BankGroupSwapAlt. – Options available: Enabled/Disabled/Auto. Default option is Auto. ◆ Address Hash Bank <ul style="list-style-type: none"> – Enable or disable bank address hashing. – Options available: Disabled/Enabled/Auto. Default option is Auto. ◆ Address Hash CS <ul style="list-style-type: none"> – Enable or disable CS address hashing. – Options available: Auto/Enabled/Disabled. Default option is Auto. ◆ Address Hash Rm <ul style="list-style-type: none"> – Enable or disable RM address hashing. – Options available: Auto/Enabled/Disabled. Default option is Auto. ◆ SPD Read Optimization <ul style="list-style-type: none"> – Enable or disable SPD Read Optimization. Enabled = SPD reads are skipped for Reserved fields and most of upper 256 Bytes, Disabled = read all 512 SPD Bytes. – Options available: Auto/Enabled/Disabled. Default option is Auto.
NVDIMM	<p>Press [Enter] for more options.</p>
Memory MBIST	<p>Press [Enter] for more options</p> <ul style="list-style-type: none"> ◆ MBIST Enable <ul style="list-style-type: none"> – Enable or disable Memory MBIST. – Options available: Disabled/Enabled. Default option is Disabled. ◆ Data Eye <ul style="list-style-type: none"> – Press [Enter] for more options.

5-3-4 NBIO Common Options



Parameter	Description
NBIO Common Options	
IOMMU	Enable/Disable IOMMU. Options available: Enabled/Disabled. Default setting is Disabled .
ACS Enable	AER must be enabled for ACS enable to work. Options available: Enable/Disabled/Auto. Default option is Auto .
PCIe ARI Support	Enables Alternative Routing ID Interpretation. Options available: Disable/Enable/Auto. Default option is Auto .
PCIe Ten Bit Tag Support	Enables PCIe ten bit tags for supported devices. Auto = Disabled Options available: Disable/Enable/Auto. Default option is Auto .
HD Audio Enable	Enables or disables HD Audio. Options available: Enable/Disabled/Auto. Default option is Auto .

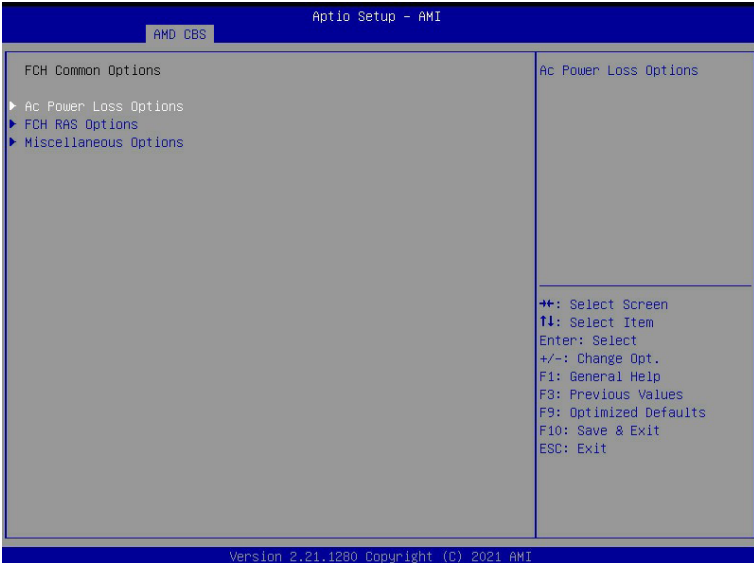
Parameter	Description
SMU Common Options	<p data-bbox="370 145 610 166">Press [Enter] for more options.</p> <ul style="list-style-type: none"> <li data-bbox="370 174 884 283">◆ Determinism Control <ul style="list-style-type: none"> <li data-bbox="408 205 884 252">– Auto = Use the fused determinism, Manual = User can set customized determinism. <li data-bbox="408 260 880 283">– Options available: Manual/Auto. Default option is Manual. <li data-bbox="370 291 884 401">◆ cTDP Control <ul style="list-style-type: none"> <li data-bbox="408 323 951 370">– Auto = Use the fused TDP, Manual = User can set customized TDP. TDP is used to define the RC thermal model only. <li data-bbox="408 377 880 401">– Options available: Manual/Auto. Default option is Manual. <li data-bbox="370 409 795 456">◆ Fan Control <ul style="list-style-type: none"> <li data-bbox="408 440 795 456">– Press [Enter] to configure the fan control table. <li data-bbox="370 464 876 542">◆ CLD0_VDDP Control <ul style="list-style-type: none"> <li data-bbox="408 495 876 519">– Manual = User can set customized CLD0_VDDP voltage. <li data-bbox="408 526 859 542">– Options available: Auto/Manual. Default option is Auto. <li data-bbox="370 550 868 660">◆ EfficiencyModeEn <ul style="list-style-type: none"> <li data-bbox="408 581 951 628">– 0 = use performance optimized CCLK DPM settings, 1 = use power efficiency optimized CCLK DPM settings. <li data-bbox="408 636 868 660">– Options available: Auto/Enabled. Default option is Auto. <li data-bbox="370 667 951 777">◆ Package Power Limit Control <ul style="list-style-type: none"> <li data-bbox="408 699 951 746">– Auto = Use the fused PPT, Manual = User can set PPT. PPT will be used as the ASIC power limit. <li data-bbox="408 754 880 777">– Options available: Manual/Auto. Default option is Manual. <li data-bbox="370 785 951 895">◆ xGMI Link Width Control <ul style="list-style-type: none"> <li data-bbox="408 816 951 863">– Auto = Use degault xGMI link width controller, Manual = User can set custom xGMI link width controller settings. <li data-bbox="408 871 859 895">– Options available: Manual/Auto. Default option is Auto. <li data-bbox="370 903 827 981">◆ APBDIS <ul style="list-style-type: none"> <li data-bbox="408 934 788 958">– 0 = not APBDIS (mission mode), 1 = APBDIS. <li data-bbox="408 965 827 981">– Options available: 0/1/Auto. Default option is Auto. <li data-bbox="370 989 942 1067">◆ DF Cstates <ul style="list-style-type: none"> <li data-bbox="408 1020 671 1044">– Enable or disable DF C-states. <li data-bbox="408 1052 942 1067">– Options available: Disabled/Enabled/Auto. Default option is Auto. <li data-bbox="370 1075 942 1154">◆ CPPC <ul style="list-style-type: none"> <li data-bbox="408 1107 625 1130">– Enable or disable CPPC. <li data-bbox="408 1138 942 1154">– Options available: Disabled/Enabled/Auto. Default option is Auto. <li data-bbox="370 1161 919 1240">◆ BoostFmaxEn <ul style="list-style-type: none"> <li data-bbox="408 1193 919 1216">– Auto = Use degault Fmax, Manual = User can set boost Fmax. <li data-bbox="408 1224 859 1240">– Options available: Manual/Auto. Default option is Auto.

Parameter	Description
NBIO RAS Common Options	<p data-bbox="370 147 607 166">Press [Enter] for more options.</p> <ul style="list-style-type: none"> <li data-bbox="370 174 859 224">◆ NBIO RAS Global Control <ul style="list-style-type: none"> <li data-bbox="405 205 859 224">– Options available: Manual/Auto. Default option is Auto. <li data-bbox="370 232 934 313">◆ NBIO RAS Control <ul style="list-style-type: none"> <li data-bbox="405 260 708 279">– 0 = Disabled, 1 = MCA, 2 = Legacy. <li data-bbox="405 291 934 310">– Options available: Disabled/MCA/Legacy. Default option is MCA. <li data-bbox="370 321 908 402">◆ Egress Poison Severity High <ul style="list-style-type: none"> <li data-bbox="405 349 908 402">– Enter a value. Each bit set to 1 enables high severity on the associated IOHC egress port. A bit of 0 indicates low severity. <li data-bbox="370 410 908 492">◆ Egress Poison Severity Low <ul style="list-style-type: none"> <li data-bbox="405 439 908 492">– Enter a value. Each bit set to 1 enables high severity on the associated IOHC egress port. A bit of 0 indicates low severity. <li data-bbox="370 500 942 635">◆ NBIO SyncFlood Generation <ul style="list-style-type: none"> <li data-bbox="405 528 942 609">– This value may be used to mask SyncFlood caused by NBIO RAS options. When set to TRUE SyncFlood from NBIO is masked. When set to FALSE NBIO is capable of generating SyncFlood. <li data-bbox="405 617 942 636">– Options available: Enabled/Disabled/Auto. Default option is Auto. <li data-bbox="370 642 934 777">◆ NBIO SyncFlood Reporting <ul style="list-style-type: none"> <li data-bbox="405 671 934 752">– This value may be used to enable SyncFlood reporting to APLM. When set to TRUE SyncFlood will be reported to APLM. When set to FALSE that reporting will be disabled. <li data-bbox="405 760 934 779">– Options available: Enabled/Disabled. Default option is Disabled. <li data-bbox="370 785 929 926">◆ Egress Poison Mask High <ul style="list-style-type: none"> <li data-bbox="405 813 929 926">– Enter a value. These set the enable mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions. <li data-bbox="370 934 929 1075">◆ Egress Poison Mask Low <ul style="list-style-type: none"> <li data-bbox="405 962 929 1075">– Enter a value. These set the enable mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions. <li data-bbox="370 1083 934 1249">◆ Uncorrected Converted to Poison Enable Mask High <ul style="list-style-type: none"> <li data-bbox="405 1111 934 1249">– Enter a value. These set the enable mask for masking of uncorrectable parity errors on internal arrays. For each bit set to 1, a system fatal error event is triggered for UCP errors on arrays associated with that egress port. For each bit set to 0, errors are masked. <li data-bbox="370 1257 934 1420">◆ Uncorrected Converted to Poison Enable Mask Low <ul style="list-style-type: none"> <li data-bbox="405 1285 934 1420">– Enter a value. These set the enable mask for masking of uncorrectable parity errors on internal arrays. For each bit set to 1, a system fatal error event is triggered for UCP errors on arrays associated with that egress port. For each bit set to 0, errors are masked.

Parameter	Description
NBIO RAS Common Options (continued)	<ul style="list-style-type: none"> ◆ System Hub Watchdog Timer <ul style="list-style-type: none"> – Enter a value. This value specifies the timer interval of the SYSHUB watchdog timer in milliseconds. ◆ SLINK Read Response OK <ul style="list-style-type: none"> – This value specifies whether SLINK read response errors are converted to an Okay response. When this value is set to TRUE, read response errors are converted to Okay responses with data of all FFs. When set to FALSE read response errors are not converted. – Options available: Enabled/Disabled. Default option is Disabled. ◆ SLINK Read Response Error Handling <ul style="list-style-type: none"> – This value specifies whether SLINK write response errors are converted to an Okay response. When this value is set to 0, write response errors will be logged in the MCA. When set to 1, write response errors will trigger an MCOMMIT error. When this value is set to 2, write response errors are converted to Okay responses. – Options available: Enabled/Trigger MCOMMIT Error/Log Errors in MCA. Default option is Log Errors in MCA. ◆ Log Poison Data from SLINK <ul style="list-style-type: none"> – This value specifies whether poison data propagated from SLINK will generate a deferred error. When this value is set to TRUE, deferred errors are enabled. When set to FALSE, errors are not generated. – Options available: Enabled/Disabled. Default option is Disabled. ◆ PCIe Aer Reporting Mechanism <ul style="list-style-type: none"> – This value selects the method of reporting AER errors from PCI Express. A value of 0 indicates that the hardware will report the error through MCA. A value of 1 allows OS First handling of the errors through generation of a system control interrupt (SCI). A value of 2 provides for Firmware First handling of errors through generation of a system management interrupt (SMI). – Options available: OS First/MCA/Auto. Default option is Auto. ◆ Edpc Control <ul style="list-style-type: none"> – (0) Disabled; (1) Enabled; (3) Auto. – Options available: Disabled/Enabled/Auto. Default option is Disabled. ◆ NBIO Poison Consumption <ul style="list-style-type: none"> – Options available: Auto/Enabled/Disabled. Default option is Auto.

Parameter	Description
NBIO RAS Common Options (continued)	<ul style="list-style-type: none"> ◆ Sync Flood on PCIe Fatal Error <ul style="list-style-type: none"> – When 'Sync Flood on PCIe Fatal Error' is True, PcdAmdPcieSyncFloodOnFatal should be set to True. When 'Sync Flood on PCIe Fatal Error' is False, PcdAmdPcieSyncFloodOnFatal should be set to False. When 'Sync Flood on PCIe Fatal Error' is Auto, PcdAmdPcieSyncFloodOnFatal should retain its AGESA default. – Options available: Auto/True/False. Default option is Auto.
Enable AER Cap	Enables Advanced Error Reporting Capability. Options available: Enable/Disabled/Auto. Default option is Auto .
Early Link Speed	Sets Early Link Speed. Options available: Auto/Gen1/Gen2. Default option is Auto .
Hot Plug Handling mode	Controls the Hot Plug Handling mode. Options available: A0 Mode/OS First (No Error Handling)/OS First (Error Handling - Not Implementd/Firmware First (Not Implemented)/Auto. Default option is Auto .
Presence Detect Select mode	Controls the Presence Detect Select mode. Options available: OR/And/Auto. Default option is Auto .
Preferred IO Device	Enter a value for the preferred IO device. [23:16] Bus Number [15:8] Dev Number [7:0] Fun Number

5-3-5 FCH Common Options

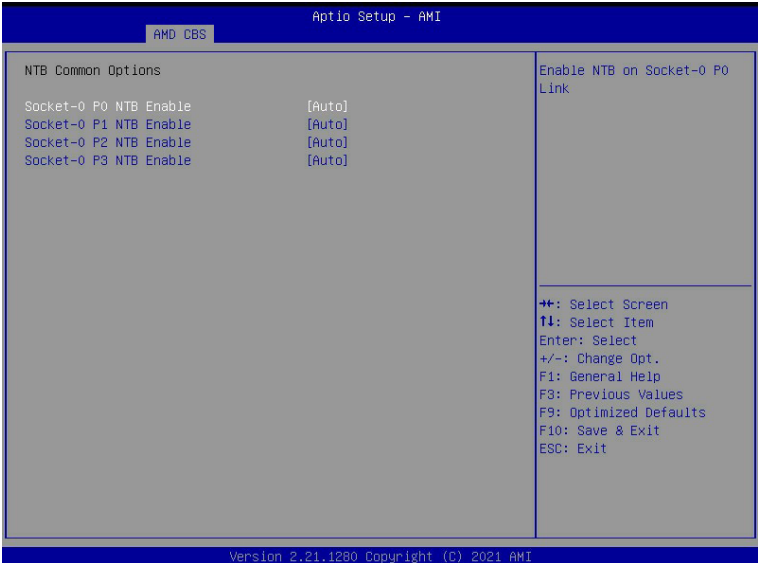


Parameter	Description
FCH Common Options	
SATA Configuration Options	<ul style="list-style-type: none"> ◆ SATA Enable <ul style="list-style-type: none"> - Enable or disable OnChip SATA controller. - Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ SATA RAS Support <ul style="list-style-type: none"> - Enable or disable SATA RAS support. - Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ Sata Disabled AHCI Prefetch Function <ul style="list-style-type: none"> - Enable or disable Sata Disabled AHCI Prefetch Function. - Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ Aggressive SATA Device Sleep Port 0 <ul style="list-style-type: none"> - Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ Aggressive SATA Device Sleep Port 1 <ul style="list-style-type: none"> - Options available: Disabled/Enabled/Auto. Default setting is Auto.

Parameter	Description
USB Configuration Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ XHCI Controller0 Enable <ul style="list-style-type: none"> – Enable or disable USB3 controller. – Options available: Enabled/Disabled/Auto. Default setting is Auto. ◆ XHCI Controller1 Enable <ul style="list-style-type: none"> – Enable or disable USB3 controller. – Options available: Enabled/Disabled/Auto. Default setting is Auto. ◆ USB ecc SMI Enable <ul style="list-style-type: none"> – Options available: Enabled/Off/Auto. Default setting is Auto. ◆ MCM USB enable <ul style="list-style-type: none"> – Press [Enter] for advanced configurations.
SD Dump Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ SD Configuration Mode <ul style="list-style-type: none"> – Select SD Mode. – Options available: SD Dump disabled/SD Dump Enabled. Default setting is SD Dump disabled.
AC Power Loss Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ AC Loss Control <ul style="list-style-type: none"> – Select AC Loss Control Method. – Options available: Power Off/Power On/Last State. Default setting is Last State.
I2C Configuration Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ I2C 0/1/2/3/4/5 Enable <ul style="list-style-type: none"> – Enable or disable I2C 0/1/2/3/4/5. – Options available: Disabled/Enabled/Auto. Default setting is Auto.
Uart Configuration Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ Uart 0 Enable <ul style="list-style-type: none"> – Uart 0 has no HW FC if Uart 2 is enabled. – Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ Uart 1 Enable <ul style="list-style-type: none"> – Uart 1 has no HW FC if Uart 3 is enabled. – Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ Uart 2 Enable (no HW FC) <ul style="list-style-type: none"> – Options available: Disabled/Enabled/Auto. Default setting is Auto. ◆ Uart 3 Enable (no HW FC) <ul style="list-style-type: none"> – Options available: Disabled/Enabled/Auto. Default setting is Auto.
ESPI Configuration Options	<p>Press [Enter] for more options.</p> <ul style="list-style-type: none"> ◆ ESPI Enable <ul style="list-style-type: none"> – Options available: Disabled/Enabled/Auto. Default setting is Auto.

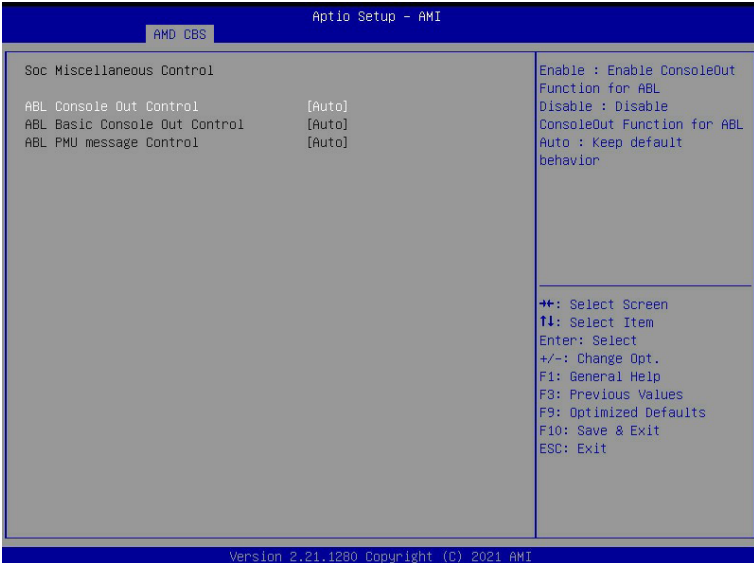
Parameter	Description
eMMC Options	<p data-bbox="370 147 610 166">Press [Enter] for more options.</p> <ul style="list-style-type: none"> <li data-bbox="370 174 951 341">◆ eMMC/SD Configure <ul style="list-style-type: none"> <li data-bbox="408 205 951 341">– Options available: Disabled/SD Normal Speed/SD High Speed/SD UHSI-SDR50/SD UHSI-DDR50/SDUHSI-SDR104/eMMC Emmc Backward Compatibility/eMMC High Speed SDR/eMMC High Speed DDR/eMMC HS200/eMMC HS400/eMMC HS300/Auto. Default setting is Auto. <li data-bbox="370 351 951 456">◆ Driver Type <ul style="list-style-type: none"> <li data-bbox="408 382 777 401">– BIOS will select MS driver for SD selections. <li data-bbox="408 409 951 456">– Options available: AMD eMMC Driver/MS Driver/Auto. Default setting is Auto. <li data-bbox="370 465 951 519">◆ D3 Cold Support <ul style="list-style-type: none"> <li data-bbox="408 497 951 519">– Options available: Disabled/Enabled/Auto. Default setting is Auto. <li data-bbox="370 528 951 577">◆ eMMC Boot <ul style="list-style-type: none"> <li data-bbox="408 553 951 577">– Options available: Disabled/Enabled/Auto. Default setting is Auto.
FCH RAS Options	<p data-bbox="370 589 610 608">Press [Enter] for more options.</p> <ul style="list-style-type: none"> <li data-bbox="370 616 951 669">◆ ALink RAS Support <ul style="list-style-type: none"> <li data-bbox="408 647 951 669">– Options available: Disabled/Enabled/Auto. Default setting is Auto. <li data-bbox="370 678 951 785">◆ Reset after sync flood <ul style="list-style-type: none"> <li data-bbox="408 710 951 757">– Enable AB to forward downstream sync-flood message to system controller. <li data-bbox="408 765 951 785">– Options available: Disabled/Enabled/Auto. Default setting is Auto.

5-3-6 NTB Common Options



Parameter	Description
NTB Common Options	
NTB Enable	Enable or disable OnChip SATA controller. Options available: Auto/Enable. Default setting is Auto .

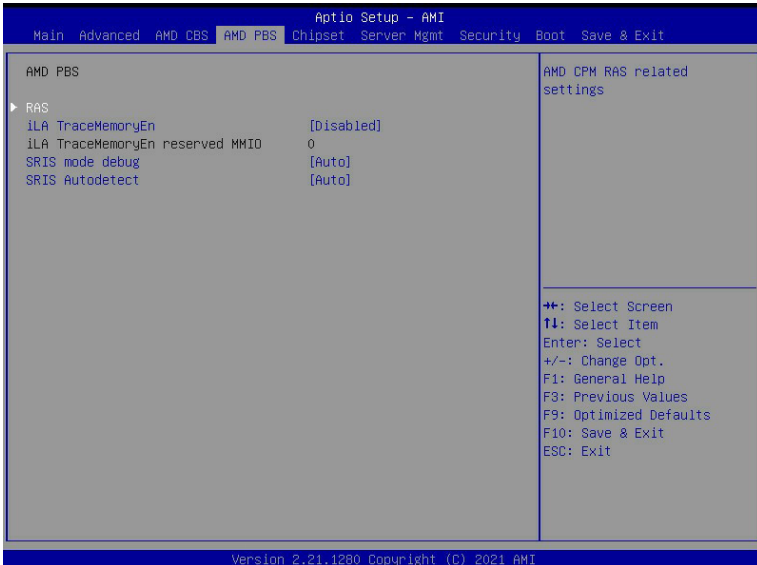
5-3-7 SOC Miscellaneous Control



Parameter	Description
Soc Miscellaneous Control	
ABL Console Out Control	Enable = Enable ConsoleOut Function for ABL Disable = Disable ConsoleOut Function for ABL Auto = Keep default behavior Options available: Disable/Enable/Auto. Default setting is Auto .

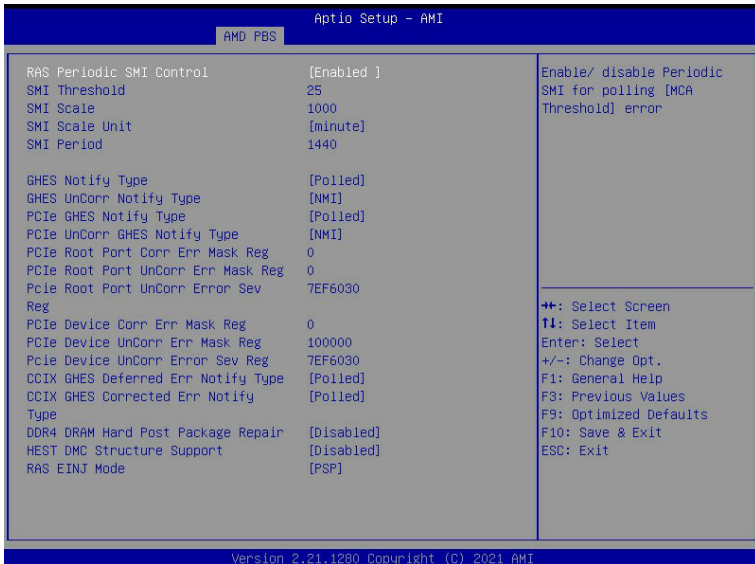
5-4 AMD PBS Option Menu

AMD PBS Option menu displays submenu options for configuring the function of AMD PBS. Select a submenu item, then press [Enter] to access the related submenu screen.



Parameter	Description
AMD PBS	
RAS	Press [Enter] for advanced configurations.
SPI Locking	Enable or disable SPI Locking for protect ROM part. Options Available: Enabled/Disabled. Default option is Disabled .

5-4-1 RAS

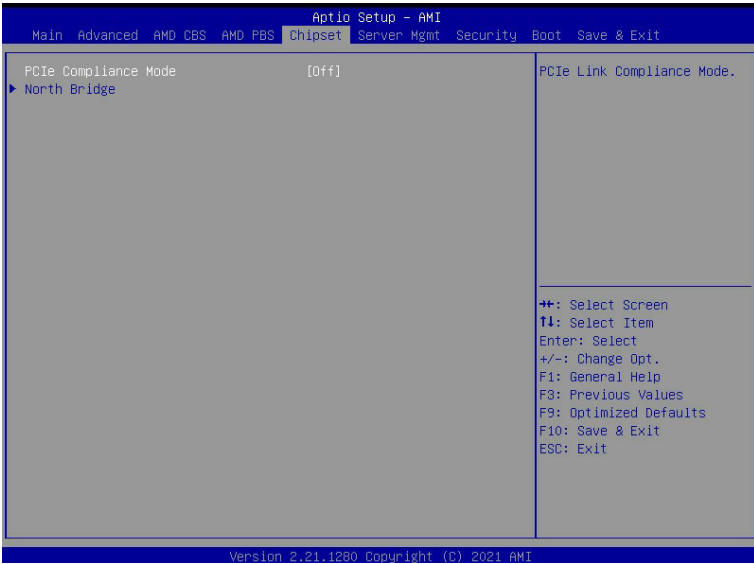


Parameter	Description
RAS Periodic SMI Control	Enable or disable Periodic SMI for polling [MCA Threshold] error. Options Available: Disabled/Enabled. Default option is Enabled .
SMI Threshold	Enter a value. Limits the number of [MCA Threshold and Deferred Error SMI source] per a unit of time (Defined by [SMI Scale]). Default value is 5 dec interrupts .
SMI Scale	Enter a value. Defines the time scale. Default value is 1000 dec .
SMI Scale Unit	Defines the unit of time scale. Options available: millisecond/second/minute. Default option is millisecond .
SMI Period	Enter a value. Defines the polling interval in milliseconds. Default option is 1000 dec . Maximum value is 32767 dec. 0 = disable.
GHEs Notify Type	Notification type for deferred/corrected errors. Options Available: Polled/SCI. Default option is Polled .
GHEs UnCorr Notify Type	Notification type for uncorrected errors. Options Available: Polled/NMI. Default option is NMI .
PCIe GHEs Notify Type	Notification type for PCIe corrected errors. Options Available: Polled/SCI. Default option is Polled .

Parameter	Description
PCIe UnCorr GHES Notify Type	Notification type for PCIe uncorrected errors. Options Available: Polled/NMI. Default option is NMI .
PCIe Root Port Corr Err Mask Reg	Enter a value. Initialize the PCIe AER Corrected Error Mask register of Root Port.
PCIe Root Port UnCorr Err Mask Reg	Enter a value. Initialize the PCIe AER Uncorrected Error Mask register of Root Port.
PCIe Root Port UnCorr Error Sev Reg	Enter a value. Initialize the PCIe AER Uncorrected Error Severity register of Root Port.
PCIe Device Corr Err Mask Reg	Enter a value. Initialize the PCIe AER Corrected Error Mask register of PCIe Device.
PCIe Device UnCorr Err Mask Reg	Enter a value. Initialize the PCIe AER Uncorrected Error Mask register of PCIe Device.
PCIe Device UnCorr Error Sev Reg	Enter a value. Initialize the PCIe AER Uncorrected Error Severity registers of PCIe Device.
CCIX GHES Deferred Err Notify Type	Notification type for CCIX deferred errors. Options Available: Polled/SCI. Default option is Polled .
CCIX GHES Corrected Err Notify Type	Notification type for CCIX corrected errors. Options Available: Polled/SCI. Default option is Polled .
DDR4 DRAM Hard Post Package Repair	This feature allows spare DRAM rows to replace malfunctioning rows via an in-field repair mechanism. Options Available: Disabled/Enabled. Default option is Disabled .
HEST DMC Structure Support	HEST DMC (Deferred Machine Check) Structure Support. Options Available: Disabled/Enabled. Default option is Disabled .

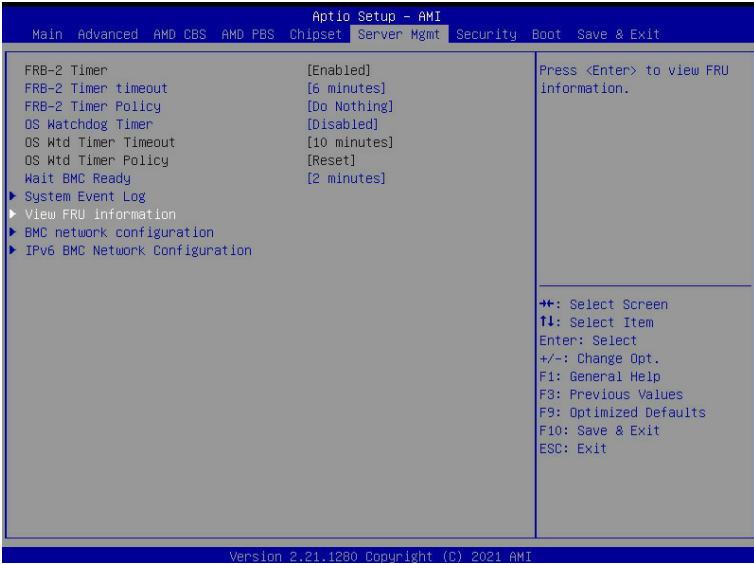
5-5 Chipset Setup Menu

Chipset Setup menu displays submenu options for configuring the function of the North Bridge. Select a submenu item, then press [Enter] to access the related submenu screen.



Parameter	Description
PCIe Link Training Type	PCIe Link training in 1 or 2 steps. Options available: 1 Step/2Step. Default setting is 1 Step .
PCIe Compliance Mode	Options available: On/Off. Default setting is Off .
Program All VR	Enables or disables program all VR on MB. Options available: Disabled/Enabled. Default setting is Enabled .
North Bridge	Press [Enter] for more information on the North Bridge.

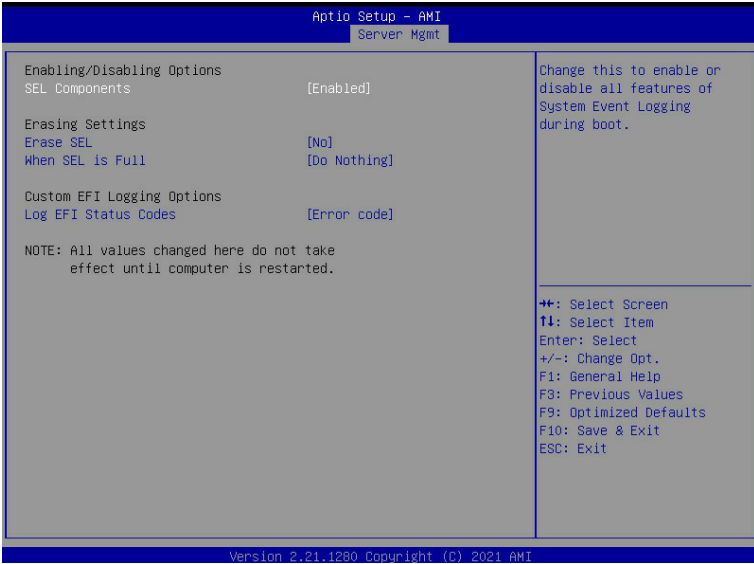
5-6 Server Management Menu



Parameter	Description
FRB-2 Timer	FRB-2 timer (POST timer).
FRB-2 Timer timeout	Configure the FRB2 Timer timeout. Options available: 3 minutes/4 minutes/5 minutes/6 minutes. Default setting is 6 minutes . (NOTE) This item is configurable when FRB-2 Timer is set to Enabled.
FRB-2 Timer Policy	Configure the FRB2 Timer policy. Options available: Do Nothing/Reset/Power Down. Default setting is Do Nothing . (NOTE) This item is configurable when FRB-2 Timer is set to Enabled.
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled/Disabled. Default setting is Disabled .
OS Wtd Timer Timeout	Configure OS Watchdog Timer. Options available: 5 minutes/10 minutes/15 minutes/20 minutes. Default setting is 10 minutes . (NOTE) This item is configurable when OS Watchdog Timer is set to Enabled.
OS Wtd Timer Policy	Configure OS Watchdog Timer Policy. Options available: Reset/Do Nothing/Power Down. Default setting is Reset . (NOTE) This item is configurable when OS Watchdog Timer is set to Enabled.
Wait BMC Ready	Configure time to wait BMC ready. Options available: Disabled/2 minutes/4 minutes/6 minutes. Default setting is 2 minutes .
System Event Log	Press [Enter] to configure advanced items.

Parameter	Description
View FRU Information	Press [Enter] to view the advanced items.
BMC network configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

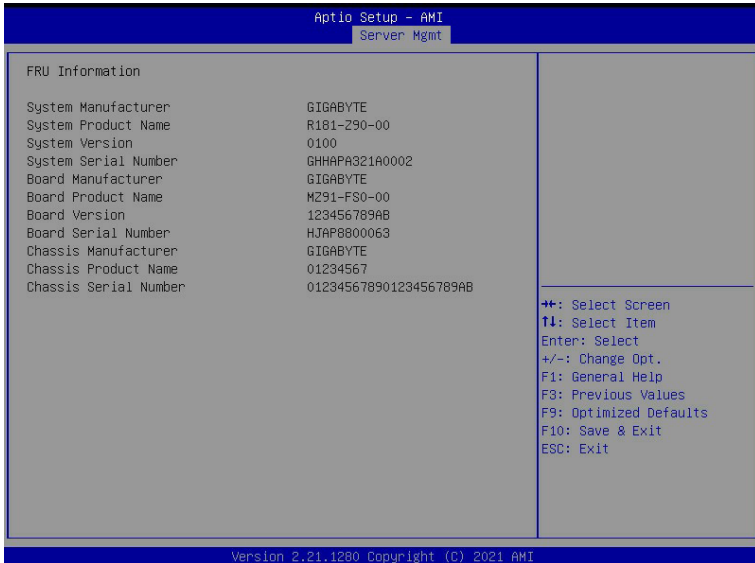
5-6-1 System Event Log



Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Enabled/Disabled. Default setting is Enabled .
Erasing Settings	
Erasing SEL	Choose options for erasing SEL. Options available: No/Yes, On next reset/Yes, On every reset. Default setting is No .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing/Erased Immediately. Default setting is Do Nothing .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled/Both/Error code/Progress code. Default setting is Error code .

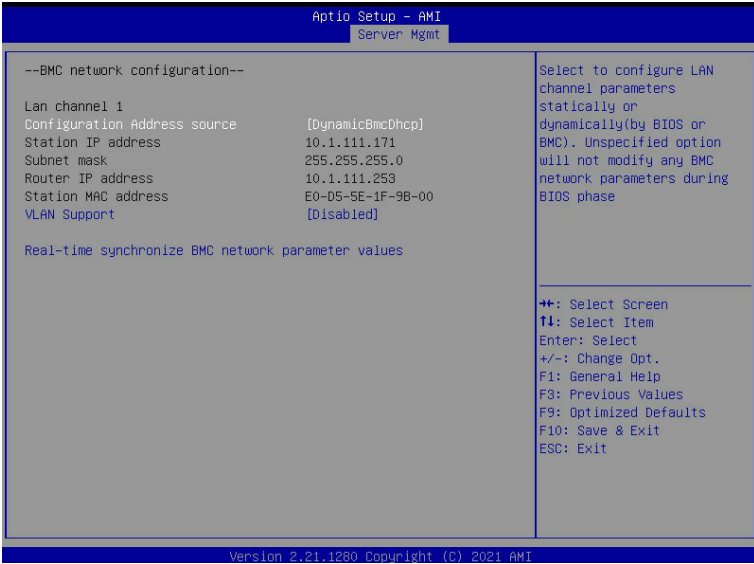
5-6-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



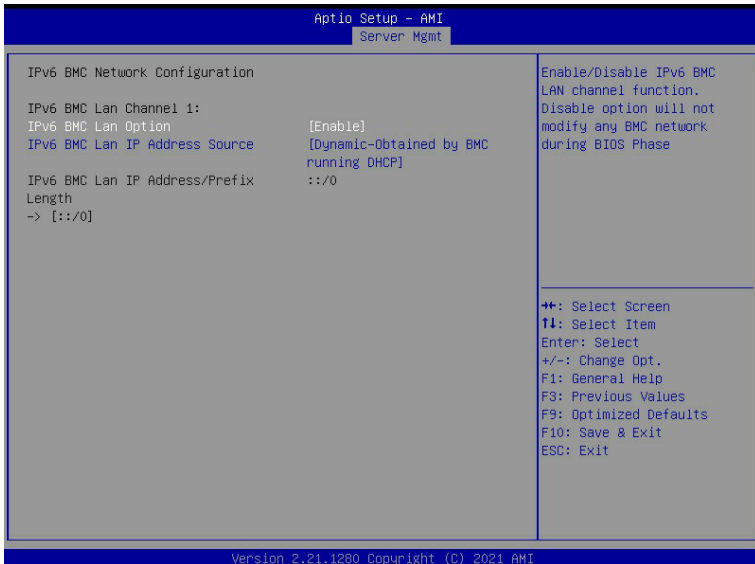
(Note) The model name will vary depends on the product you purchased.

5-6-3 BMC Network Configuration



Parameter	Description
BMC network configuration	
Lan Channel 1	
Configuration Address source	Select to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase. Options available: Unspecified/Static/DynamicBmcDhcp. Default setting is DynamicBmcDhcp .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
Real-time synchronize BMC network parameter values	Press [Enter] to synchronize the BMC network parameter values.

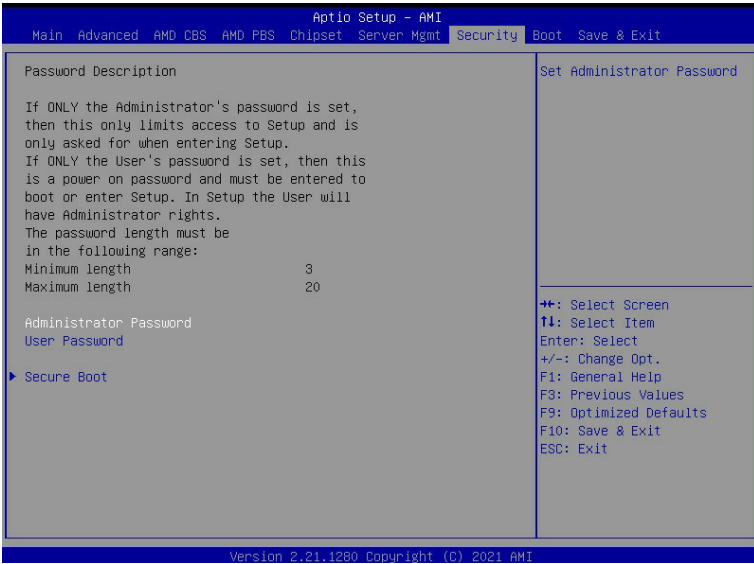
5-6-4 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC Network Configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified/Disable/Enable. Default setting is Enable .
IPv6 BMC Lan IP Address Source	Select to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified/Static/Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP .
IPv6 BMC Lan IP Address/Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

5-7 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

- **Administrator Password**
Entering this password will allow the user to access and change all settings in the Setup Utility.
- **User Password**
Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

5-7-1 Secure Boot



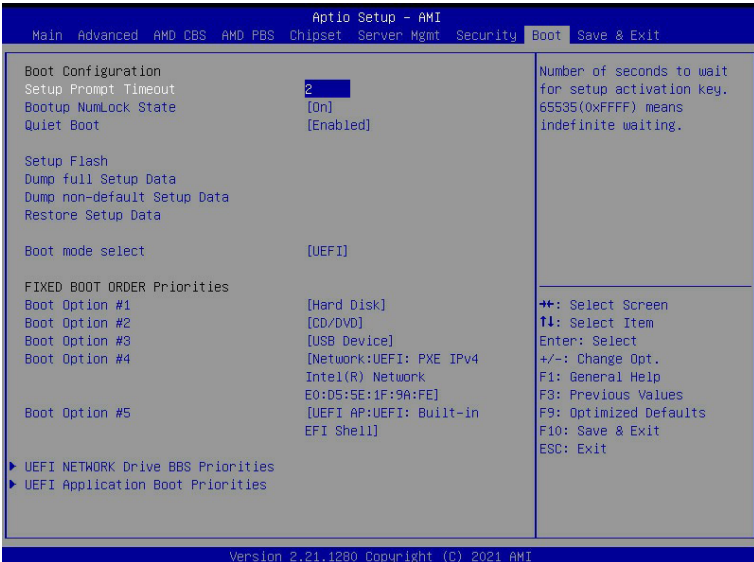
Parameter	Description
System Mode	Displays the system is in User mode or Setup mode.
Secure Boot	Enables/Disables Secure Boot. The mode change requires a platform reset. Options available: Disabled/Enabled. Default setting is Disabled .
Secure Boot Mode ^(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all the files being loaded before Windows loads and gets to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys from the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard/Custom. Default setting is Custom .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Key Management	Press [Enter] to configure advanced items. Please note that this item is configurable when Secure Boot Mode is set to Custom.

(Note) Advanced items prompt when this item is set to **Custom**.

Parameter	Description
Key Management (continued)	<ul style="list-style-type: none"> ◆ Factory Key Provision <ul style="list-style-type: none"> – Installs factory default Secure Boot keys after the platform resets and the system is in Setup Mode. – Options available: Disabled/Enabled. Default setting is Disabled. ◆ Restore Factory Keys <ul style="list-style-type: none"> – Installs factory default Secure Boot key databases. It will force the system in User Mode. – Options available: Yes/No. ◆ Enroll Efi Image <ul style="list-style-type: none"> – Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db). ◆ Restore DB defaults <ul style="list-style-type: none"> – Press [Enter] to restore DB variable to factory defaults. – Options available: Yes/No. ◆ Secure Boot variable <ul style="list-style-type: none"> – Displays the current status of the variables used for secure boot. ◆ Platform Key (PK) <ul style="list-style-type: none"> – Displays the current status of the Platform Key (PK). – Press [Enter] to configure a new PK. – Options available: Set Update. ◆ Key Exchange Keys (KEK) <ul style="list-style-type: none"> – Displays the current status of the Key Exchange Key Database (KEK). – Press [Enter] to configure a new KEK or load additional KEK from storage devices. – Options available: Set Update/Append. ◆ Authorized Signatures (DB) <ul style="list-style-type: none"> – Displays the current status of the Authorized Signature Database. – Press [Enter] to configure a new DB or load additional DB from storage devices. – Options available: Set Update/Append. ◆ Forbidden Signatures (DBX) <ul style="list-style-type: none"> – Displays the current status of the Forbidden Signature Database. – Press [Enter] to configure a new dbx or load additional dbx from storage devices. – Options available: Set Update/Append. ◆ Authorized TimeStamps (DBT) <ul style="list-style-type: none"> – Displays the current status of the Authorized TimeStamps Database. – Press [Enter] to configure a new DBT or load additional DBT from storage devices. – Options available: Set Update/Append. ◆ OsRecovery Signatures <ul style="list-style-type: none"> – Displays the current status of the OsRecovery Signature Database. – Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. – Options available: Set Update/Append.

5-8 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

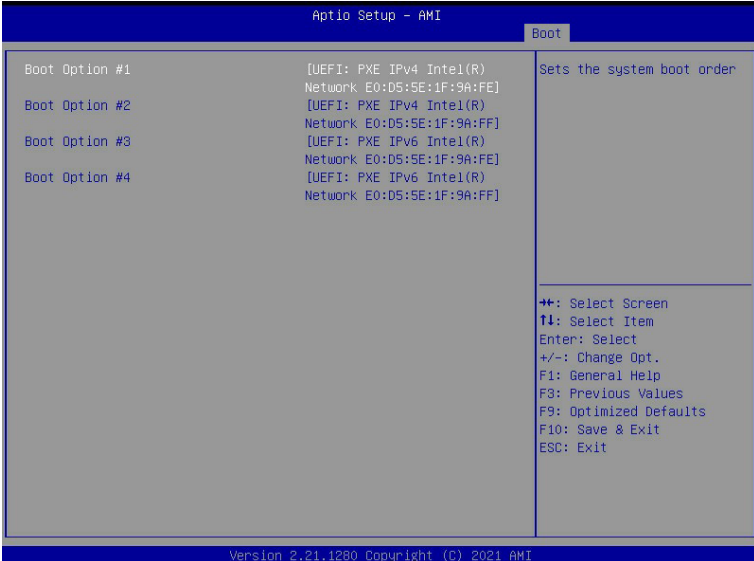


Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On/Off. Default setting is On .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Disabled/Enabled. Default setting is Enabled .
Boot mode select	Selects the boot mode. Options available: LEGACY/UEFI. Default setting is UEFI .
FIXED BOOT ORDER Priorities	
Boot Option #1 / #2 / #3 / #4 / #5	Press [Enter] to configure the boot priority. By default, the server searches for boot devices in the following sequence: <ol style="list-style-type: none"> 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.

Parameter	Description
UEFI Network Drive BBS Priorities	Press [Enter] to configure the boot priority.
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.

5-8-1 UEFI NETWORK Drive BBS Priorities

The UEFI network drive BBS priorities submenu allows you to specify the boot device priority from the available UEFI network drives during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.



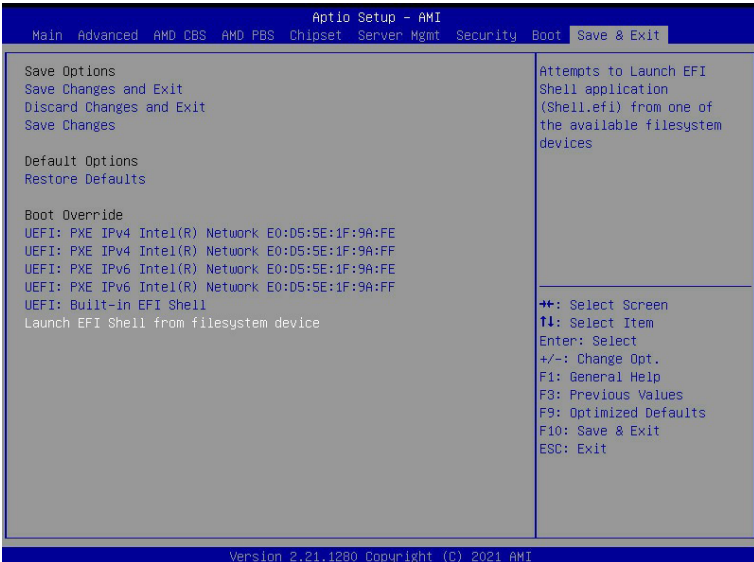
5-8-2 UEFI Application Boot Priorities

The UEFI application boot priorities submenu allows you to specify the boot device priority from the available UEFI applications during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.



5-9 Save & Exit Menu

The Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press [Enter].



Parameter	Description
Save Options	
Save Changes and Exit	Saves changes made and closes the BIOS setup. Options available: Yes/No.
Discard Changes and Exit	Discards changes made and exits the BIOS setup. Options available: Yes/No.
Save Changes	Save changes done so far to any of the setup options. Options available: Yes/No.
Default Options	
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes/No.
Boot Override	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell from filesystem device	Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices.

5-10 BIOS POST Beep code (AMI standard)

5-10-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXE IPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

5-10-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met