



A2SDi-TP8F/LN4F  
A2SDi-16C-TP8F

USER'S MANUAL

Revision 1.0a

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Manual Revision 1.0a

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# Preface

## About This Manual

This manual is written for system integrators, IT technicians and knowledgeable end users. It provides information for the installation and use of the A2SDi-TP8F/LN4F, A2SDi-16C-TP8F motherboard.

## About This Motherboard

The Supermicro A2SDi-TP8F/LN4F, A2SDi-16C-TP8F motherboard comes with an integrated Intel® Atom SoC (system-on-a-chip) that has up to 16 Cores and is optimized for low-power consumption. The integrated memory controller supports up to 64GB DDR4 ECC/Non-ECC Unbuffered SO-DIMM with a max data rate of 2400MHz. This motherboard also provides support for USB 3.0, 16 lanes of PCI Express 3.0 connectivity and 10GbE Ethernet. The A2SDi-TP8F/LN4F motherboard is an excellent choice for microservers. Please note that this motherboard is intended to be installed and serviced by professional technicians only.

For processor/memory updates, please refer to our website at <http://www.supermicro.com/products/>.

## Conventions Used in the Manual

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:



**Warning!** Indicates important information given to prevent equipment/property damage or personal injury.



**Warning!** Indicates high voltage may be encountered when performing a procedure.



**Important:** Important information given to ensure proper system installation or to relay safety precautions.



**Note:** Additional Information given to differentiate various models or provides information for correct system setup.

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# Chapter 1

## Introduction

Congratulations on purchasing your computer motherboard from an industry leader. Supermicro boards are designed to provide you with the highest standards in quality and performance.

In addition to the motherboard, several important parts that are included with the system are listed below. If anything listed is damaged or missing, please contact your retailer.

### 1.1 Checklist

Main Parts List		
Description	Part Number	Quantity
Supermicro Motherboard	A2SDi-TP8F/LN4F, A2SDi-16C-TP8F	1
SATA Cables	CBL-0044L	4
I/O Shield	MCP-260-00098-0N	1

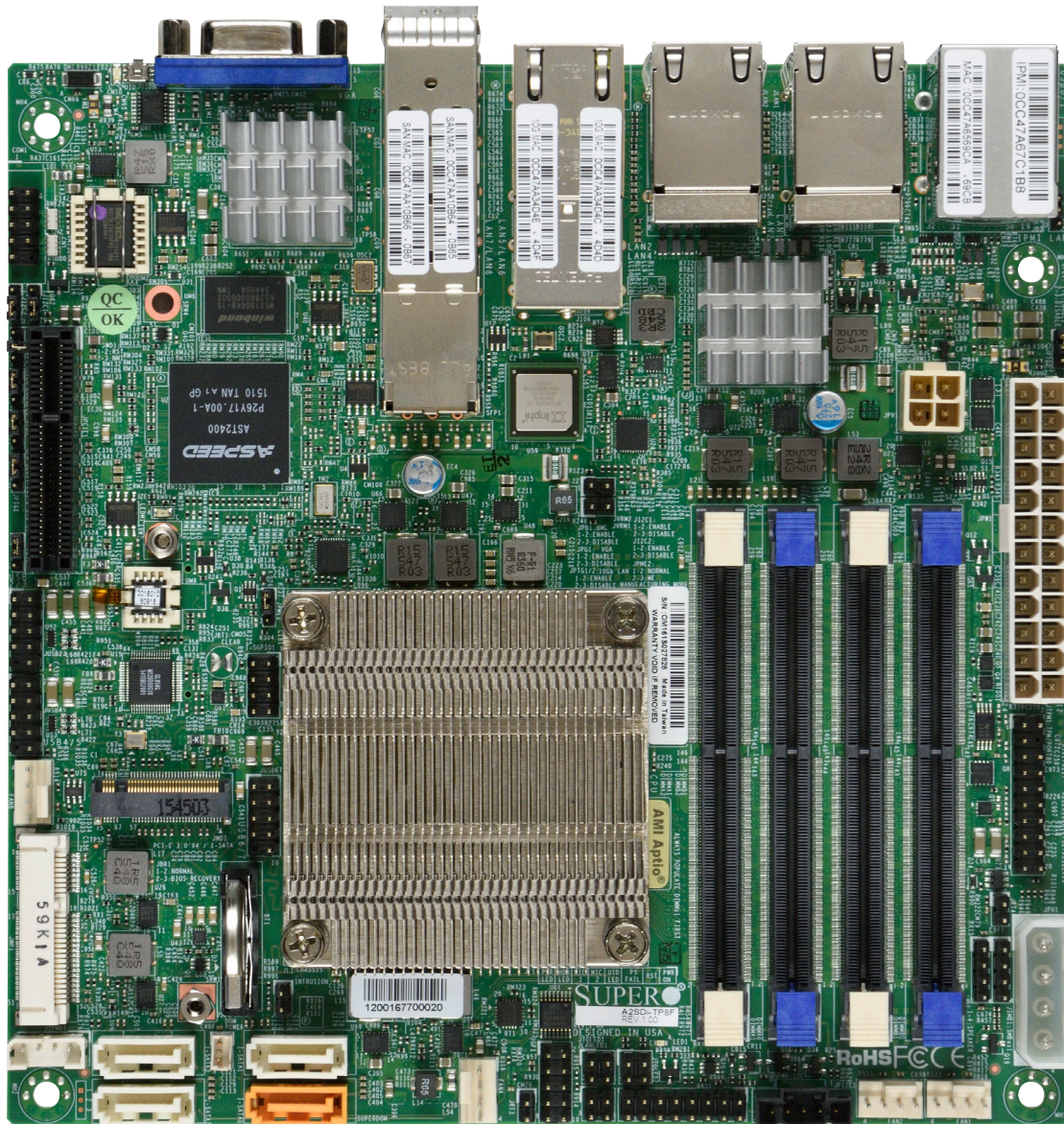
### Important Links

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your server.

- Supermicro product manuals: <http://www.supermicro.com/support/manuals/>
- Product drivers and utilities: <https://www.supermicro.com/wftp/driver/>
- Product safety info: [http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm)
- If you have any questions, please contact our support team at: [support@supermicro.com](mailto:support@supermicro.com)

This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.

Figure 1-1. A2SDi-TP8F Motherboard Image



**Note:** All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.



Figure 1-2. A2SDi-LN4F Motherboard Image

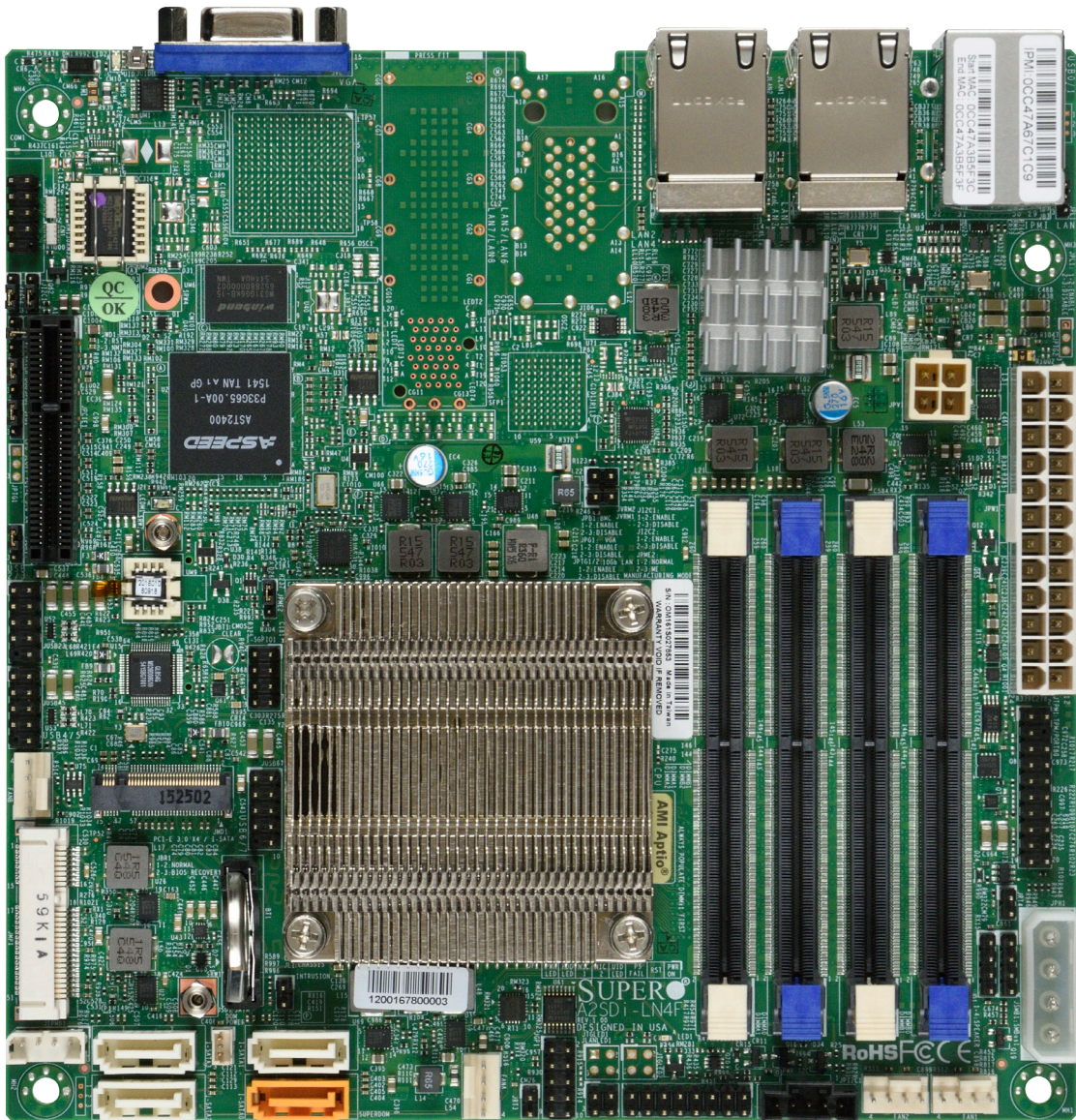
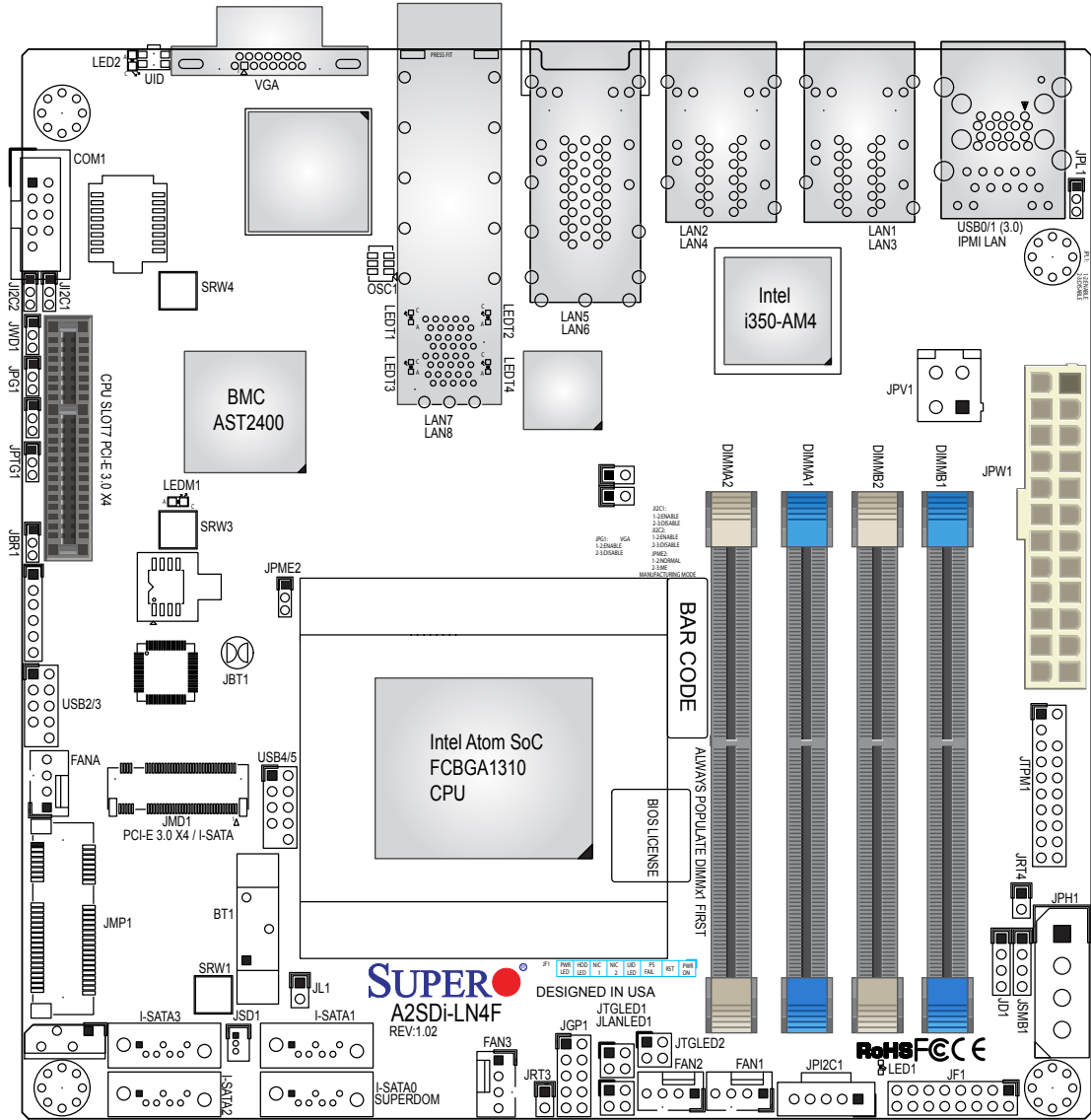

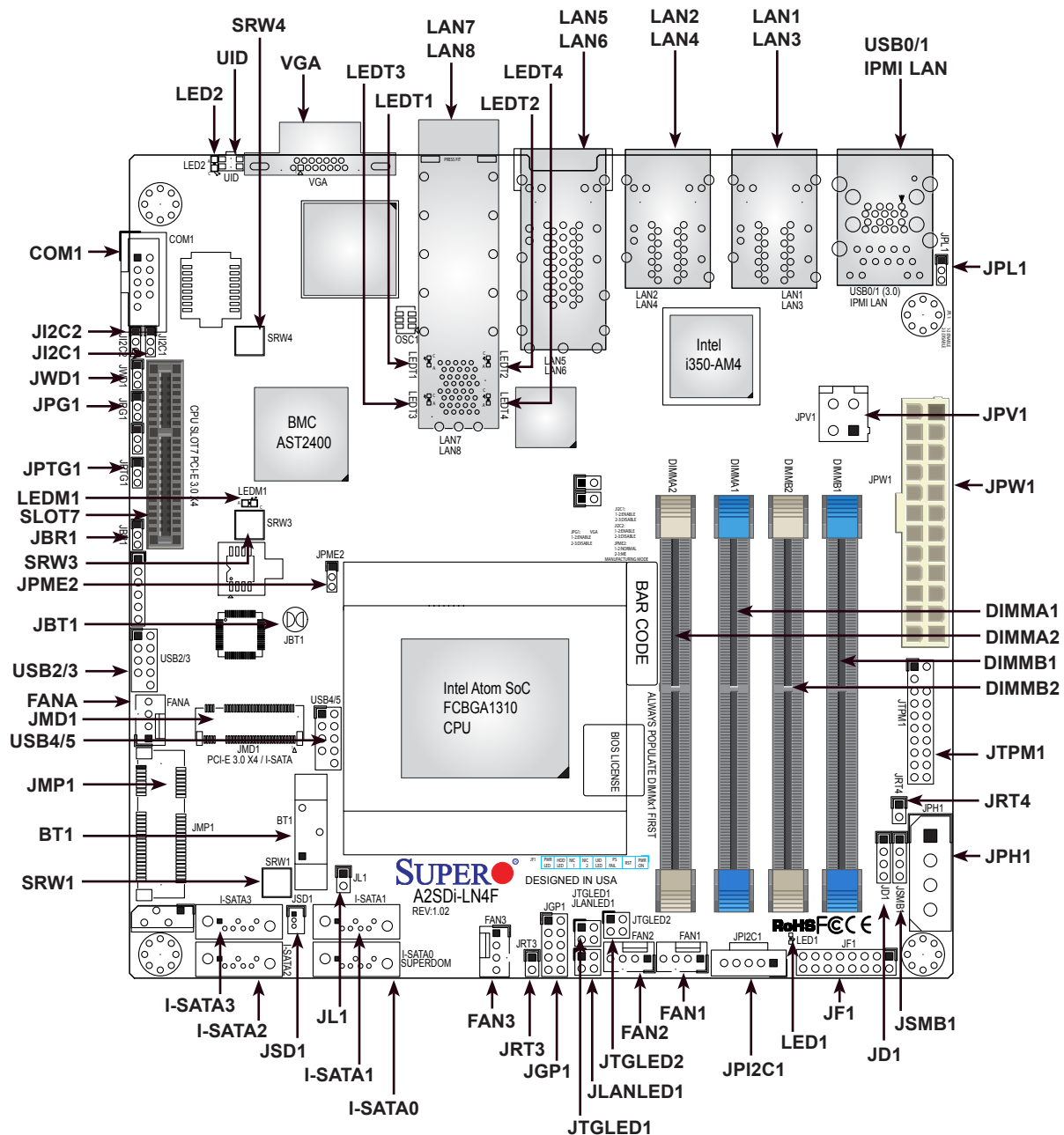


Figure 1-3. A2SDi-TP8F/LN4F Motherboard Layout  
(not drawn to scale)



 **Note:** Components not documented are for internal testing only.

## Quick Reference



### Notes:

- See Chapter 2 for detailed information on jumpers, I/O ports, and JF1 front panel connections.
- "■" indicates the location of Pin 1.
- Jumpers/LED indicators not indicated are used for testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.



## Quick Reference Table

Jumper	Description	Default Setting
JBR1	BIOS Recovery	Pins 1-2 (Normal)
JBT1	CMOS Clear	Open (Normal)
JI <sup>2</sup> C1, JI <sup>2</sup> C2	SMB to PCI-E Slots Enable/Disable	Both Pins 2-3 (Disabled)
JPG1	VGA Enable/Disable	Pins 1-2 (Enabled)
JPL1	LAN1/2/3/4 Enable/Disable	Pins 1-2 (Enabled)
JPME2	Manufacturing Mode	Pins 1-2 (Normal)
JPTG1	10Gb LAN Enable/Disable	Pins 1-2 (Enabled)
JWD1	Watch Dog	Pins 1-2 (Reset)

LED	Description	Status
LED1	Onboard Power LED	Solid Green: Power On
LED2	UID LED	Solid Blue: Unit Identified
LEDM1	BMC Heartbeat LED	Blinking Green: BMC Normal
LEDT1	LAN8 Activity	Blinking Green: LAN8 Active
LEDT2	LAN7 Link Status	Solid Green: LAN7 Normal
LEDT3	LAN7 Activity	Blinking Green: LAN7 Active
LEDT4	LAN8 Link Status	Solid Green: LAN8 Normal

Connector	Description
BT1	Onboard Battery
COM1	COM Header
FAN1, FAN2, FAN3, FANA	System/CPU Fan Headers (FAN1: CPU Fan)
IPMI_LAN	Dedicated IPMI LAN Port
I-SATA0 ~ I-SATA3	Intel® PCH SATA 3.0 Ports (I-SATA0 is SuperDOM)
JD1	Speaker Header
JF1	Front Control Panel Header
JGP1	General Purpose I/O Header
JL1	Chassis Intrusion Header
JLANLED1	LAN3/LAN4 Activity LED Header
JMD1	M.2 PCI-E 3.0 x4 / I-SATA4
JMP1	Mini PCI-E 2.0 x1 / I-SATA5 Slot
JPH1	4-pin Power Connector for HDD use (To provide power from the motherboard to onboard HDD devices)
JPI <sup>2</sup> C1	Power Supply SMBbus I <sup>2</sup> C Header
JPV1	12V DC Power Connector
JPW1	24-pin ATX Power Connector
JRT3	Thermal Diode 1 Header
JRT4	Thermal Diode 2 Header



**Note:** Table is continued on the next page.

<b>Connector</b>	<b>Description</b>
JSD1	SATA DOM Power Connector
JSMB1	PCH SMBus Header
JTGLED1	LAN5/LAN6 Activity LED Header
JTGLED2	LAN7/LAN8 Activity LED Header
JTPM1	Trusted Platform Module/Port 80 Connector
LAN1 ~ LAN8	LAN1 - LAN4: GbE LAN (RJ45) Ports LAN5 - LAN6: 10GBaseT LAN (RJ45) Ports LAN7 - LAN8: 10G LAN (SFP+) Ports (LAN5 - LAN8 not available on A2SDi-LN4F)
SLOT7	CPU PCI-E 3.0 x4 Slot
SRW1, SRW3, SRW4	M.2 Holding Screws
UID	Unit ID Button
USB0/1	Back panel Universal Serial Bus (USB) 3.0 Ports
USB2/3, USB4/5	Front Accessible USB 2.0 Headers
VGA	VGA Port

## Motherboard Features

<b>Motherboard Features</b>	
<b>CPU</b>	
<ul style="list-style-type: none"> <li>Intel® Atom SoC C3958/C3858/C3850 FCBGA1310 Processor</li> </ul>	
<b>Memory</b>	
<ul style="list-style-type: none"> <li>Up to 64GB DDR4 ECC/Non-ECC Unbuffered SO-DIMM, two DPC (DIMM Per Channel) up to 2400MT/s</li> </ul>	
<b>DIMM Size</b>	
<ul style="list-style-type: none"> <li>Up to 16GB at 1.2V</li> </ul>	
<b>Expansion Slots</b>	
<ul style="list-style-type: none"> <li>One (1) PCI Express 3.0 x4</li> <li>One (1) Mini PCI Express x1</li> <li>One M.2 3.0 x4</li> </ul>	
<b>Network</b>	
<ul style="list-style-type: none"> <li>Intel i350-AM4 Base-T NIC</li> <li>Intel X557-AT2 Base-T PHY</li> <li>CS4227 SFP+ PHY</li> </ul>	
<b>BaseBoard Management Controller (BMC)</b>	
<ul style="list-style-type: none"> <li>ASpeed AST 2400 Baseboard Controller (BMC) supports IPMI 2.0</li> <li>One (1) Dedicated IPMI LAN port located on the rear IO back panel</li> </ul>	
<b>Graphics</b>	
<ul style="list-style-type: none"> <li>Graphics controller via ASpeed 2400 BMC</li> </ul>	
<b>I/O Devices</b>	
<ul style="list-style-type: none"> <li>Serial (COM) Port</li> <li>SATA 3.0 Ports</li> </ul>	<ul style="list-style-type: none"> <li>One (1) serial header</li> <li>Four (4) SATA 3.0 ports (I-SAT0 ~ I-SATA3) I-SATA0 is SuperDOM</li> </ul>
<b>Peripheral Devices</b>	
<ul style="list-style-type: none"> <li>Two (2) USB 3.0 ports on the rear I/O panel (USB0/1)</li> <li>Two (2) internal USB 2.0 headers with two (2) USB connections each for front access (USB2/3, USB4/5)</li> </ul>	
<b>BIOS</b>	
<ul style="list-style-type: none"> <li>128 Mb SPI AMI BIOS® SM Flash UEFI BIOS</li> <li>ACPI 4.0, USB keyboard, Plug-and-Play (PnP), SPI quad speed support, Riser card auto detection support, RTC (Real Time Clock) wakeup, Dual Boot Block support, and SMBIOS 2.7</li> </ul>	



**Note:** The table above is continued on the next page.

## Motherboard Features

### Power Management

- Main switch override mechanism
- Power-on mode for AC power recovery
- ACPI power management
- S5 support
- Wake-on-Ring, Wake-on-LAN
- Management Engine (ME)

### System Health Monitoring

- Onboard voltage monitoring for +3.3V, 3.3V Standby, +5V, +5V Standby, +12V, VBAT, Memory, System Temp., Memory Temp.
- CPU/system overheat LED and control
- CPU Thermal Trip support
- Status monitor for speed control
- Status monitor for on/off control

### Fan Control

- Fan status monitoring via IPMI connections
- Dual cooling zone
- Low-noise fan speed control
- Pulse Width Modulation (PWM) fan control

### System Management


- Trusted Platform Module (TPM) support
- PECI (Platform Environment Control Interface) 2.0 support
- UID (Unit Identification)/Remote UID
- System resource alert via SuperDoctor® 5
- SuperDoctor® 5, Watch Dog, NMI
- Chassis intrusion header and detection

### LED Indicators

- CPU/Overheating
- Power/Suspend-state indicator LED
- Fan Failure
- UID/Remote UID.
- HDD activity. LAN activity.

### Dimensions

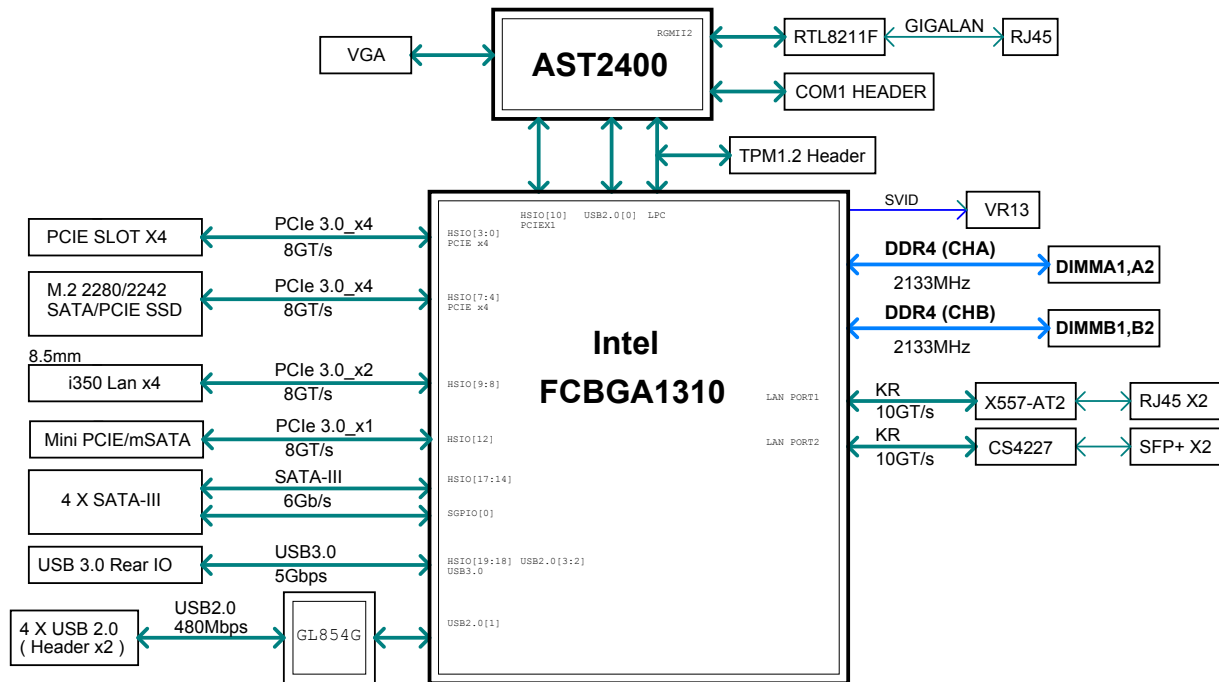
- 6.7" (L) x 6.7" (W) (170.18 mm x 170.18 mm)


 **Note 1:** The CPU maximum thermal design power (TDP) is subject to chassis and heatsink cooling restrictions. For proper thermal management, please check the chassis and heatsink specifications for proper CPU TDP sizing.

**Note 2:** For IPMI configuration instructions, please refer to the Embedded IPMI Configuration User's Guide available at <http://www.supermicro.com/support/manuals/>.

**Note 3:** It is strongly recommended that you change BMC log-in information upon initial system power-on. The manufacture default username is ADMIN and the password is ADMIN. For proper BMC configuration, please refer to [https://www.supermicro.com/products/nfo/files/IPMI/Best\\_Practices\\_BMC\\_Security.pdf](https://www.supermicro.com/products/nfo/files/IPMI/Best_Practices_BMC_Security.pdf)

**Figure 1-4.**  
**System Block Diagram**



 **Note:** This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.

## 1.2 Processor and Chipset Overview

Built upon the functionality and capability of the Intel® Atom SoC C3000 series processor, the A2SDi-TP8F/LN4F, A2SDi-16C-TP8F motherboard offers maximum I/O expandability, energy efficiency, and data reliability in a 14-nm process architecture, and is optimized for embedded storage and microserver solutions

The A2SDi-TP8F/LN4F, A2SDi-16C-TP8F supports the following features:

- Intel QuickAssist Technology (TP8F and 16C-TP8F)
- Intel Manageability Engine
- Intel Turbo Boost Technology (LN4F only)
- Configurable TDP (cTDP) and Lower-Power Mode
- Adaptive Thermal Management/Monitoring
- PCI-E 3.0 Connectivity
- 10GbE Ethernet
- System Management Bus (SMBus) Specification, Version 2.0
- Intel Virtualization Technology for Directed I/O (Intel VT-x and VT-d)

## 1.3 Special Features

This section describes the health monitoring features of the A2SDi-TP8F/LN4F, A2SDi-16C-TP8F motherboard. The motherboard has an onboard System Hardware Monitor chip that supports system health monitoring.

### **Recovery from AC Power Loss**

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is Last State.

## 1.4 System Health Monitoring

This section describes the health monitoring features of the A2SDi-TP8F/LN4F, A2SDi-16C-TP8F motherboard. The motherboard has an onboard Baseboard Management Controller (BMC) chip that supports system health monitoring. Once a voltage becomes unstable, a warning is given or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

### Onboard Voltage Monitors

The onboard voltage monitor will continuously scan crucial voltage levels. Once a voltage becomes unstable, it will give a warning or send an error message to the screen. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor. Real time readings of these voltage levels are all displayed in BIOS.

### Fan Status Monitor with Firmware Control

The system health monitor embedded in the BMC chip can check the RPM status of the cooling fans. The CPU and chassis fans are controlled via IPMI.

### Environmental Temperature Control

System Health sensors in the BMC monitor the temperatures and voltage settings of onboard processors and the system in real time via the IPMI interface. Whenever the temperature of the CPU or the system exceeds a user-defined threshold, system/CPU cooling fans will be turned on to prevent the CPU or the system from overheating.



**Note:** To avoid possible system overheating, please be sure to provide adequate air-flow to your system.

### System Resource Alert

This feature is available when used with SuperDoctor 5®. SuperDoctor 5 is used to notify the user of certain system events. For example, you can configure SuperDoctor 5 to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond a predefined range.

## 1.5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a computer system including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard disk drives and printers.



In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with Windows 8/R2, and Windows 2012/R2 operating systems.

## 1.6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates. In areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges. For the A2SDi-TP8F and A2SDi-16C-TP8F motherboard, please use a power supply that supports 2.5A or above on 5V standby.

## 1.7 Super I/O

The Super I/O (Aspeed AST2400 chip) includes a data separator, write pre-compensation circuitry, decode logic, data rate selection, a clock generator, drive interface control logic and interrupt and DMA logic. The wide range of functions integrated onto the Super I/O greatly reduces the number of components required for interfacing with floppy disk drives.

The Super I/O provides two high-speed, 16550 compatible serial communication ports (UARTs), one of which supports serial infrared communication. Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through a SMI or SCI function pin. It also features auto power management to reduce power consumption.

The IRQs, DMAs and I/O space resources of the Super I/O can be flexibly adjusted to meet ISA PnP requirements, which support ACPI and APM (Advanced Power Management).

## 1.8 Advanced Power Management

The following new advanced power management feature is supported by the motherboard.

### **Management Engine (ME)**

The Management Engine, which is an ARC controller embedded in the IOH (I/O Hub), provides Server Platform Services (SPS) to your system. The services provided by SPS are different from those provided by the ME on client platforms.

# Chapter 2

## Installation

### 2.1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your motherboard, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

#### Precautions

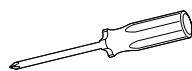
- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of CMOS onboard battery as specified by the manufacturer. Do not install the CMOS battery upside down, which may result in a possible explosion.

#### Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static protected.

## 2.2 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly.



Phillips Screwdriver (1)

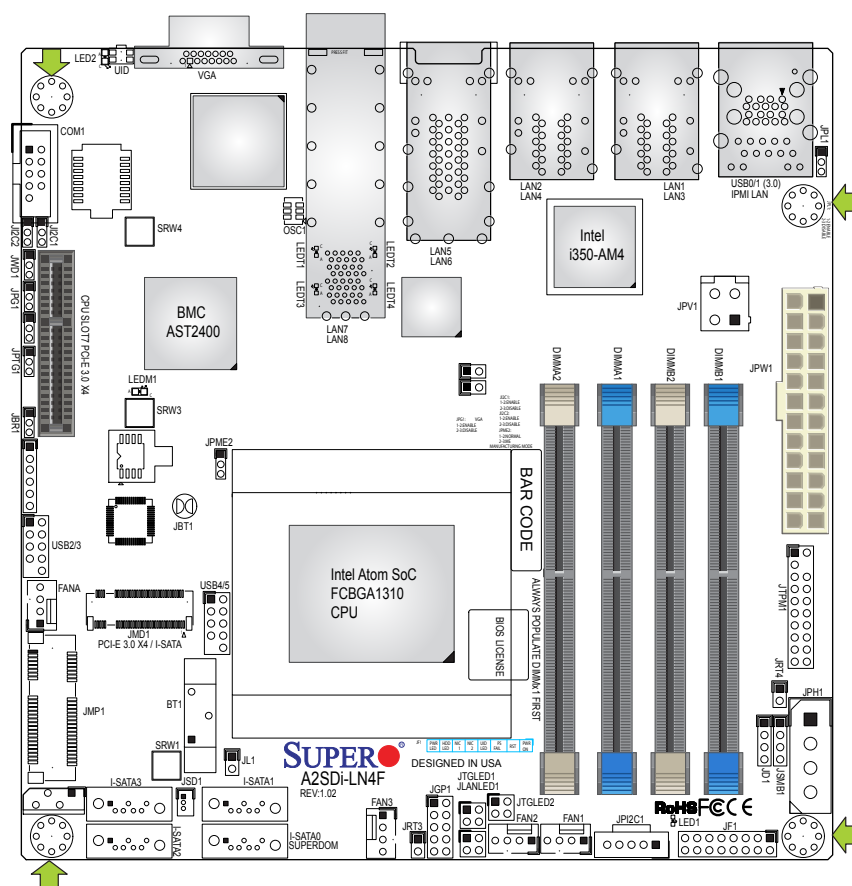


Phillips Screws (4)



Standoffs (4)  
Only if Needed

### Tools Needed



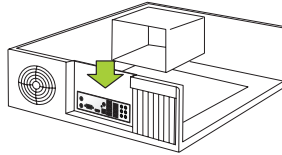
### Location of Mounting Holes



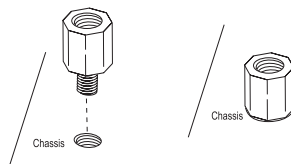
**Note:** 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

## Installing the Motherboard

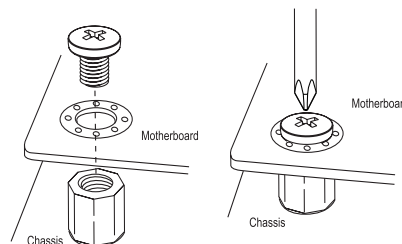
1. Install the I/O shield into the back of the chassis.




2. Locate the mounting holes on the motherboard. See the previous page for the location.



3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.



4. Install standoffs in the chassis as needed.
5. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.
6. Using the Phillips screwdriver, insert a Phillips head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

 **Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

## 2.4 Memory Support and Installation



**Note:** Check the Supermicro website for recommended memory modules.



**Important:** Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

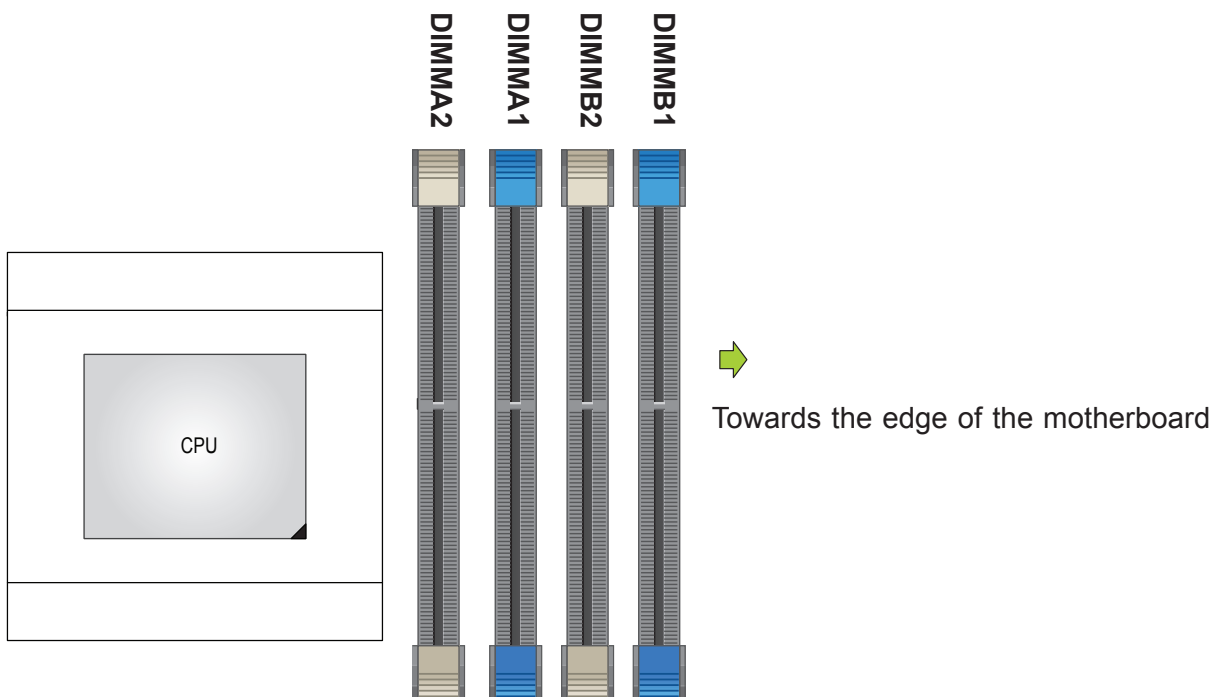
### Memory Support

The A2SDi-TP8F/A2SDi-LN4F, A2SDi-16C-TP8F supports up to 64GB DDR4 ECC/Non-ECC Unbuffered SO-DIMM, two DPC (DIMM Per Channel) up to 2400MT/s. Populating these SO-DIMM slots with a pair of memory modules of the same type and size will result in interleaved memory, which will improve memory performance.

### Memory Population Guidelines

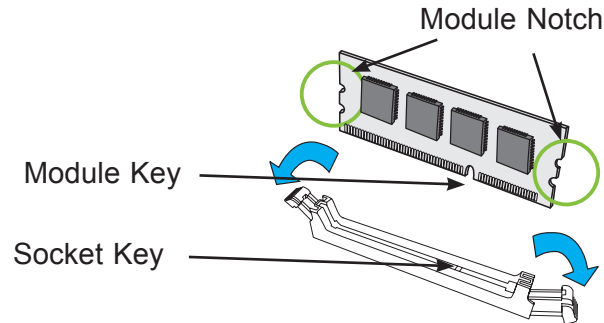
For optimal memory performance, follow the table below when populating memory. Populate DIMM slots in the following order: DIMMA1, DIMMB1, then DIMMA2, DIMMB2

Unbuffered DDR4 ECC/Non-ECC SO-DIMM Memory				
DIMMs per channel	DIMMs per channel	DIMM Type	POR Speed	Memory Population Sequence
2	1	Unbuffered DDR4 SO-DIMM	1600, 1866 2133, 2400MT/s	A1, B1 (2 DIMMs)
2	2	Unbuffered DDR4 SO-DIMM	1600, 1866 2133, 2400MT/s	A1, B1, A2, B2 (4 DIMMs)

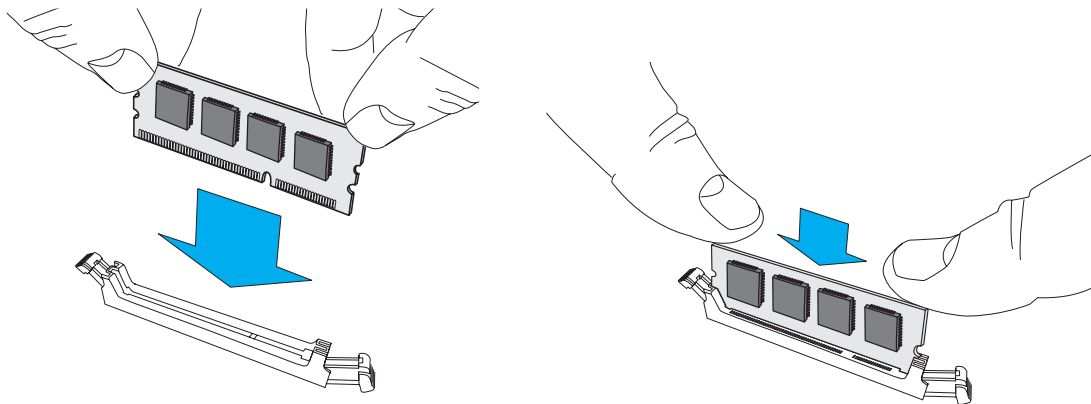


## DIMM Installation

1. Install the desired number of SODIMMs into the memory slots, starting with DIMMA1, DIMMB1, then DIMMA2, DIMMB2.
2. Align the key on the bottom of the SODIMM module against the receptive point on the memory slot. Take note of the notches on the side of the DIMM module and of the locking clips on the socket to avoid causing damage.



3. Press the SODIMM module straight down into the socket with both hands until it is securely seated in the socket. The side clips will automatically lock the module into place.



## DIMM Removal

Use your thumbs to push the side clips away from the module to release it from the socket.

## 2.5 Rear I/O Ports

See Figure 2-2 below for the locations and descriptions of the various I/O ports on the rear of the motherboard.

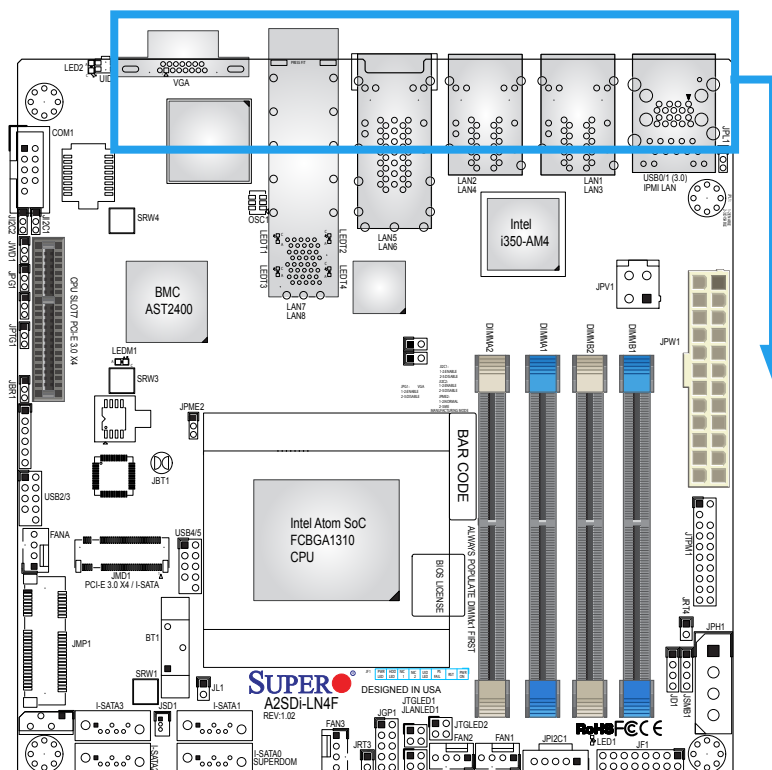
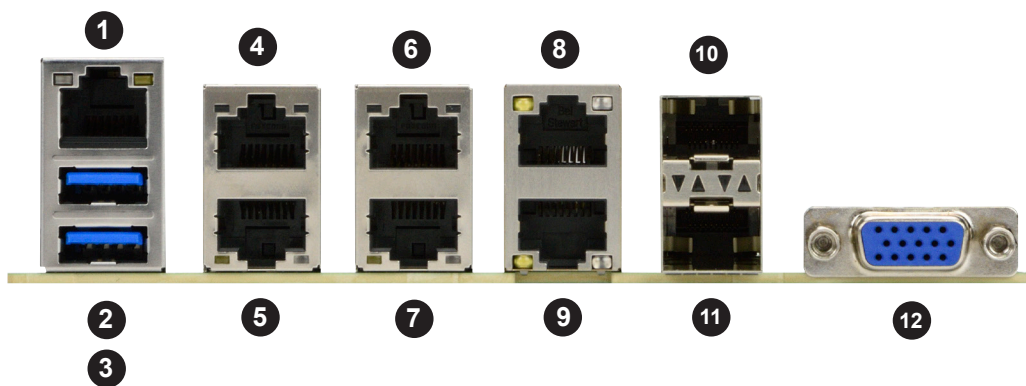


Figure 2-2. I/O Port Locations and Definitions



Rear I/O Ports					
#	Description	#	Description	#	Description
1.	IPMI LAN Port	5.	LAN1	9.	LAN5 (on TP8F)
2.	USB1	6.	LAN4	10.	LAN8 (on TP8F)
3.	USB0	7.	LAN2	11.	LAN7 (on TP8F)
4.	LAN3	8.	LAN6 (on TP8F)	12.	VGA

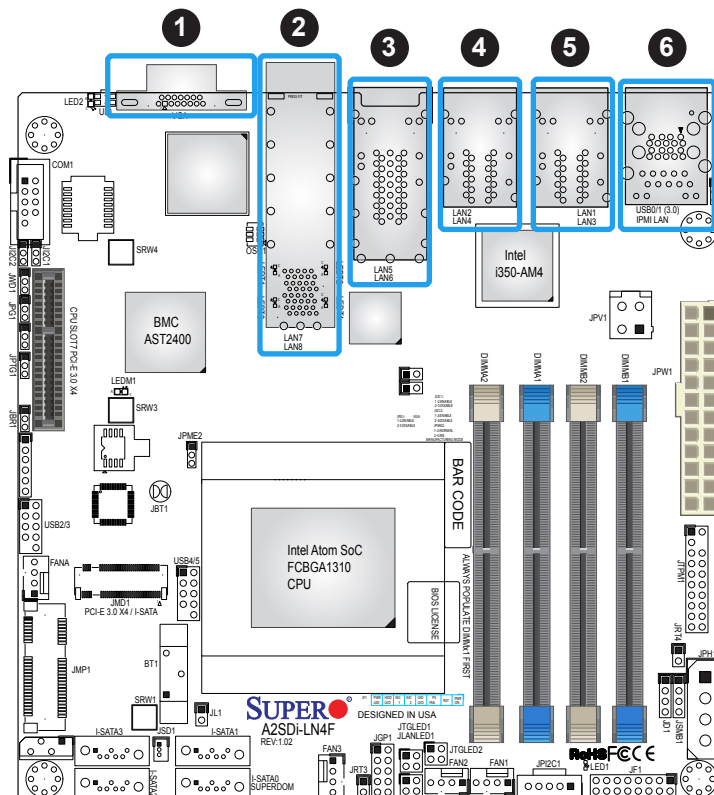


## VGA Port

The onboard VGA port is located next to LAN Ports 7/8 on the I/O back panel. Use this connection for VGA display.

## LAN Ports

The A2SDi-TP8F and ASDi-16C-TP8F has eight LAN ports, and the A2SDi-LN4F has four. These ports are located on the I/O back panel and accept RJ45 cables. There is also a dedicated IPMI LAN port on the I/O back panel.



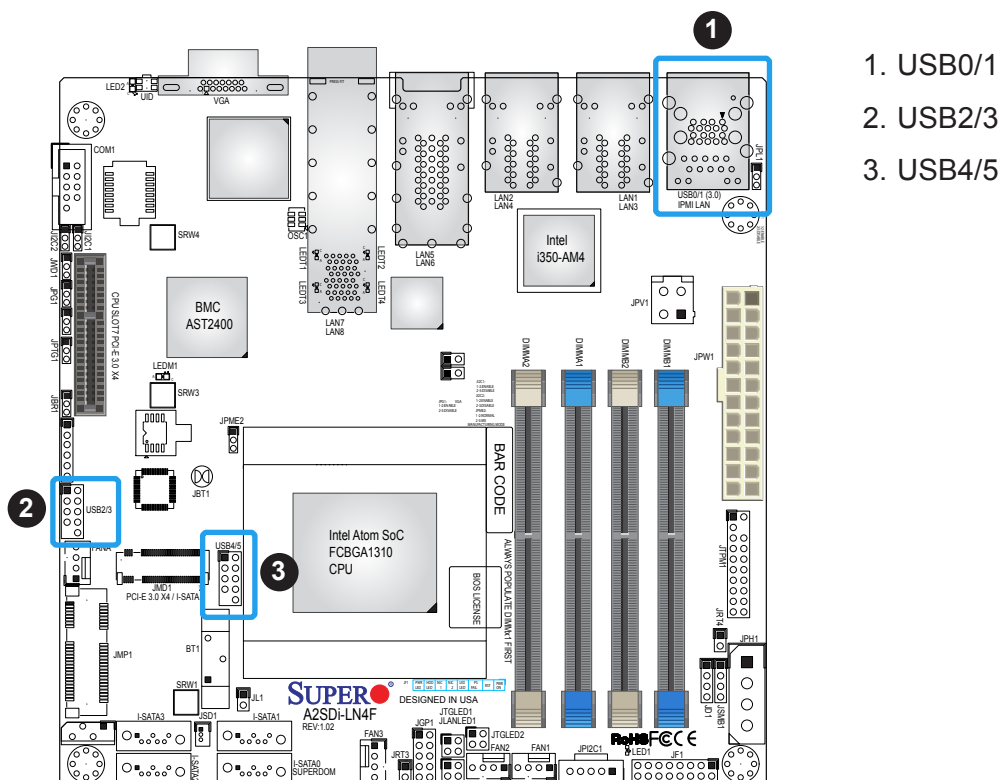
1. VGA Port
2. LAN7/8 (-TP8F only)
3. LAN5/6 (-TP8F only)
4. LAN2/4
5. LAN1/3
6. IPMI LAN

## Universal Serial Bus (USB) Ports

There are two USB 3.0 ports (USB0/1) on the I/O back panel. The motherboard also has two front access USB 2.0 headers (USB2/3 and USB4/5). The onboard headers can be used to provide front side USB access with a cable (not included).

Back Panel USB (3.0) Pin Definitions			
Pin#	Definition	Pin#	Definition
1	VBUS	10	Power
2	D-	11	USB 2.0 Differential Pair
3	D+	12	
4	Ground	13	Ground of PWR Return
5	StdA_SSRX-	14	SuperSpeed Receiver
6	StdA_SSRX+	15	Differential Pair
7	GND_DRAIN	16	Ground for Signal Return
8	StdA_SSTX-	17	SuperSpeed Transmitter
9	StdA_SSTX+	18	Differential Pair

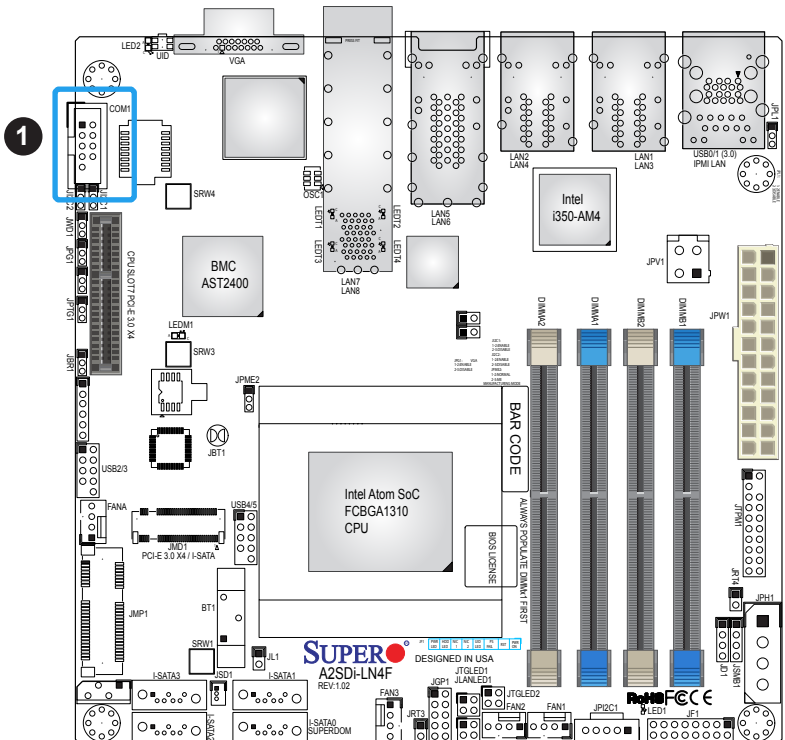
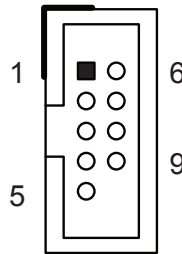
Front Panel USB 2.0 Header Pin Definitions			
Pin#	Definition	Pin#	Definition
1	+5V	2	+5V
3	USB_PN2	4	USB_PN3
5	USB_PP2	6	USB_PP3
7	Ground	8	Ground
9	Key	10	Ground



## COM Header

There is one COM header (COM1) on the motherboard.

COM Header Header Pin Definitions			
Pin#	Definition	Pin#	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	N/A



1. COM1

## 2.6 Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro chassis. See the figure below for the descriptions of the front control panel buttons and LED indicators.

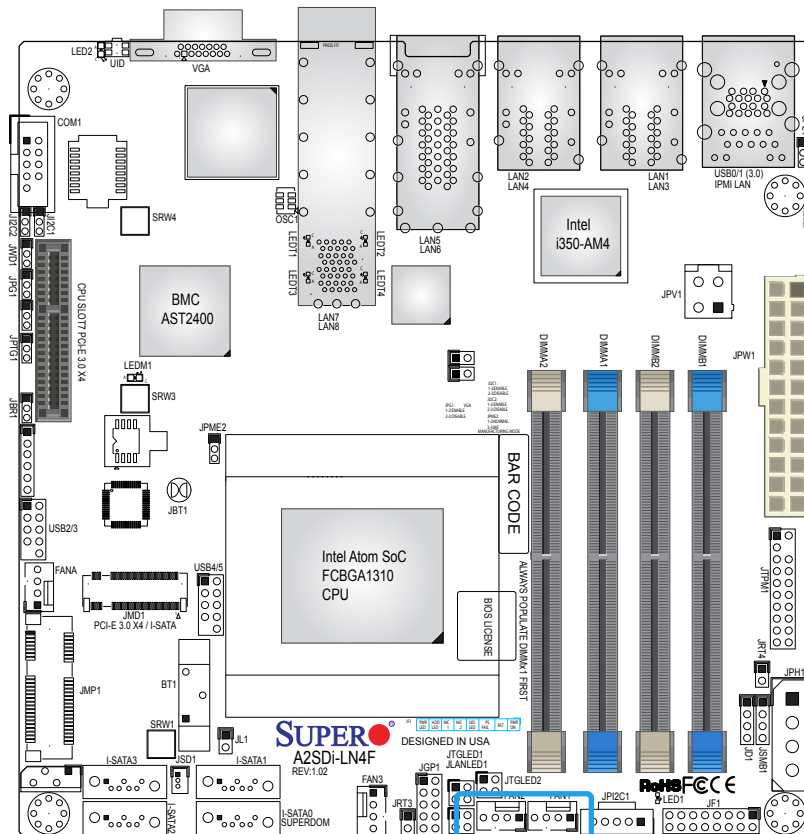


Figure 2-3. JF1 Header Pins

	1	2	
<b>PWR</b> } Power Button	○	○	Ground
<b>Reset</b> } Reset Button	○	○	Ground
<b>3.3 V</b>	○	○	Power Fail LED
<b>UID LED</b>	○	○	OH/Fan Fail LED
<b>3.3V Stby</b>	○	○	NIC2 Active LED
<b>3.3V Stby</b>	○	○	NIC1 Active LED
<b>3.3V Stby</b>	○	○	HDD LED
<b>3.3V</b>	○	○	PWR LED
	15	16	

## Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table below for pin definitions.

Power LED Pin Definitions (JF1)	
Pins	Definition
15	+3.3V
16	PWR LED

## HDD LED

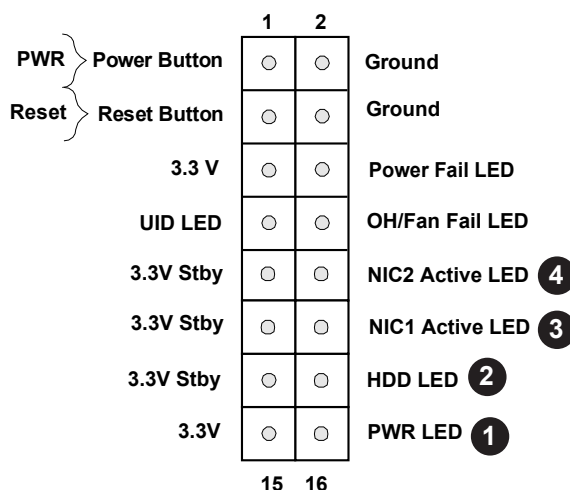
The HDD LED connection is located on pins 13 and 14 of JF1. Attach a cable here to indicate the status of HDD-related activities, including IDE, SATA activities. Refer to the table below for pin definitions.

HDD LED Pin Definitions (JF1)	
Pins	Definition
13	+3.3V Stby
14	HD LED

## NIC1/NIC2 (LAN1/LAN2)

The NIC (Network Interface Controller) LED connection for LAN port 1 is located on pins 11 and 12 of JF1, and the LED connection for LAN Port 2 is on Pins 9 and 10. NIC1 LED and NIC2 LED are 2-pin NIC LED headers. Attach NIC LED cables to NIC1 and NIC2 LED indicators to display network activities. Refer to the table below for pin definitions.

LAN1/LAN2 LED Pin Definitions (JF1)	
Pins	Definition
9/11	+3.3V Stby
10/12	LAN Activity LED



1. PWR LED
2. HDD LED
3. NIC1 LED
4. NIC2 LED

### Power Button

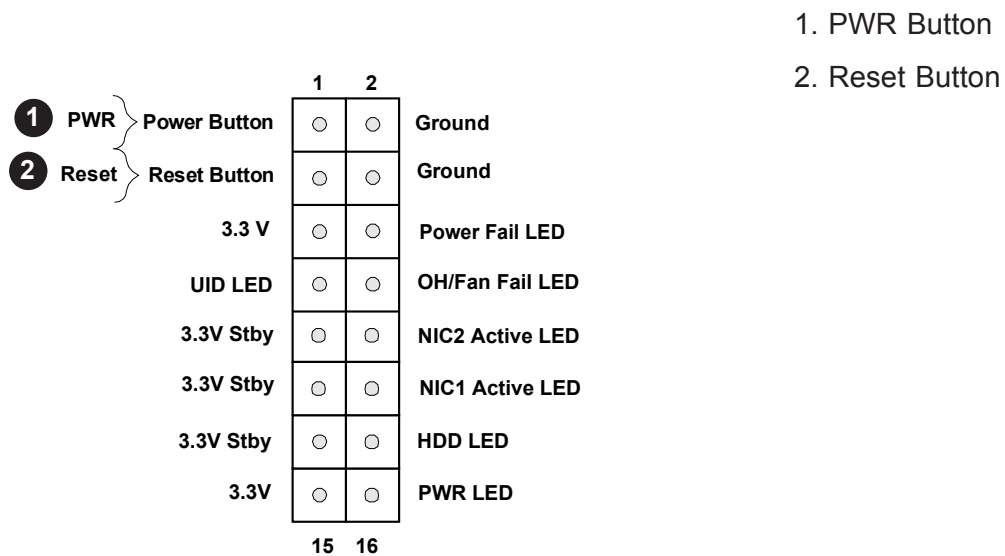
The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power in the suspend mode, press the button for at least 4 seconds. Refer to the table below for pin definitions.

Power Button Pin Definitions (JF1)	
Pins	Definition
1	Signal
2	Ground

### Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the computer case to reset the system. Refer to the table below for pin definitions.

Reset Button Pin Definitions (JF1)	
Pins	Definition
3	Reset
4	Ground



## Power Fail

The Power Fail LED connection is located on pins 5 and 6. Refer to the table below for pin definitions.

PWR Fail Pin Definitions (JF1)	
Pins	Definition
5	3.3V
6	Power Fail

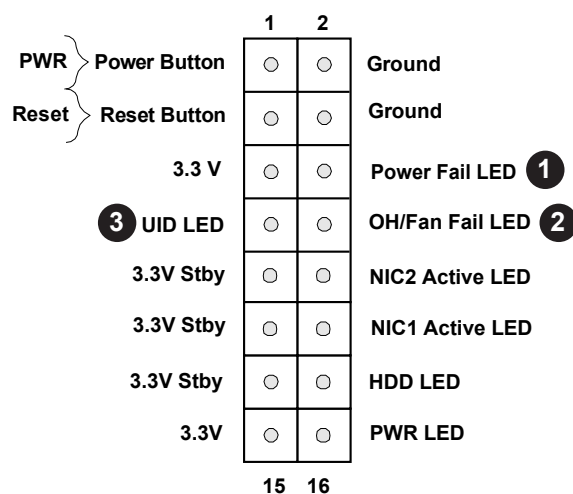
## OH/Fan Fail LED

Connect an LED cable to pins 7 and 8 of JF1 to use the Overheat/Fan Fail and UID LED connections. The blue LED on pin 7 works as the front panel UID LED indicator. The red LED on pin 8 provides warnings of overheating, fan failure or power failure. The red LED takes precedence over the blue LED by default. Refer to the tables below for pin definitions.

UID LED Pin Definitions (JF1)	
Pins	Definition
7	UID LED
8	OH/Fan Fail LED

UID LED Color and Status	
Color	Status
Blue: On	Unit Identified

Power LED Pin Definitions (JF1)	
State	Definition
Off	Normal
On	Overheat
Blinking	Fan Fail



1. Power Fail LED
2. OH/Fan Fail LED
3. UID LED

## 2.7 Connectors

### Power Connections

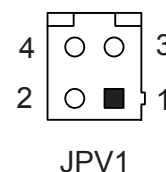
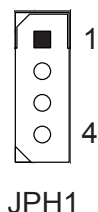
#### Main ATX Power Supply Connector

The primary power supply connector (JPW1) meets the ATX SSI EPS 24-pin specification. JPV1 is the 12V DC power connector that provides alternative power for special enclosure when the 24-pin ATX power is not in use. JPH1 is a 4-pin HDD power connector that provides power to onboard HDD devices.

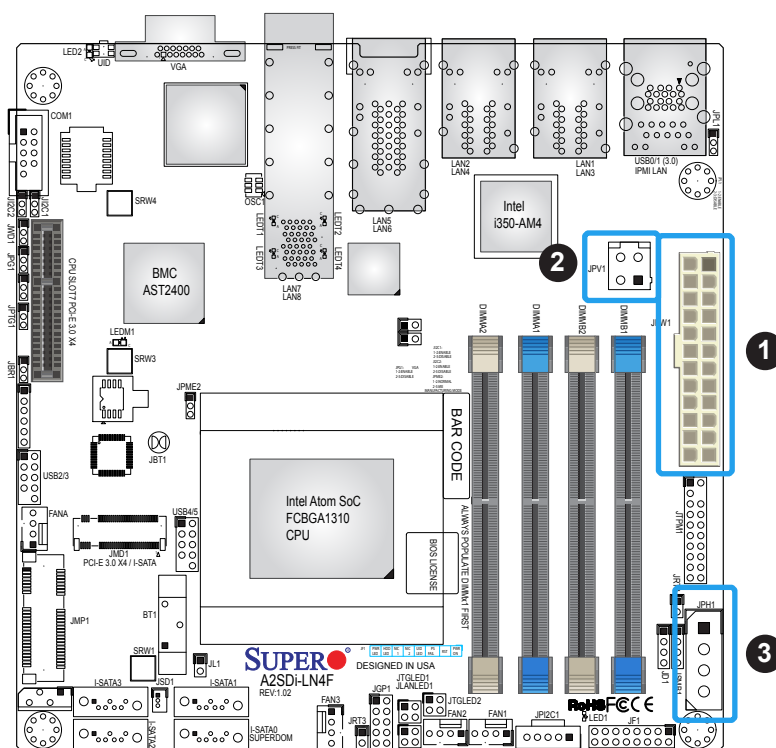
ATX Power 24-pin Connector Pin Definitions			
Pin#	Definition	Pin#	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

4-pin HDD Power Pin Definitions	
Pin#	Definition
1	12V
2-3	Ground
4	5V

+12V 4-pin Power Pin Definitions	
Pin#	Definition
1-2	Ground
3-4	+12V



#### Required Connection



1. 24-Pin ATX Main PWR
2. 12V DC Power
3. 4-pin HDD Power



## Headers

### Fan Headers

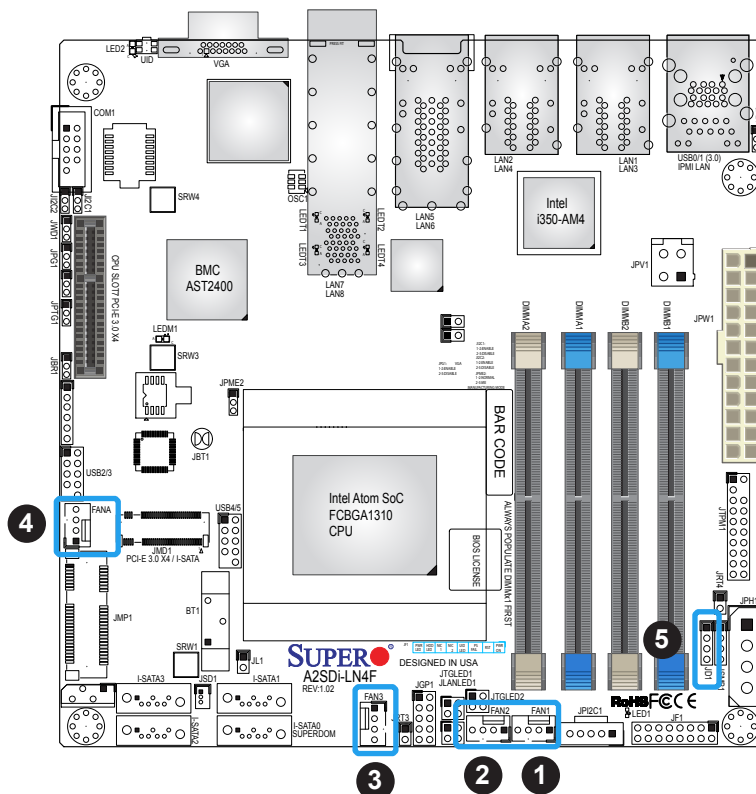
There are four 4-pin fan headers on the motherboard. Pins 1-3 are backward compatible with traditional 3-pin fans. The onboard fan speeds are controlled by Thermal Management (via Hardware Monitoring) in the BIOS. When using Thermal Management setting, please use all 3-pin fans or all 4-pin fans.

Fan Header Pin Definitions	
Pin#	Definition
1	Ground (Black)
2	+12V (Red)
3	Tachometer
4	PWM Control

### Speaker Header

On the JD1 header, pins 1-4 are for the internal speaker.

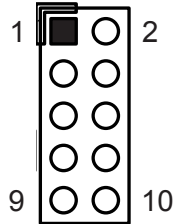
Speaker Header Pin Definitions	
Pin Setting	Definition
Pins 1-4	Speaker



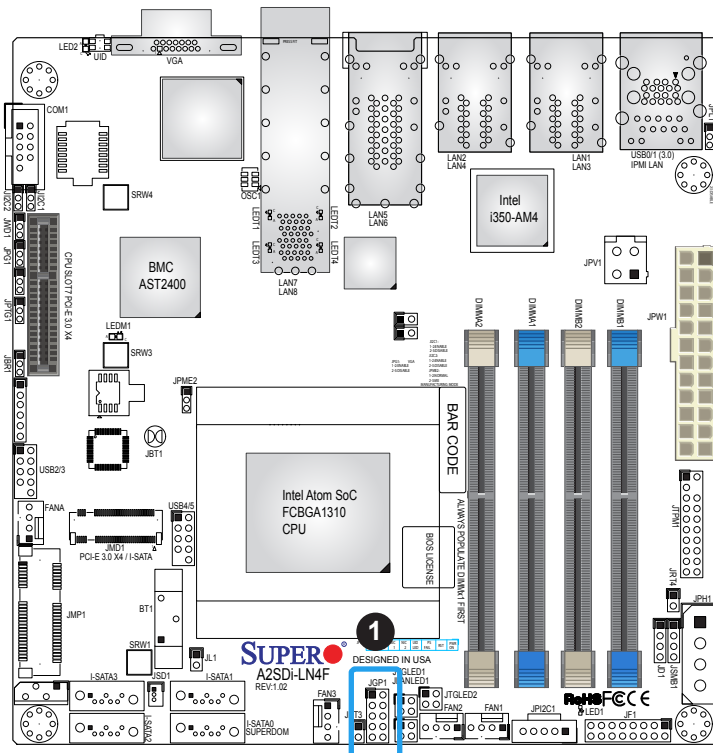
1. FAN1
2. FAN2
3. FAN3
4. FANA
5. Speaker Header

### General Purpose I/O Header

JGP1 is a general purpose input/output header via Intel SoC. Refer to the table below for pin definitions.



JGP1 Header Pin Definitions		
Pin#	Definition	
1	+5V	+5V
2	Ground	Ground
3	GP0	GPP_E0
4	GP1	GPP_F1
5	GP2	GPP_E1
6	GP3	GPP_F2
7	GP4	GPP_E2
8	GP5	GPP_F3
9	GP6	GPP_F0
10	GP7	GPP_F4



### 1. General Purpose I/O

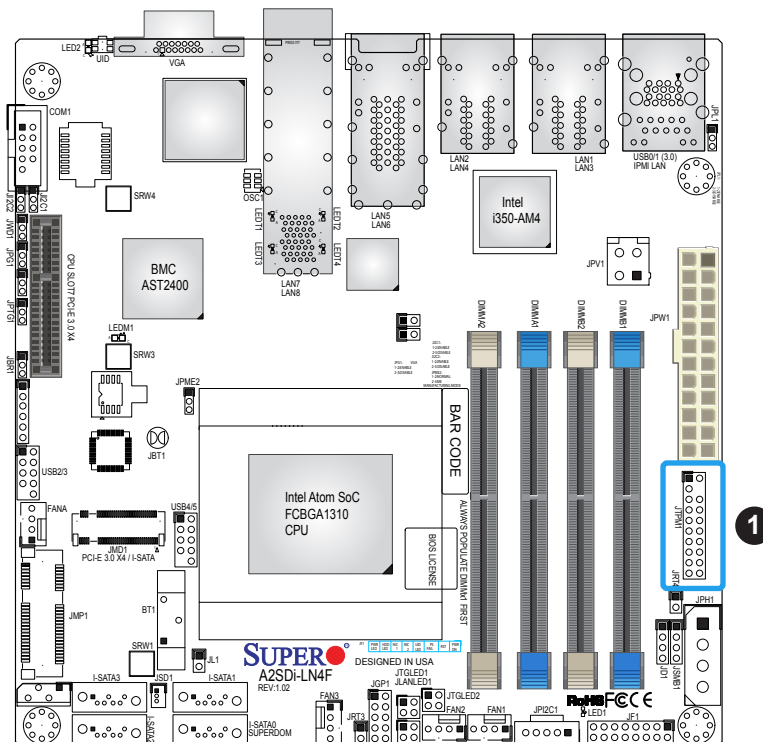
## TPM Header

The JTPM1 header is used to connect a Trusted Platform Module (TPM), which is available from a third-party vendor. A TPM is a security device that supports encryption and authentication in hard drives. It enables the motherboard to deny access if the TPM associated with the hard drive is not installed in the system. Refer to the table below for pin definitions.



**Note:** Please go to the following link for more information on TPM: <http://www.supermicro.com/manuals/other/TPM.pdf>

Trusted Platform Module Header Pin Definitions			
Pin#	Definition	Pin#	Definition
1	LCLK	2	GND
3	LFRAME#	4	No Pin
5	LRESET#	6	+5V (X)
7	LAD3	8	LAD2
9	3.3V	10	LAD1
11	LAD0	12	GND
13	SMB_CLK4 (X)	14	SMB_DAT4 (X)
15	P3V3_STBY	16	SERIRQ
17	GND	18	GND
19	P3V3_STBY	20	LDRQ# (X)



### 1. TPM Header

### Disk-On-Module Power Connector

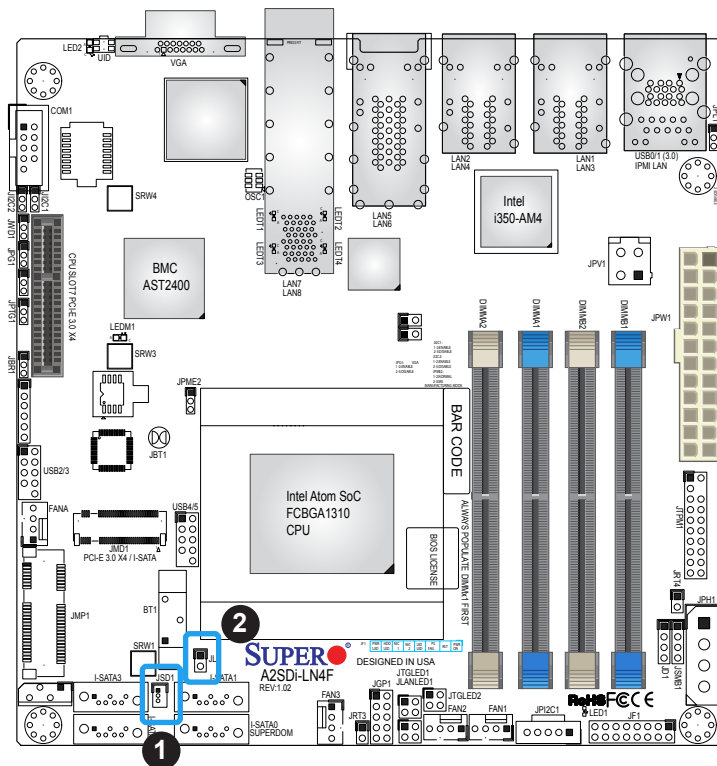
The Disk-On-Module (DOM) power connector at JSD1 provides 5V power to a solid-state DOM storage device connected to one of the SATA ports. Refer to the table below for pin definitions.

DOM Power Pin Definitions	
Pin#	Definition
1	5V
2	Ground
3	Ground

### Chassis Intrusion

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to the header to inform you when the chassis is opened.

Chassis Intrusion Pin Definitions	
Pins	Definition
1	Ground
2	Intrusion Input



1. SATA DOM
2. Chassis Intrusion

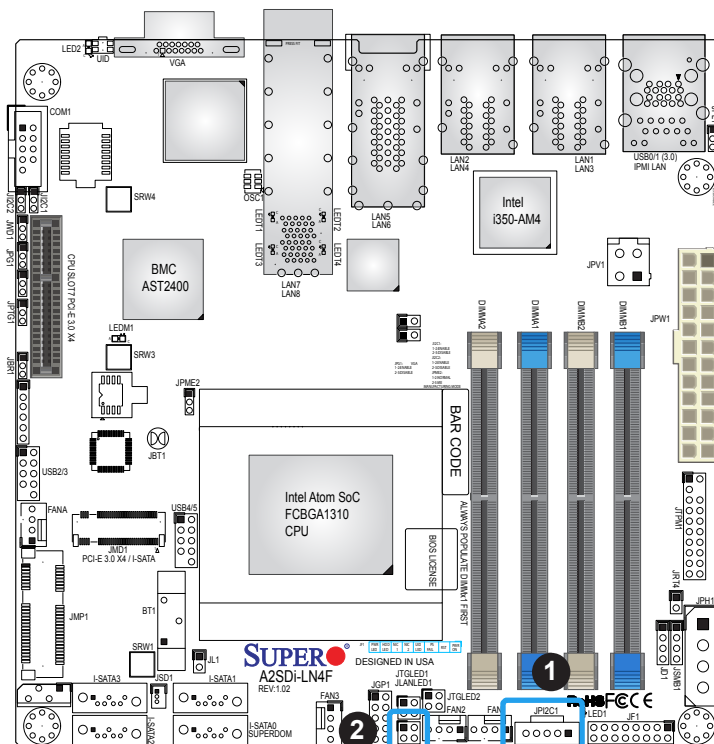
## Power SMB (I<sup>2</sup>C) Header

Power System Management Bus (I<sup>2</sup>C) header at JPI<sup>2</sup>C1 monitors the power supply, fan and system temperatures. Refer to the table below for pin definitions.

Power SMB Header Pin Definitions	
Pin#	Definition
1	Clock
2	Data
3	Power Fail
4	Ground
5	NC

## JLANLED1

JLANLED1 is the activity header for LAN 3 and LAN4.



1. Power SMB Header
2. LAN3/LAN4 Activity Header

## SATA Ports

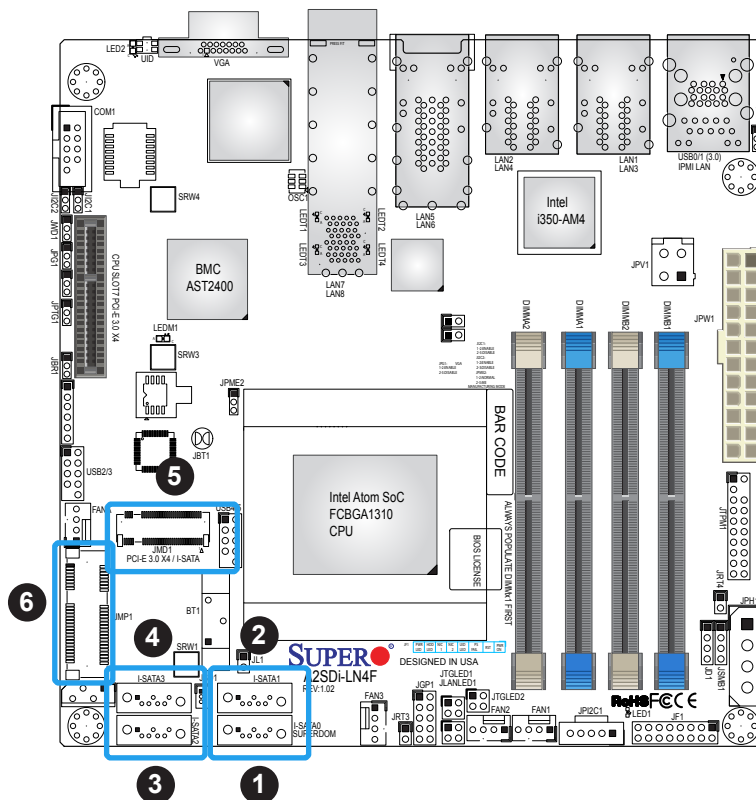
The A2SDi-TP8F/LN4F, A2SDi-16C-TP8F has four SATA 3.0 ports that are supported by the Intel Atom C3000 SoC.

## M.2 Socket

The JMD1 M.2 socket is designed for internal mounting devices. This motherboard deploys an M Key 2242/80 dedicated for SSD devices with the ultimate performance capability in a PCI Express 3.0 X4 interface for native PCIe SSD support. The A2SDi-TP8F/LN4F, A2SDi-16C-TP8F M.2 is mux with the I-SATA4 port for legacy SATA SSD devices.

## Mini PCI-E Slot

JMP1 is a mini PCI-E 3.0 X1 slot that is mux with I-SATA5.



1. I-SATA0
2. I-SATA1
3. I-SATA2
4. I-SATA3
5. M.2/I-SATA4
6. Mini-PCI-E/I-SATA5

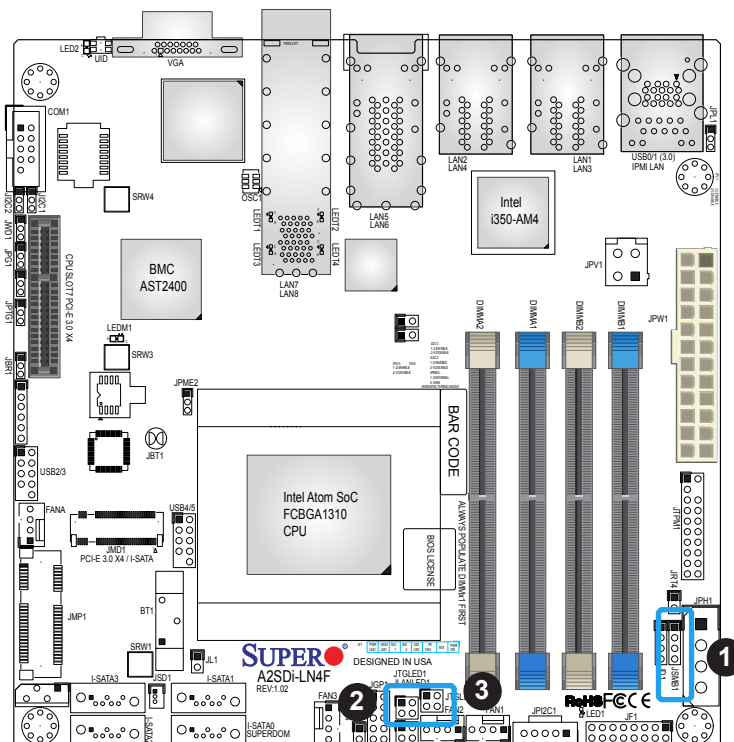
## System Management Bus Header

A System Management Bus header for additional slave devices or sensors is located at JSMB1. Refer to the table below for pin definitions.

SMBus Header Pin Definitions	
Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection

## LED Activity Headers

JTGLED1 is the activity LED header for LAN5 and LAN6. JTGLED2 is the activity LED header for LAN7 and LAN8.



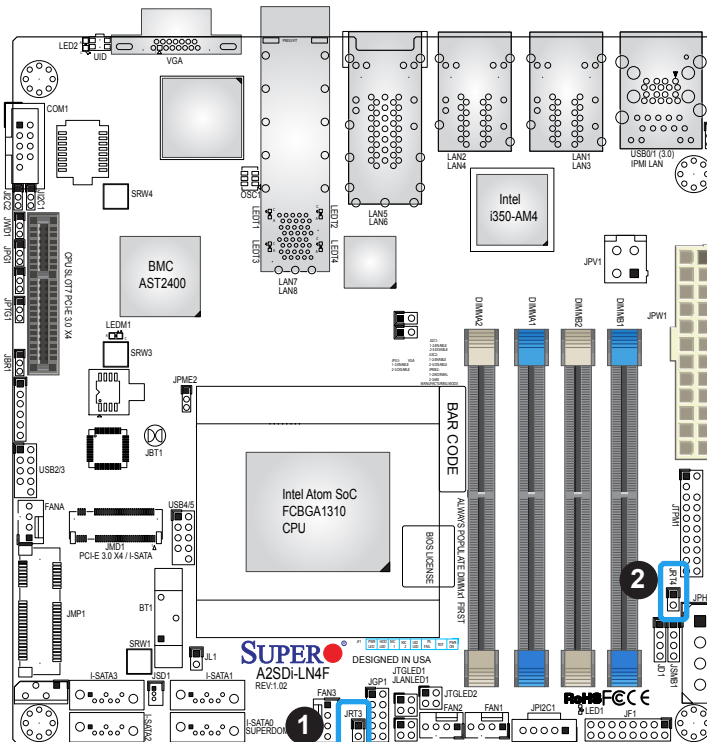
1. SMBus Header
2. JTGLED1
3. JTGLED2

### Thermal Diode Headers

JRT3 is the thermal diode 1 header, and JRT4 is the thermal diode 2 header. They are thermal sensor headers that provide additional system temperature monitoring.

Thermal Diode 1 Header Pin Definitions	
Pin#	Definition
1	TD1_P
2	TD1_N

Thermal Diode 2 Header Pin Definitions	
Pin#	Definition
1	TD2_P
2	TD2_N




1. Thermal Diode 1
2. Thermal Diode 2



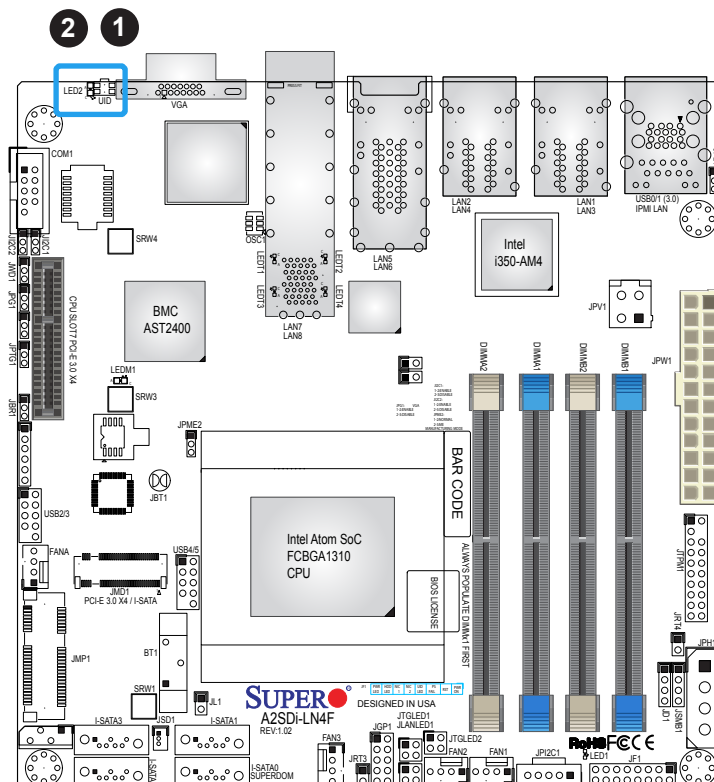
## Unit Identifier Switch/UID LED Indicator

A Unit Identifier (UID) switch and an LED Indicator are located on the motherboard. The UID switch is located at UID, which is next to the VGA port on the back panel. The UID LED (LED2) is located next to the UID switch. When you press the UID switch, the UID LED will be turned on. Press the UID switch again to turn off the LED indicator. The UID Indicator provides easy identification of a system unit that may be in need of service.

 **Note:** UID can also be triggered via IPMI on the motherboard. For more information on IPMI, please refer to the IPMI User's Guide posted on our website at <http://www.supermicro.com>.

UID Switch Pin Definitions	
Pin#	Definition
1	Ground
2	Ground
3	Button In
4	Button In

UID LED Pin Definitions	
Color	Status
Blue: On	Unit Identified




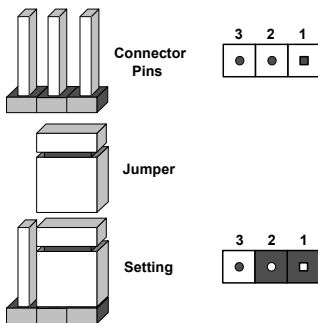
1. UID Switch
2. UID LED

## 2.8 Jumper Settings

### How Jumpers Work

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the diagram below for an example of jumping pins 1 and 2. See the motherboard layout page for jumper locations.

 **Note:** On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.




### CMOS Clear

JBT1 is used to clear CMOS, which will also clear any passwords. Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

#### To Clear CMOS

1. First power down the system and unplug the power cord(s).
2. Remove the cover of the chassis to access the motherboard.
3. Remove the onboard battery from the motherboard.
4. Short the CMOS pads with a metal object such as a small screwdriver for at least four seconds.
5. Remove the screwdriver (or shorting device).
6. Replace the cover, reconnect the power cord(s), and power on the system.

 **Note:** Clearing CMOS will also clear all passwords.

Do not use the PW\_ON connector to clear CMOS.



## VGA Enable/Disable

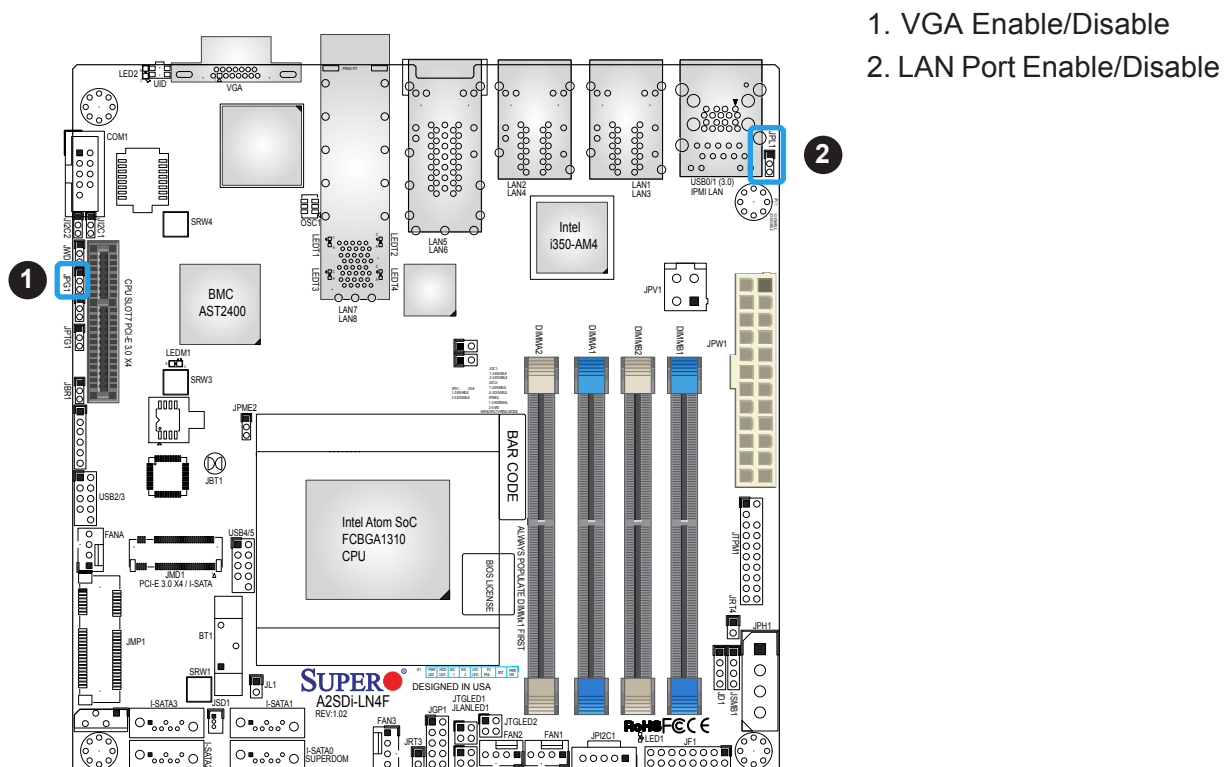
JPG1 allows you to enable or disable the VGA port using the onboard graphics controller. The default setting is Enabled.

VGA Enable/Disable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

## LAN Port Enable/Disable

Change the setting of jumper JPL1 to enable or disable the LAN ports 1 ~ 4. The default setting is Enabled.


LAN1/2 Enable/Disable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled



1. VGA Enable/Disable
2. LAN Port Enable/Disable

## Watch Dog

JWD1 controls the Watch Dog function. Watch Dog is a monitor that can reboot the system when a software application hangs. Jumping pins 1-2 will cause Watch Dog to reset the system if an application hangs. Jumping pins 2-3 will generate a non-maskable interrupt signal for the application that hangs. Watch Dog must also be enabled in BIOS. The default setting is Reset.

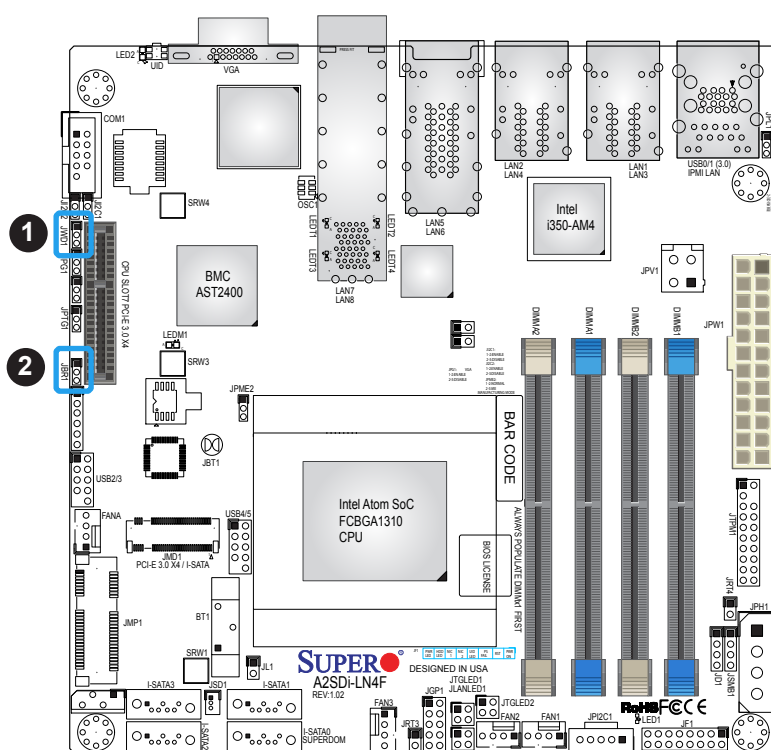
 **Note:** When Watch Dog is enabled, users need to write their own application software to disable it.

Watch Dog Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Reset
Pins 2-3	NMI
Open	Disabled

## BIOS Recovery

Close pins 2-3 of jumper JBR1 for BIOS recovery. The default setting is on pins 1-2 for normal operation. Refer to the table below for jumper settings.

BIOS Recovery Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Normal
Pins 2-3	BIOS Recovery



1. Watch Dog
2. BIOS Recovery

## SMBus to PCI Slots

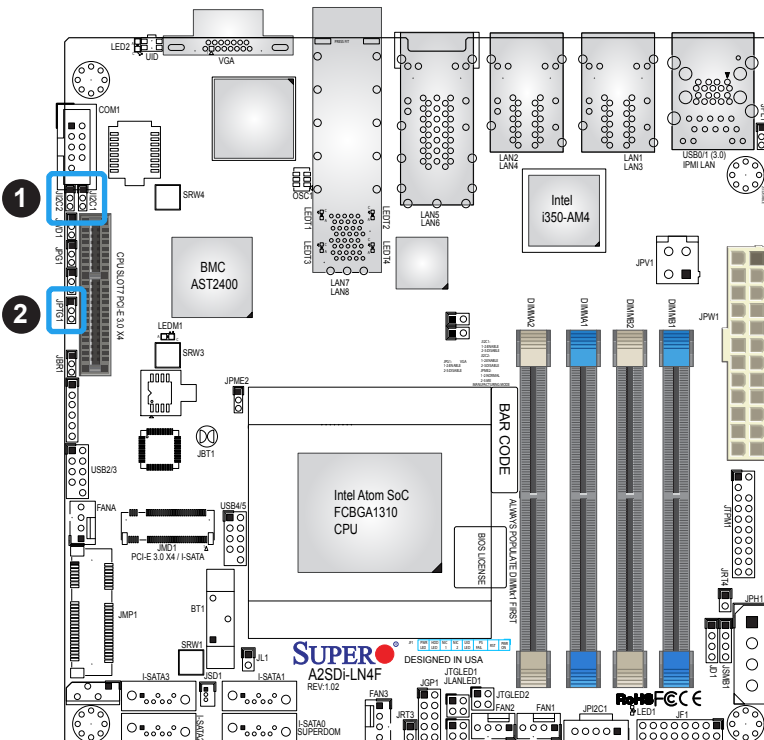
Jumpers JI<sup>2</sup>C1 and JI<sup>2</sup>C2 allow you to connect the System Management Bus (I<sup>2</sup>C) to the PCI-E/PCI slots. Both jumpers must be set to the same setting (JI<sup>2</sup>C1 controls the clock and JI<sup>2</sup>C2 controls the data). The default setting is set to pins 2-3 (Disabled).

SMBus to PCI Slots Jumper Settings		
JI <sup>2</sup> C1 Setting	JI <sup>2</sup> C2 Setting	Definition
JI <sup>2</sup> C1: Pins 1-2	JI <sup>2</sup> C2: Pins 1-2	Enabled
JI <sup>2</sup> C1: Pins 2-3	JI <sup>2</sup> C2: Pins 2-3	Disabled

## JPTG1

Use this jumper to enable 10Gb LAN support. Refer to the table below for jumper settings.

10Gb LAN Enable/Disable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled



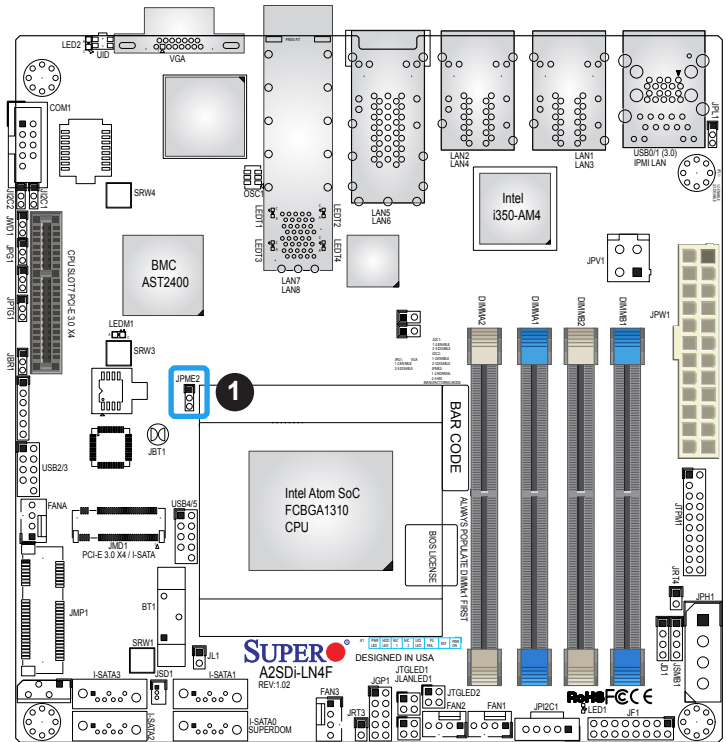
1. SMBus to PCI Slots
2. 10Gb LAN Enable/Disable

### Manufacturing Mode Select

Close JPME2 to bypass SPI flash security and force the system to use the Manufacturing Mode, which will allow you to flash the system firmware from a host server to modify system settings. Refer to the table below for jumper settings.

Manufacturing Mode Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Normal (Default)
Pins 2-3	Manufacturing Mode

### 1. Manufacturing Mode



## 2.9 LED Indicators

### LAN1~4 LEDs

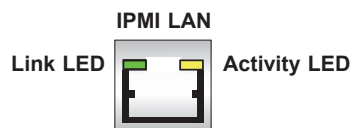
Each Ethernet port on the I/O back panel has two LEDs. One LED indicates activity when flashing while the other LED may be green, amber or off to indicate the speed of the connection.

LAN1~4 LED (Connection Speed Indicator)	
LED Color	Definition
Off	10 Mb/s
Green	100 Mb/s
Amber	1 Gb/s

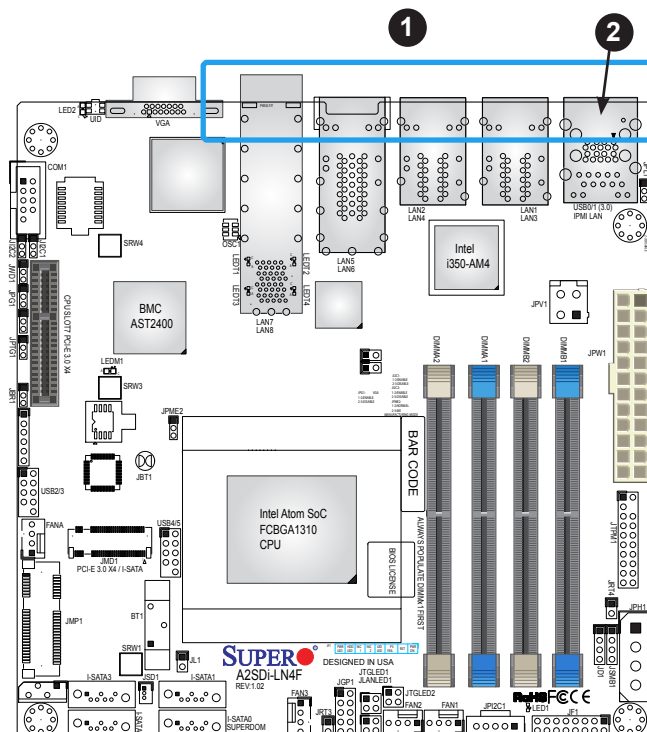
LAN5/6 10G LED (Connection Speed Indicator)	
LED Color	Definition
Off	No Connection
Green	10G
Amber	1G

### IPMI LAN LEDs

A dedicated IPMI LAN is also included on the motherboard. The amber LED on the right of the IPMI LAN port indicates activity, while the green LED on the left indicates the speed of the connection. Refer to the table below for more information.



IPMI LAN LEDs		
Color	Status	Definition
Off	Off	No Connection
Solid Green	Link/Speed (Left)	100 Mb/s
Blinking Amber	Activity (Right)	Active



1. LAN Port LEDs
2. Dedicated IPMI LAN LED

### Onboard Power LED

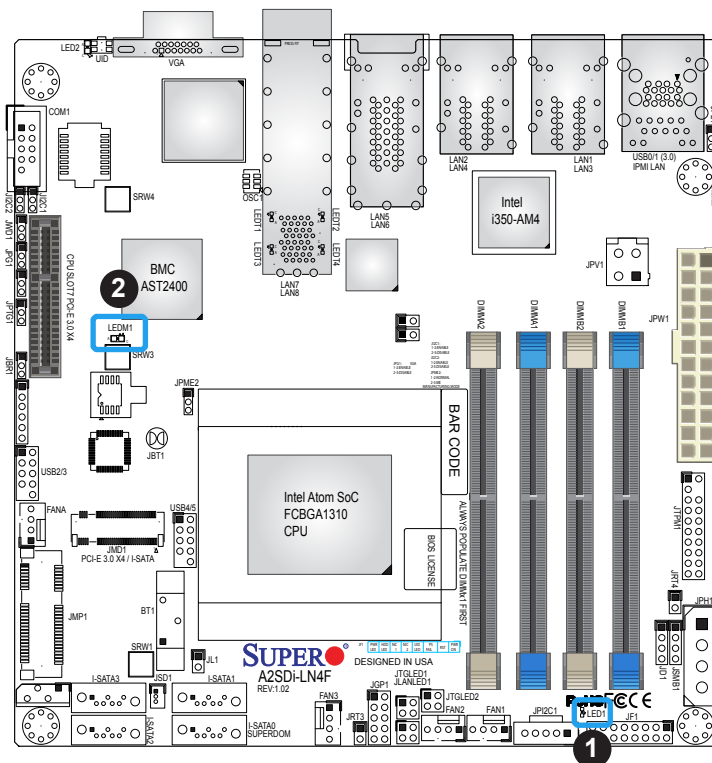
LED1 is an Onboard Power LED. When this LED is lit, it means power is present on the motherboard. In suspend mode, this LED will blink on and off. Be sure to turn off the system and unplug the power cord(s) before removing or installing components.

Onboard Power LED Indicator	
LED Color	Definition
Off	System Off (power cable not connected)
Green	System On

### BMC Heartbeat LED

LEDM1 is the BMC heartbeat LED. When the LED is blinking green, BMC is functioning normally. Refer to the table below for the LED status.

Onboard Power LED Indicator	
LED Color	Definition
Blinking Green	BMC Normal



1. Onboard Power LED
2. BMC Heartbeat LED



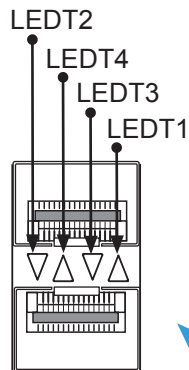
## 10G LAN Activity LED

The 10G LAN Activity LED for LAN7 is located at LEDT3, and the 10G LAN LED Activity LED for LAN8 is located at LEDT1. When the LEDs are blinking, LAN7/LAN8 are active. Refer to the table below for the colors and definitions.

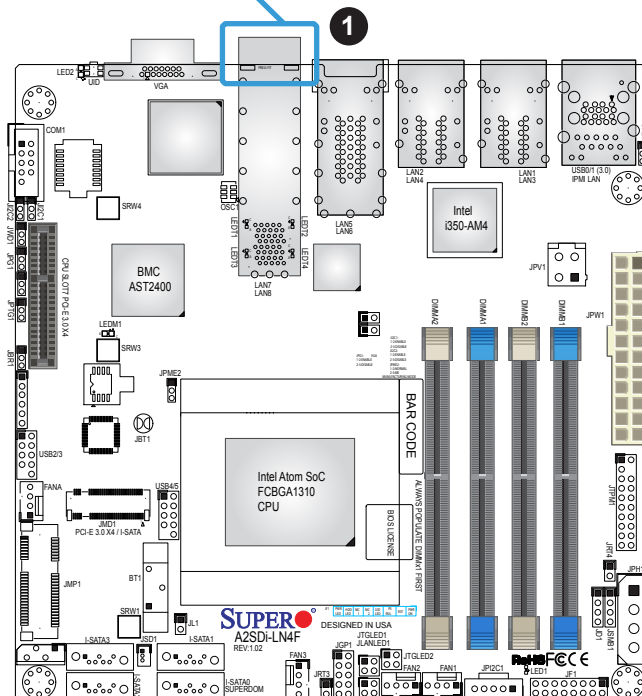
10G LAN Activity LED Indicator	
LED Color	Definition
LEDT3 Blinking Green	LAN7 Active
LEDT1 Blinking Green	LAN8 Active

## 10G LAN Link Status LED

The 10G LAN Link status LED for LAN7 is located at LEDT2, and the 10G LAN Link status LED for LAN8 is located at LEDT4. When the LEDs are on, LAN7/LAN8 are working properly. Refer to the table below for the colors and definitions.



10G Link Status LED Indicator	
LED Color	Definition
LEDT2 Solid Green	LAN7 Normal
LEDT4 Solid Green	LAN8 Normal



1. 10G LAN Activity and Link LEDs

# Chapter 3

## Troubleshooting

### 3.1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any non hot-swap hardware components.

#### Before Power On

1. Check that the power LED on the motherboard is on.
2. Make sure that the power connector is connected to your power supply.
3. Make sure that no short circuits exist between the motherboard and chassis.
4. Disconnect all cables from the motherboard, including those for the keyboard and mouse.
5. Remove all add-on cards.
6. Install a CPU, a heatsink\*, and connect the internal speaker and the power LED to the motherboard. Check all jumper settings as well. (Make sure that the heatsink is fully seated.)
7. Use the correct type of onboard CMOS battery (CR2032) as recommended by the manufacturer. To avoid possible explosion, do not install the CMOS battery upside down.

#### No Power

1. Make sure that no short circuits exist between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

## No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.



**Note:** If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to Appendix B.

## System Boot Failure

If the system does not display POST (Power-On-Self-Test) or does not respond after the power is turned on, check the following:

1. Check for any error beep from the motherboard speaker.
  - If there is no error beep, try to turn on the system without DIMM modules installed. If there is still no error beep, replace the motherboard.
  - If there are error beeps, clear the CMOS settings by unplugging the power cord and contacting both pads on the CMOS Clear Jumper (JBT1). Refer to chapter 2.
2. Remove all components from the motherboard, especially the DIMM modules. Make sure that system power is on and that memory error beeps are activated.
3. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this Chapter.

## Memory Errors

1. Make sure that the DIMM modules are properly and fully installed.
2. Confirm that you are using the correct memory. Also, it is recommended that you use the same memory type and speed for all DIMMs in the system. See Section 2.4 for memory details.
3. Check for bad DIMM modules or slots by swapping modules between slots and noting the results.
4. Check the power supply voltage 115V/230V switch.

## Losing the System's Setup Configuration

1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1.5 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the setup configuration problem, contact your vendor for repairs.

## When the System Becomes Unstable

### ***A. If the system becomes unstable during or after OS installation, check the following:***

1. CPU/BIOS support: Make sure that your CPU is supported and that you have the latest BIOS installed in your system.
2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.



**Note:** Click on the Tested Memory List link on the motherboard product page to see a list of supported memory.

3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
4. System cooling: Check the system cooling to make sure that all heatsink fans and CPU/system fans, etc., work properly. Check the hardware monitoring settings in the IPMI to make sure that the CPU and system temperatures are within the normal range. Also check the front panel Overheat LED and make sure that it is not on.
5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on the minimum power requirements.
6. Proper software support: Make sure that the correct drivers are used.

### ***B. If the system becomes unstable before or during OS installation, check the following:***

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD/DVD and CD/DVD-ROM.
2. Cable connection: Check to make sure that all cables are connected and working properly.

3. Using the minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use the minimum configuration (but with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.
4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.
5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

## 3.2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, we do not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please review the 'Troubleshooting Procedures' and 'Frequently Asked Questions' (FAQs) sections in this chapter or see the FAQs on our website at <http://www.supermicro.com/FAQ/index.php> before contacting Technical Support.
2. BIOS upgrades can be downloaded from our website ([http://www.supermicro.com/ResourceApps/BIOS\\_IPMI\\_Intel.html](http://www.supermicro.com/ResourceApps/BIOS_IPMI_Intel.html)).  
**Note:** Not all BIOS can be flashed depending on the modifications to the boot block code.
3. If you still cannot resolve the problem, include the following information when contacting us for technical support:
  - Motherboard model and PCB revision number
  - BIOS release date/version (this can be seen on the initial display when your system first boots up)
  - System configuration

An example of a Technical Support form is posted on our website.

Distributors: For immediate assistance, please have your account number ready when contacting our technical support department by e-mail.

### 3.3 Frequently Asked Questions

**Question: What type of memory does my motherboard support?**

**Answer:** The A2SDi-TP8F/LN4F, A2SDi-16C-TP8F motherboard supports up to 64 GB of DDR4 2133MHZ ECC UDIMM memory. See Section 2.4 for details on installing memory.

**Question: How do I update my BIOS?**

**Answer:** It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at [http://www.supermicro.com/ResourceApps/BIOS\\_IPMI\\_Intel.html](http://www.supermicro.com/ResourceApps/BIOS_IPMI_Intel.html). Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You can choose from the zip file and the .exe file. If you choose the zip BIOS file, please unzip the BIOS file onto a bootable USB device. Run the batch file using the format FLASH.BAT filename.rom from your bootable USB device to flash the BIOS. Then, your system will automatically reboot.

**Question: Why can't I turn off the power using the momentary power on/off switch?**

**Answer:** The instant power off function is controlled in BIOS by the Power Button Mode setting. When the On/Off feature is enabled, the motherboard will have instant off capabilities as long as the BIOS has control of the system. When the Standby or Suspend feature is enabled or when the BIOS is not in control such as during memory count (the first screen that appears when the system is turned on), the momentary on/off switch must be held for more than four seconds to shut down the system. This feature is required to implement the ACPI features on the motherboard.

## 3.4 Battery Removal and Installation

### Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

### Proper Battery Disposal

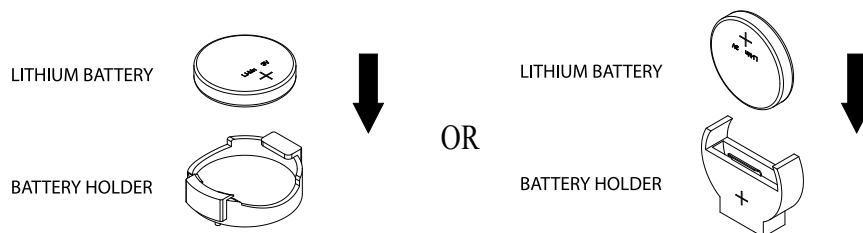
Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

### Battery Installation

1. To install an onboard battery, follow steps 1 and 2 above and continue below:
2. Identify the battery's polarity. The positive (+) side should be facing up.
3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.



**Important:** When replacing a battery, be sure to only replace it with the same type.





## 3.5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (<http://www.supermicro.com/support/rma/>).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

## Chapter 4

# UEFI BIOS

### 4.1 Introduction

This chapter describes the AMIBIOS™ Setup utility for the A2SDi-TP8F, A2SDi-16C-TP8F motherboard. The BIOS is stored on a chip and can be easily upgraded using a flash program.



**Note:** Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to BIOS that may not be reflected in this manual.

#### Starting the Setup Utility

To enter the BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

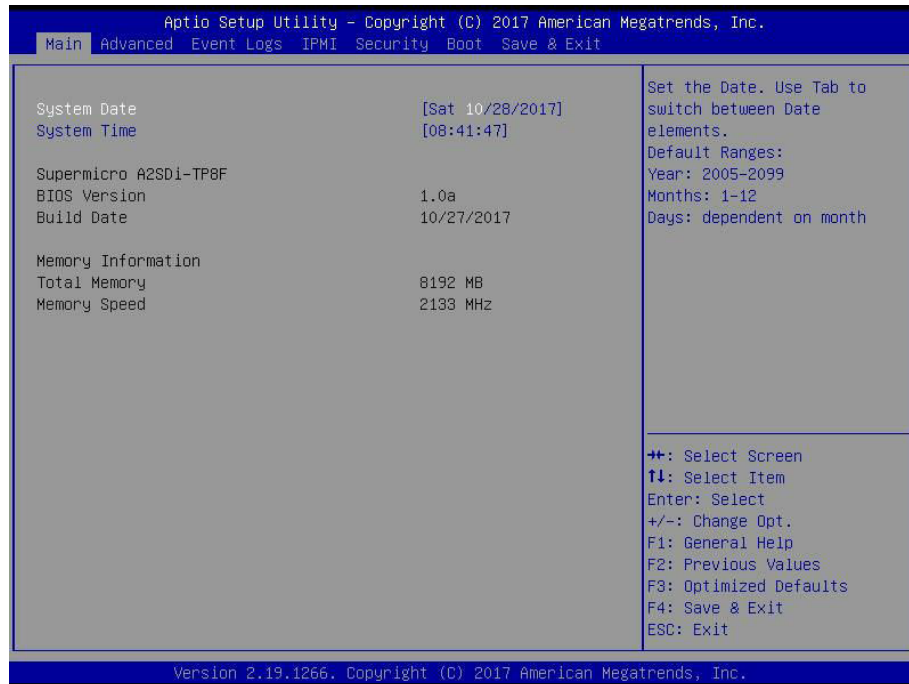
The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. “Grayed-out” options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A " ►" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.


## 4.2 Main Setup

When you first enter the AMI BIOS setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below and the following items will be displayed:



### System Date/System Time

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in MM/DD/YYYY format. The time is entered in HH:MM:SS format.

 **Note:** The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00. The date's default value is the BIOS build date after RTC reset.

### Supermicro A2SDi-TP8F/A2SDi-LN4F, A2SDi-16C-TP8F

#### BIOS Version

This item displays the version of the BIOS ROM used in the system.

#### Build Date

This item displays the date when the version of the BIOS ROM used in the system was built.

## **Memory Information**

### **Total Memory**

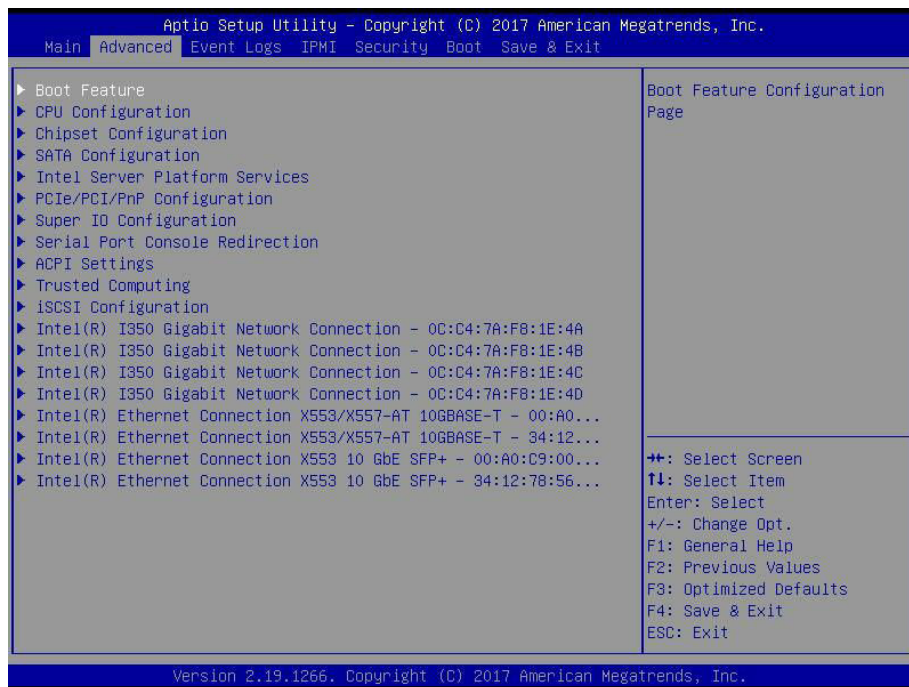
This item displays the total size of memory available in the system.

### **Memory Speed**

This item displays the default speed of the memory modules installed in the system.

## 4.3 Advanced

Use the arrow keys to select Advanced setup and press <Enter> to access the submenu items:



**Warning:** Take caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency or an incorrect BIOS timing setting may cause the system to malfunction. When this occurs, restore to default manufacturer settings.

### ► Boot Feature

#### Quiet Boot

Use this feature to select the screen display between POST messages or the OEM logo at bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are Disabled and **Enabled**.

#### Bootup NumLock State

Use this feature to set the Power-on state for the Numlock key. The options are Off and **On**.

#### Wait For "F1" If Error

This feature forces the system to wait until the F1 key is pressed if an error occurs. The options are Disabled and **Enabled**.

## Power Configuration

### Watch Dog Function

If enabled, the Watch Dog timer will allow the system to reboot when it is inactive for more than 5 minutes. The options are **Disabled** and **Enabled**.

### Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select **4 Seconds Override** for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select **Instant Off** to instantly power off the system as soon as the user presses the power button. The options are **4 Seconds Override** and **Instant Off**.

### Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select **Power Off** for the system power to remain off after a power loss. Select **Power On** for the system power to be turned on after a power loss. Select **Last State** to allow the system to resume its last power state before a power loss. The options are **Stay Off**, **Power On**, and **Last State**.

## ► CPU Configuration

The following CPU information will display:

- Displays the CPU model
- Processor ID
- Microcode Revision
- Processor Frequency
- CPU BCLK Frequency
- L1 Cache RAM
- L2 Cache RAM

### EIST (GV3)

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are **Disable** and **Enable**.

### BIOS Request Frequency

Use this feature to instruct how much frequency will be delivered to the processor. The options are **Disable** and **Enable**.

**Turbo**

Select Enable for processor cores to run faster than the frequency specified by the manufacturer. The options are Disable and **Enable**.

**TM1**

Select Enable to activate TM1 support for system thermal monitoring. TM1 allows the CPU to regulate its power consumption based upon the modulation of the CPU Internal clock when the CPU temperature reaches a pre-defined overheating threshold. The options are Disable and **Enable**.

**TM2 Mode**

Use this feature to select the throttling mode for TM2. The options are LFM Throttling and **Adaptive Throttling**.

**Dynamic Self Refresh**

Select Enable to support Dynamic Self-Refreshing for the onboard memory controller. The options are **Disable** and Enable.

**CPU C State**

Select Enabled to enhance the Cx state of the CPU. Reboot the system for this feature to take effect. The options are Disable and **Enable**.

***\*If the feature above is enabled, the following four features are available for configuration:***

**Package C State limit**

Use this item to set the limit on the C-State package register. The options are No Pkg C-State, C1, and **C3**.

**Max Core C-State**

Use this feature to specify which idle power saving state to enter. The options are C1 and **C6**.

**Enhanced Halt State (C1E)**

This feature is not available if the EIST and CPU C State features are disabled. Select Enable to enable Enhanced Halt State support, which will significantly reduce the CPU's power consumption by minimizing the CPU's clock cycles and voltage use during a Halt State. The options are Disable and **Enable**.

**Monitor/Mwait**

Select Enable to enable the Monitor/Mwait instructions. The Monitor instructions monitors a region of memory for writes, and MWait instructions instruct the CPU to stop until the monitored region begins to write. The options are Disable and **Enable**.

### **L1 Prefetcher (Available when supported by the CPU)**

If enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L1 cache to improve CPU performance. The options are **Enable** and Disable.

### **L2 Prefetcher (Available when supported by the CPU)**

If enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are **Enable** and Disable.

### **ACPI 3.0 T-States**

Select Enable to support ACPI (Advanced Configuration and Power Interface) 3.0 T-States to determine how the processor will report to the operating system during CPU-Throttling states. The options are **Disable** and Enable.

***\*If the feature above is enabled, T-State Throttle is available for configuration:***

### **T-State Throttle**

Use this feature to select the On-Die throttling. The options are **default**, 12.5%, 25.0%, 37.5%, 50.0%, 62.25%, 75.0%, and 87.5%

### **Max CPUID Value Limit**

Use this feature to set the maximum CPU ID value. Enable this feature to boot the legacy operating systems that cannot support processors with extended CPUID functions. The options are Enable and **Disable** (for the Windows OS).

### **Execute Disable Bit (Available if supported by the OS & the CPU)**

Set to Enable for Execute Disable Bit support, which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damaging the system during a virus attack. The options are Disable and **Enable**. (Refer to Intel and Microsoft websites for more information.)

### **Virtualization Technology**

Select Enable to use Intel Virtualization Technology to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple virtual systems in one physical computer. The options are Disable and **Enable**.

### **Extended APIC (Advanced Programmable Interrupt Controller)**

Based on the Intel Hyper-Threading technology, each logical processor (thread) is assigned 256 APIC IDs (APIDs) in 8-bit bandwidth. When this item is set to Enable, the APIC ID will be expanded from 8 bits to 16 bits to provide 512 APIDs to each thread to enhance CPU performance. The options are Disable and **Enable**.

### **AES-NI**

Select Enable to use the Intel Advanced Encryption Standard (AES) New Instructions (NI) to ensure data security. The options are Disable and **Enable**.



**Lock PACKAGE\_RAPL\_LIMIT**

Use this feature to unlock the MSR 0x610. The options are **Disable** and Enable.

**PL1 Time Window**

Use this feature to define the Running Average Power Limit (RAPL) time window 1 in milliseconds. The default setting is **45**. Use the "+" or "-" keys to define the setting.

**PL1 Power Level**

Use this feature to define the Running Average Power Limit (RAPL) power limit 1 in Watts. The default setting is **32**. Use the "+" or "-" keys to define the setting.

**PL2 Power Level**

Use this feature to define the Running Average Power Limit (RAPL) power limit 2 in Watts. The default setting is **38**. Use the "+" or "-" keys to define the setting.

**Active Processor Cores**

Use this feature to set the number of processor cores that will be activated for each CPU. Select 0 to activate all processor cores.

**► Chipset Configuration**

**Warning:** Setting the wrong values in the sections below may cause the system to malfunction.

**► North Bridge Configuration**

The following memory information will be displayed:

- MRC Version
- Total Memory
- Memory Frequency

**VT-d**

Select Enabled to enable Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to VMM through the DMAR ACPI Tables. This feature offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security and availability in networking and data-sharing. The options are Disabled and **Enabled**.

**VT-d Interrupt remapping**

Use this feature to enable Interrupt Remapping support, which detects and controls external interrupt requests. The options are Disabled and **Enabled**.

### **Fast Boot**

Use this feature to enable or disable fast path through the memory reference code. The options are Disabled and **Enabled**.

### **Command Address Parity**

Use this feature to enable or disable the DDR4 command address parity. The options are **Disabled** and Enabled.

### **Memory Frequency**

Use this feature to set the maximum memory frequency for onboard memory modules. The options are DDR-1600, DDR-1867, DDR-2133, and **DDR-2400**.

### **MMIO Size / BMBOUND Base**

Use this feature to select the memory mapped IO size and BMBOUND base setting. The low for the two settings will always be 4GB. The options are **Auto**, 1024M / 3072M, and 3072M / 1024M.

### **TCL performance**

Use this feature to enable the CAS Latency (tCL) to increase memory performance. The options are Disabled and **Enabled**.

### **Memory Preservation**

Use this feature to enable or disable memory preservation after a warm reset. The options are **Disabled** and Enabled.

### **Patrol Scrub Enable**

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected in a memory module and send the correction to the requestor (the original source). When this item is set to Enable, the IO hub will read and write back one cache line every 16K cycles if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are **Enabled** and Disabled.

### **Patrol Scrub Period**

Use this feature to select the Patrol Scrub period. The options are **24 hours**, 10 hours, 4 hours, and 1 hour.

### **Demand Scrub Enable**

Demand Scrubbing is a process that allows the CPU to correct correctable memory errors found in a memory module. When the CPU or I/O issues a demand-read command, and the read data from memory turns out to be a correctable error, the error is corrected and sent to the requestor (the original source). Memory is corrected as well. Select Enable to use Demand Scrubbing for ECC memory correction. The options are **Enabled** and Disabled.

**Write Data Early Enable**

Use this feature to enable or disable Write Date Early. The options are **Disabled** and Enabled.

**Select Refresh Rate**

Use this feature to select the memory refresh rate. The options are **1x/2x** and 1x/2x/4x.

**CKE Power Down**

Clock enable (CKE) Power Down controls the low power down for the memory. The options are **Disabled**, Active Power Down, and Precharge Power Down.

**Memory Thermal Throttling**

Memory thermal throttling is a power management feature that monitors read and write activities to control power consumption. The options are Auto and **Disabled**.

***\*If the feature above is set to Auto, the next two features are available for configuration:***

**CLTT Mode**

Use this feature to select the Closed Loop Thermal Throttling mode. The options are **Normal** and Passthru.

**MEMTRIP**

Use this feature to enable or disable MEMTRIP. The options are **Disabled** and Enabled.

**Scrambler**

This feature scrambles data in the memory and makes it inaccessible. The options are Disabled and **Enabled**.

**Slow Power Down Exit**

Use this feature to enable or disable the slow exit after the precharge power down. The options are Disable dand **Enabled**.

**►South Bridge Configuration**

The following South Bridge information will display:

- USB Module Version
- USB Controllers
- USB Devices

**Legacy USB Support**

Select Enabled to support onboard legacy USB devices. Select Auto to disable legacy support if there are no legacy USB devices present. Select Disable to have all USB devices available for EFI applications only. The options are **Enabled**, Disabled, and Auto.

### **XHCI Hand-off**

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The settings are **Enabled** and Disabled.

### **Port 60/64 Emulation**

Select Enabled for I/O port 60h/64h emulation support, which in turn, will provide complete legacy USB keyboard support for the operating systems that do not support legacy USB devices. The options are Disabled and **Enabled**.

## **►IQAT Configuration**

### **IQAT**

Select Enabled to hide IQAT devices from the operating system. The options are Disabled and **Enabled**.

### **Set 64B MRR/MPL**

Use this feature to enable or disable the 64-bit MRR/MPL in IQAT. The options are Disabled and **Enabled**

## **►SATA Configuration**

### **mSATA Selection**

Two interface options are available, either mSATA or **mSATA/Mini PCIe**.

## **►SATA0**

### **SATA 0 Enable controller**

This item enables or disables the onboard SATA controller supported by the processor. The options are **Enabled** and Disabled.

### **SATA 0 LPM (Link Power Management)**

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity and will return the link to an active state when I/O activity resumes. The options are Enabled and **Disabled**.

***\*If the feature above is enabled, the SATA 0 ALPM feature is available for configuration:***

### **SATA 0 ALPM**

Use this feature to disable or enable the Aggressive Link Power Management. The options are Enabled and **Disabled**.

## ► I-SATA (M.2)

This following information is displayed:

- Device Information
- Device Size

### I-SATA (M.2) Enable/disable port

Use this feature to disable or enable the SATA port number. The options are **Enabled** and **Disabled**.

### I-SATA (M.2) Spin up

When the value of an edge detect or the value of an image binary (pixel) of a device is from 0 to 1, select **Enabled** to allow the PCH to start a COMRESET initialization sequence on this device. The options are **Enabled** and **Disabled**.

## ► SATA1

### SATA 1 Enable controller

This item enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are **Enabled** and **Disabled**.

### SATA 1 LPM (Link Power Management)

When this item is set to **Enabled**, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity and will return the link to an active state when I/O activity resumes. The options are **Enabled** and **Disabled**.

*\*If the feature above is enabled, the SATA 1 ALPM feature is available for configuration:*

### SATA 1 ALPM

Use this feature to disable or enable the Aggressive Link Power Management. The options are **Enabled** and **Disabled**.

### SATA 1 SGPIO/LED

Use this feature to select the SATA LED or SGPIO LED. The options are **SGPIO** and **SATA LED**.

### ► I-SATA0 ~ I-SATA3, mSATA

This following information is displayed for each SATA drive entry:

- Device Information
- Device Size

#### **Enable/disable port**

Use this feature to disable or enable the SATA port number. The options are **Enabled** and Disabled.

#### **Spin up**

When the value of an edge detect or the value of an image binary (pixel) of a device is from 0 to 1, select Enabled to allow the PCH to start a COMRESET initialization sequence on this device. The options are Enabled and **Disabled**.

### ► Intel Server Platform Services

This feature displays the following ME information:

- General ME Configuration
- Operational Firmware Version
- ME Firmware Type
- Backup Firmware Version
- Recovery Firmware Version
- ME Firmware Features
- ME Firmware Status #1
- ME Firmware Status #2
  - Current State
  - Error Code

## ► PCIe/PCI/PnP Configuration

The following PCI information will be displayed:

- PCI Bus Driver Version
- PCI Devices Common Settings:

### **Above 4G Decoding (Available if the system supports 64-bit PCI decoding)**

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Enabled and **Disabled**.

### **SR-IOV Support (Available if the system supports Single-Root Virtualization)**

Select Enabled for Single-Root IO Virtualization support. SR-IOV is an extension of the PCI Express interface and consists of two functions: physical functions (PF) and virtual functions (VF). PF is the primary function and is used to control and configure PCI Express devices, whereas VF is the lightweight function that offers limited configuration. The options are Enabled and **Disabled**.

### **Maximum Payload**

Select Auto for the system BIOS to automatically set the maximum payload value for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

### **Maximum Read Request**

Select Auto for the system BIOS to automatically set the maximum size for a read request for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

### **ASPM Support**

Use this feature to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are **Disabled**, Auto, and Force L0s.

**Warning:** Enabling ASPM support may cause some PCI-E devices to fail!

### **ARI Forwarding**

When this feature is enable, the Downstream port disables its traditional device number to 0 when turning Type1 Configuration Request into a Type0 Configuration Request. The default is **Disabled**.

### **CPU SLOT7 PCI-E 3.0 X4 OPROM**

Use this feature to select the firmware type for the add-on card for this slot. The options are Disabled, Legacy, and **EFI**.

### **M.2 PCI-E 3.0 X4 OPROM**

Use this feature to select the firmware type for the add-on card for this slot. The options are Disabled, Legacy, and **EFI**.

### **Mini PCI-E OPROM**

Use this feature to select which firmware type to load for the add-on card for this slot. The options are Disabled, Legacy, and **EFI**.

### **Onboard LAN OPROM Type**

Use this feature to select the Onboard LAN Option ROM type. The options are Disabled, Legacy, and **EFI**.

### **Onboard Video OPROM**

Use this feature to select the Onboard Video Option ROM type. The options are Disabled, Legacy, and **EFI**.

### **VGA Priority**

Use this feature to select the type of video display. The options are **Onboard** and Offboard.

### **Network Stack**

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are **Enabled** and Disabled.

***\*If "Network Stack" is enabled, the following four features are available for configuration:***

#### **IPv4 PXE Support**

Use this feature to enable Ipv4 PXE Boot Support. If this feature is disabled, it will not create the Ipv4 PXE Boot option. The options are Disabled and **Enabled**.

#### **IPv6 PXE Support**

Use this feature to enable Ipv6 PXE Boot Support. If this feature is disabled, it will not create the Ipv6 PXE Boot option. The options are **Disabled** and Enabled.

#### **PXE boot wait time**

Use this feature to select the wait time to press the ESC key to abort the PXE boot. The default is 0.

#### **Media detect count**

Use this feature to select the wait time in seconds to detect LAN media. The default is 1.

## **► Super IO Configuration**

### **Super IO Chip AST2400**



## ► Serial Port 1 Configuration

### Serial Port 1

Select Enabled to enable the onboard serial port specified by the user. The options are **Enabled** and Disabled.

### Device Settings

This item displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.



**Note:** This feature is hidden when Serial Port 1 is set to Disabled.

### Serial Port 1 Change Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 1. Select **Auto** for the BIOS to automatically assign the base I/O and IRQ address to a serial port specified. The options are **Auto**, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

## ► Serial Port Console Redirection

### COM1

#### COM 1 Console Redirection

Select Enabled to enable COM Port 1 for Console Redirection, which will allow a client machine to be connected to a host machine at a remote site for networking. The options are Enabled and **Disabled**.

*\*If the feature above is set to Enabled, the following features will become available for configuration:*

#### ► COM1 Console Redirection Settings

##### COM1 Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

### **COM1 Bits per second**

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

### **COM1 Data Bits**

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

### **COM1 Parity**

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

### **COM1 Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

### **COM1 Flow Control**

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### **COM1 VT-UTF8 Combo Key Support**

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

### **COM1 Recorder Mode**

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### **COM1 Resolution 100x31**

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

### **COM1 Putty KeyPad**

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

## SOL Console Redirection

Select Enabled to use the SOL port for Console Redirection. The options are Disabled and Enabled.

*\*If the feature above is enabled, the following features are available for configuration:*

### ► SOL Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

#### SOL

##### SOL Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

##### SOL Bits per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

##### SOL Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

##### SOL Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

##### SOL Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

### **SOL Flow Control**

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### **SOL VT-UTF8 Combo Key Support**

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

### **SOL Recorder Mode**

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### **SOL Resolution 100x31**

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

### **SOL Putty KeyPad**

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

## **Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)**

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

### **EMS (Emergency Management Services) Console Redirection**

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are **Disabled** and Enabled.

***\*If the feature above is set to Enabled, the following features are available for configuration:***

#### **►EMS Console Redirection Settings**

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

**Out-of-Band Mgmt Port**

The feature selects a serial port in a client server to be used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are COM1 and **SOL**.

**Terminal Type**

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, VT100+, **VT-UTF8**, and ANSI.

**Bits per second**

This feature sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

**Flow Control**

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

**Data Bits****Parity****Stop Bits****►ACPI Settings**

Use this feature to configure Advanced Configuration and Power Interface (ACPI) power management settings for your system.

**WHEA Support**

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are Disabled and **Enabled**.

## ► Trusted Computing

*\*The features in the Trusted Computing section are displayed if a TPM 2.0 module is detected:*

### TPM20 Device Found

Vendor: IFX

Firmware Version: 5.51

### Configuration

#### Security Device Support

If this feature and the TPM jumper on the motherboard are both set to Enabled, onboard security devices will be enabled for TPM support to enhance data integrity and network security. Please reboot the system for a change on this setting to take effect. The options are Disable and **Enable**.

The following TPM information will be displayed:

- Active PCR banks
- Available PCR banks

*\*If the feature "Security Device Support" is enabled, the following features are available for configuration:*

#### SHA-1 PCR Bank

Use this item to disable or enable the SHA-1 Platform Configuration Register (PCR) bank for the installed TPM device. The options are Disabled and **Enabled**.

#### SHA256 PCR Bank

Use this item to disable or enable the SHA256 Platform Configuration Register (PCR) bank for the installed TPM device. The options are Disabled and **Enabled**.

#### Pending operation

Use this item to schedule a TPM-related operation to be performed by a security device for system data integrity. Your system will reboot to carry out a pending TPM operation. The options are **None** and TPM Clear.

#### Platform Hierarchy

Use this item to disable or enable platform hierarchy for platform protection. The options are Disabled and **Enabled**.

### **Storage Hierarchy**

Use this item to disable or enable storage hierarchy for cryptographic protection. The options are Disabled and **Enabled**.

### **Endorsement Hierarchy**

Use this item to disable or enable endorsement hierarchy for privacy control. The options are Disabled and **Enabled**.

### **TPM2.0 UEFI Spec Version**

Use this feature to specify the TPM UEFI spec version. TCG 1.2 has support for Windows® 2012, Windows 8, and Windows 10. TCG 2 has support for Windows 10 or later. The options are TCG\_1\_2 and **TCG\_2**.

### **Physical Presence Spec Version**

Use this feature to select the PPI spec version. The options are 1.2 and **1.3**.

### **Device Select**

Use this feature to select the TPM version. TPM 1.2 will restrict support to TPM 1.2 devices. TPM 2.0 will restrict support for TPM 2.0 devices. Select Auto to enable support for both versions. The default setting is **Auto**.

## **► iSCSI Configuration**

### **iSCSI Initiator Name**

This feature allows the user to enter the unique name of the iSCSI Initiator in IQN format. Once the name of the iSCSI Initiator is entered into the system, configure the proper settings for the following items.

- **Add an Attempt**
- **Delete Attempts**
- **Change Attempt Order**

- ▶ Intel® I350 Gigabit Network Connection - OC:C4:7A:F8:1E:4A
- ▶ Intel® I350 Gigabit Network Connection - OC:C4:7A:F8:1E:4B
- ▶ Intel® I350 Gigabit Network Connection - OC:C4:7A:F8:1E:4C
- ▶ Intel® I350 Gigabit Network Connection - OC:C4:7A:F8:1E:4D
- ▶ Intel® Ethernet Connection X553/X557-AT 10GBASE-T - 00:A0...
- ▶ Intel® Ethernet Connection X553/X557-AT 10GBASE-T - 00:12...
- ▶ Intel® Ethernet Connection X553 10GbE SFP+ - 00:A0:C9:00...
- ▶ Intel® Ethernet Connection X553 10GbE SFP+ - 34:12:78:56...

These items display the following information :

### ▶ NIC Configuration

#### Link Speed

Use this feature to change the link speed and duplex for the current port. The options are **Auto Negotiated**, 10Mbps Half, 10Mbps Full, 100Mbps Half, and 100Mbps full.

#### Wake On LAN

Select enabled to wake the system with a magic packet. The options are **Enabled** and Disabled.

#### Blink LEDs

This feature allows the user to specify the duration for LEDs to blink. The range is from 0 ~ 15 seconds. The default setting is **0**.

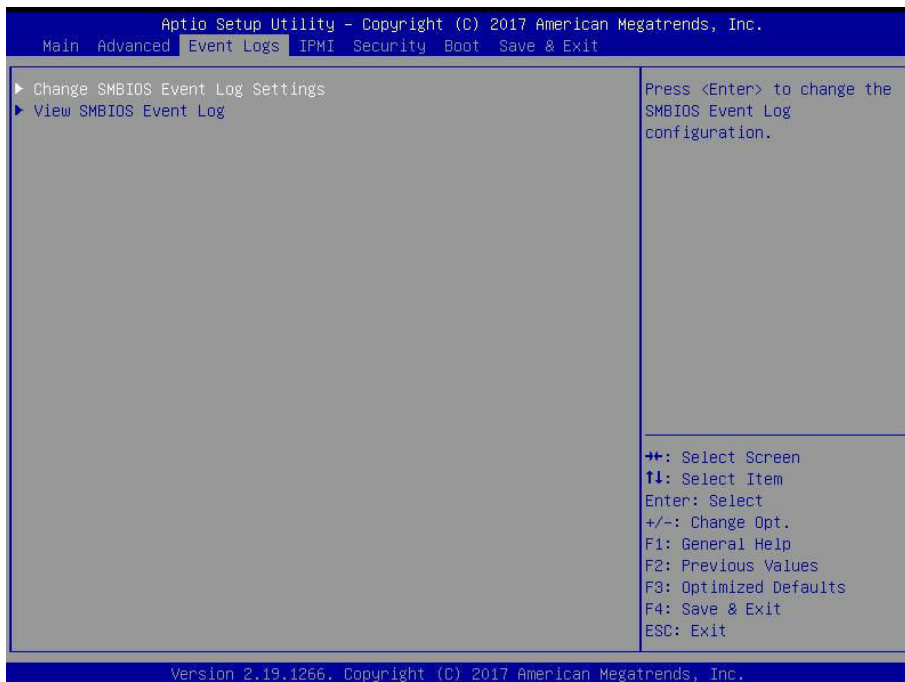
This section displays the following port information:

- UEFI Driver
- Adapter PBA
- Device Name
- Chip Type
- PCI Device ID
- PCI Address
- Link Status
- MAC Address
- Virtual MAC Address



## 4.4 Event Logs

Use this menu to configure Event Log settings.



### ► Change SMBIOS Event Log Settings

#### Enabling/Disabling Options

##### PCIe ELog Support

Use this feature to enable or disable PCIe error logging support. The options are Disabled and **Enabled**.

##### Memory ELog Support

Use this feature to enable or disable memory error logging support. The options are Disabled and **Enabled**.

##### SMBIOS Event Log

Change this item to enable or disable all features of the SMBIOS Event Logging during system boot. The options are **Enabled** and Disabled.

## Erasing Settings

### Erase Event Log

Select Enabled to erase all error events in the SMBIOS (System Management BIOS) log before an event logging is initialized at bootup. The options are **No**, Yes, Next reset, and Yes, Every reset.

### When Log is Full

Select Erase Immediately to immediately erase all errors in the SMBIOS event log when the event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and Erase Immediately.

## SMBIOS Event Log Standard Settings

### Log System Boot Event

Select Enabled to log system boot events. The options are Enabled and **Disabled**.

### MECI (Multiple Event Count Increment)

Enter the increment value for the multiple event counter. Enter a number between 1 to 255. The default setting is **1**.

### METW (Multiple Event Count Time Window)

This item is used to determine how long (in minutes) should the multiple event counter wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.



**Note:** Please reboot the system for the changes to take effect.

## ►View SMBIOS Event Log

This item allows the user to view the event in the SMBIOS event log. The following categories are displayed:

**DATE/TIME/ERROR CODE/SEVERITY**

## 4.5 IPMI

Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.



### BMC Firmware Revision

This item indicates the IPMI firmware revision used in your system.

### IPMI STATUS

This item indicates the status of the IPMI firmware installed in your system.

### ▶ System Event Log

#### Enabling/Disabling Options

#### SEL Components

Select Enabled for all system event logging at bootup. The options are Disabled and Enabled.

#### Erasing Settings

#### Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot. Select Yes, On every reset to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

### When SEL is Full

This feature allows the user to determine what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.



**Note:** After making changes on a setting, be sure to reboot the system for the changes to take effect.

## ► BMC Network Configuration

The following items will be displayed:

### IPMI LAN Selection

This feature displays the IPMI LAN setting. The default setting is **Failover**.

### IPMI Network Link Status

This feature displays the IPMI Network Link status. The default setting is **Dedicated LAN**.

### Update IPMI LAN Configuration

Select Yes for the BIOS to implement all IP/MAC address changes at the next system boot. The options are **No** and Yes.

***\*If the feature above is set to Yes, the Configuration Address Source and VLAN features are available for configuration:***

### Configuration Address Source

Use this feature to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are **DHCP** and Static.

The following items are assigned IP addresses automatically if DHCP is selected, or they can be configured manually if Static is selected.

### Station IP Address

This feature displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

### Subnet Mask

This feature displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

**Station MAC Address**

This feature displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

**Gateway IP Address**

This feature displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

**VLAN**

This feature is configurable if the Update IPMI LAN Configuration feature is set to Yes. Use this feature to enable or disable the IPMI VLAN function. The options are **Disable** and **Enable**.

***\*If the feature above is enabled, the VLAN feature below is available for configuration:***

**VLAN ID**

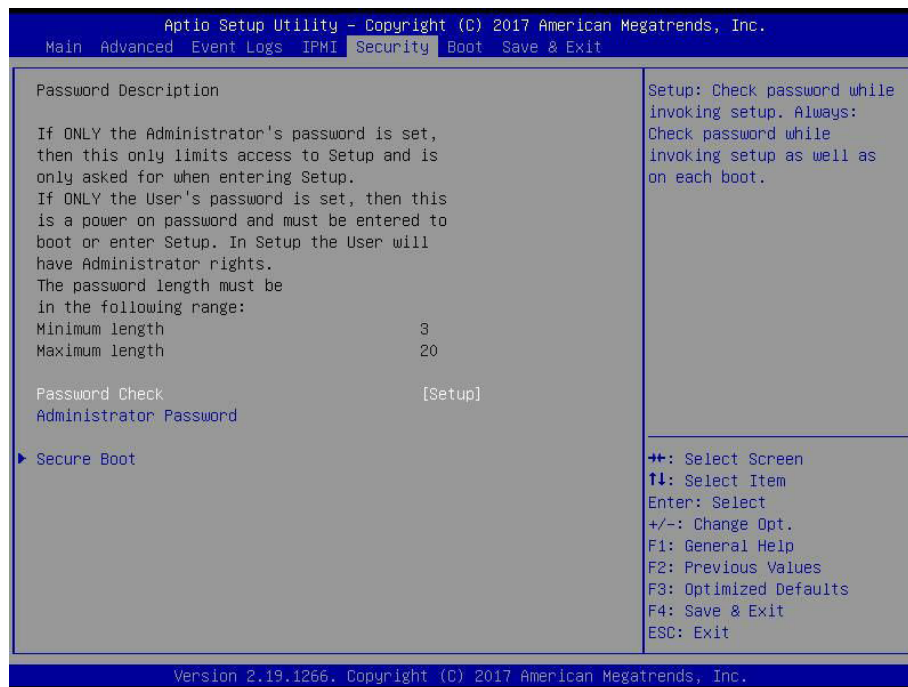
Use this feature to select a value for VLAN ID.

**IPMI Function Support**

Use this feature to enable IPMI support. The options are **Enabled** and **Disabled**. When disabled, the system powers on quicker by removing BIOS support for extended IPMI features. The Disable option is for applications that require faster power on time without using Supermicro Update Manager (SUM) or extended IPMI features. The BMC network configuration in the BIOS setup is also invalid when IPMI Function Support is disabled. The general BMC function and motherboard health monitor such as fan control are still functioning even when this option is disabled.

## 4.6 Security

Use this menu to configure the security settings for the system.



### Password Check

Select Setup for the system to check for a password at Setup. Select Always for the system to check for a password at bootup or upon entering the BIOS Setup utility. The options are **Setup** and **Always**.

### Administrator Password

Use this feature to set the administrator password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

***\*The feature below is displayed if an Administrator Password is set:***

### User Password

Use this feature to set a user password.

#### ► Secure Boot

**Platform Mode** - Setup

**Secure Boot** - Not Active

**Vendor Keys** - Not Active

**Enable Secure Boot**

Select Enable for secure boot support to ensure system security at bootup. The options are **Disabled** and Enabled.

**Secure Boot Mode**

This feature allows the user to select the desired secure boot mode for the system. The options are Standard and **Custom**.

***\*If Secure Boot Mode is set to Customized, Key Management features are available for configuration:***

**CSM Support**

This feature is for manufacturing debugging purposes.

**▶ Reset to Setup Mode**

Select Yes to delete all Secure Boot key databases and force the system to Setup Mode. The options are Yes and No.

**▶ Restore Factory Keys**

Select Yes to restore all factory keys to the default settings. The options are Yes and No.

**▶ Key Management**

This submenu allows the user to configure the following Key Management settings.

**Provision Factory Defaults**

Select Enabled to install the default Secure Boot keys set by the manufacturer. The options are **Disabled** and Enabled.

**▶ Restore Factory Keys**

Select Yes to restore all factory keys to the default settings. The options are Yes and No.

**▶ Enroll Efi Image**

This feature allows the image to run in Secure Boot mode.

**▶ Save All Secure Boot Variables**

This feature allows the user to decide if all secure boot variables should be saved.

**▶ Platform Key (PK)**

This feature allows the user to configure the settings of the platform keys.

**Set New**

Select Yes to load the new platform keys (PK) from the manufacturer's defaults. Select No to load the platform keys from a file. The options are **Yes** and No.

▶ **Key Exchange Keys (KEK)**

**Set New**

Select Yes to load the KEK from the manufacturer's defaults. Select No to load the KEK from a file. The options are Yes and No.

**Append**

Select Yes to add the KEK from the manufacturer's defaults list to the existing KEK. Select No to load the KEK from a file. The options are Yes and No.

▶ **Authorized Signatures**

**Set New**

Select Yes to load the database from the manufacturer's defaults. Select No to load the DB from a file. The options are Yes and No.

**Append**

Select Yes to add the database from the manufacturer's defaults to the existing DB. Select No to load the DB from a file. The options are Yes and No.

▶ **Forbidden Signatures**

**Set New**

Select Yes to load the DBX from the manufacturer's defaults. Select No to load the DBX from a file. The options are Yes and No.

**Append**

Select Yes to add the DBX from the manufacturer's defaults to the existing DBX. Select No to load the DBX from a file. The options are Yes and No.

▶ **Authorized TimeStamps**

**Set New**

Select Yes to load the DBT from the manufacturer's defaults. Select No to load the DBT from a file. The options are Yes and No.

**Append**

Select Yes to add the DBT from the manufacturer's defaults list to the existing DBT. Select No to load the DBT from a file. The options are Yes and No.



**► OsRecovery Signatures****Set New**

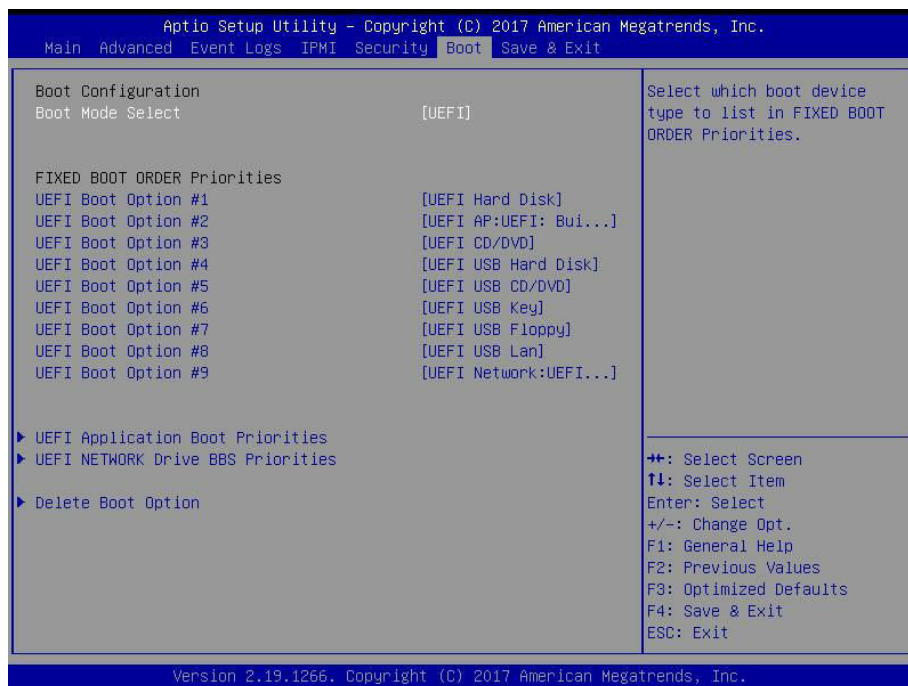
Select Yes to load the DBR from the manufacturer's defaults. Select No to load the DBR from a file. The options are Yes and No.

**Append**

Select Yes to add the DBR from the manufacturer's defaults list to the existing one. Select No to load the DBR from a file. The options are Yes and No.

## 4.7 Boot

Use this menu to configure Boot Settings:



### Fixed BOOT ORDER Priorities

This option prioritizes the order of bootable devices that the system to boot from. Press <Enter> on each entry from top to bottom to select devices.

- UEFI Boot Option #1
- UEFI Boot Option #2
- UEFI Boot Option #3
- UEFI Boot Option #4
- UEFI Boot Option #5
- UEFI Boot Option #6
- UEFI Boot Option #7
- UEFI Boot Option #8
- UEFI Boot Option #9

### ▶ UEFI Application Boot Priorities

- Boot Option # - This feature sets the system boot order of detected devices. The options are **[the list of detected boot device(s)]** and Disabled.

**►UEFI NETWORK Drive BBS Priorities**

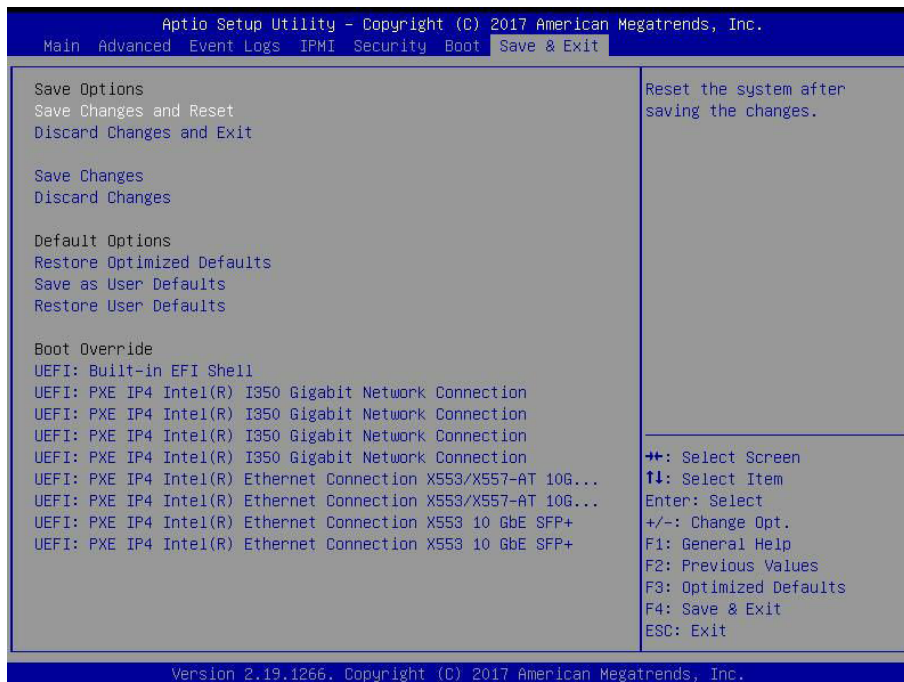
- Boot Option # - This feature sets the system boot order of detected devices. The options are **[the list of detected boot device(s)]** and Disabled.

**►Delete Boot Option**

Use this item to select a boot device to delete from the boot priority list.

## 4.8 Save & Exit

Select the Save & Exit tab from the BIOS setup screen to configure the settings below.



### Save Options

#### Save Changes and Reset

When you have completed the system configuration changes, select this option to save all changes made and reset the system.

#### Discard Changes and Exit

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

#### Save Changes

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system.

#### Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS Utility Program.

## Default Options

### Restore Optimized Defaults

To set this feature, select Restore Optimized Defaults and press <Enter>. These are factory settings designed for maximum system performance but not for maximum stability.

### Save as User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

### Restore User Defaults

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

### Boot Override

This feature allows the user to override the Boot Option Priorities sequence in the Boot menu and immediately boot the system with another device specified by the user. This is a one-time override.

### UEFI: Built-in EFI Shell

UEFI: PXE IP4 Intel® I350 Gigabit Network Connection

UEFI: PXE IP4 Intel® I350 Gigabit Network Connection

UEFI: PXE IP4 Intel® I350 Gigabit Network Connection

UEFI: PXE IP4 Intel® I350 Gigabit Network Connection

UEFI: PXE IP4 Intel® Ethernet Connection X553/X557-AT 10G...

UEFI: PXE IP4 Intel® Ethernet Connection X553/X557-AT 10G...

UEFI: PXE IP4 Intel® Ethernet Connection X553 10 GbE SFP+

UEFI: PXE IP4 Intel® Ethernet Connection X553 10 GbE SFP+

## Appendix A

### BIOS Codes

#### A.1 BIOS Error POST (Beep) Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

**Non-fatal errors** are those which, in most cases, allow the system to continue the boot-up process. The error messages normally appear on the screen.

**Fatal errors** are those which will not allow the system to continue the boot-up process. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The table below lists some common errors and their corresponding beep codes encountered by users.

BIOS Beep (POST) Codes		
Beep Code	Error Message	Description
1 beep	Refresh	Circuits have been reset (Ready to power up)
5 short, 1 long	Memory error	No memory detected in system
5 short	Display memory read/write error	Video adapter missing or with faulty memory
1 long continuous	System OH	System overheat condition

## A.2 Additional BIOS POST Codes

The AMI BIOS supplies additional checkpoint codes, which are documented online at <http://www.supermicro.com/support/manuals/> ("AMI BIOS POST Codes User's Guide").

When BIOS performs the Power On Self Test, it writes checkpoint codes to I/O port 0080h. If the computer cannot complete the boot process, a diagnostic card can be attached to the computer to read I/O port 0080h (Supermicro p/n AOC-LPC80-20).

For information on AMI updates, please refer to <http://www.ami.com/products/>.

## Appendix B

### Software Installation


#### B.1 Installing Software Programs

The Supermicro website that contains drivers and utilities for your system is at <https://www.supermicro.com/wftp/driver/>. Some of these must be installed, such as the chipset driver.

After accessing the website, go into the CDR\_Images directory and locate the ISO file for your motherboard. Download this file to create a CD/DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.)

After creating a CD/DVD with the ISO files, insert the disk into the CD/DVD drive on your system and the display shown in Figure B-1 should appear.

Another option is to go to the Supermicro website at <http://www.supermicro.com/products/>. Find the product page for your motherboard here, where you may download individual drivers and utilities to your hard drive or a USB flash drive and install from there.

 **Note:** To install the Windows OS, please refer to the instructions posted on our website at <http://www.supermicro.com/support/manuals/>.

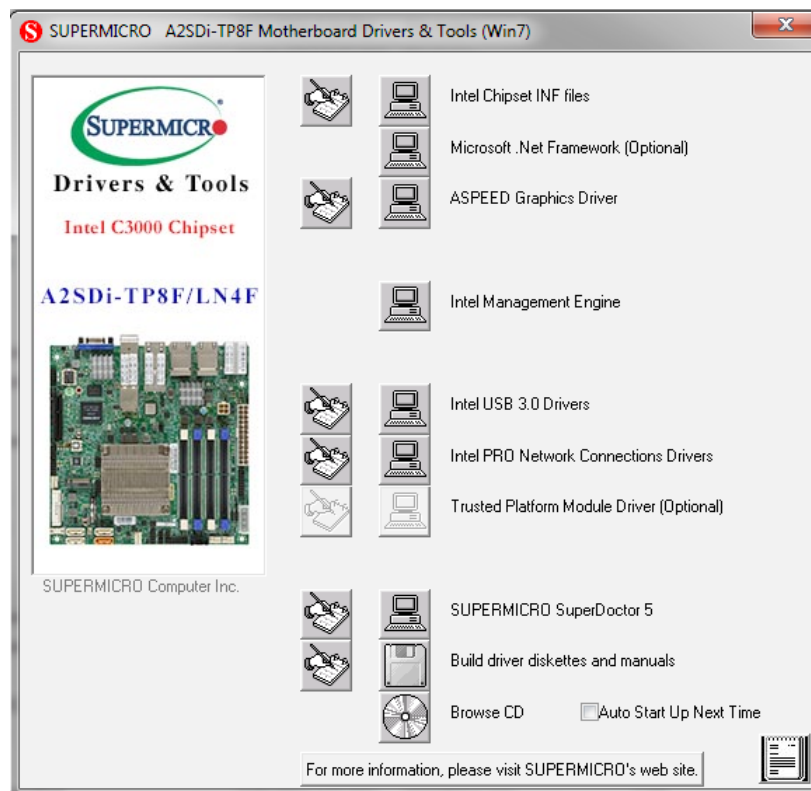


Figure B-1. Driver/Tool Installation Display Screen




Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to bottom) one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

When making a storage driver disk by booting into a driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver disk, be sure to change the SATA settings back to your original settings.

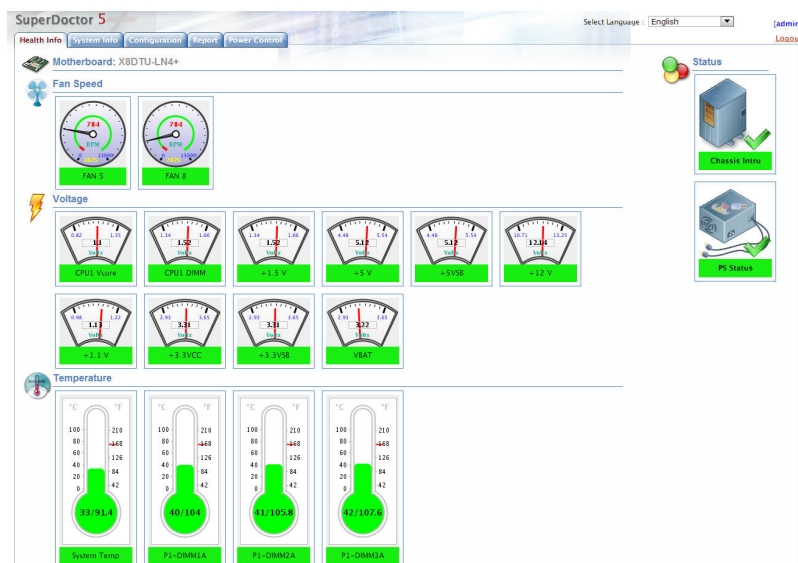
## B.2 SuperDoctor® 5


The Supermicro SuperDoctor 5 is a hardware monitoring program that functions in a command-line or web-based interface in Windows and Linux operating systems. The program monitors system health information such as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.

 **Note:** The default Username and Password for SuperDoctor 5 is admin / admin.

**Figure B-2. SuperDoctor 5 Interface Display Screen (Health Information)**



 **Note:** The SuperDoctor 5 program and user's manual can be downloaded from the Supermicro website at [http://www.supermicro.com/products/info/sms\\_sd5.cfm](http://www.supermicro.com/products/info/sms_sd5.cfm).

## Appendix C

### Standardized Warning Statements

The following statements are industry standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components.

These warnings may also be found on our website at [http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm).

#### Battery Handling



**Warning!** There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

#### 電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

#### 警告

電池更換不當會有爆炸危險。請只使用同類電池或製造商推薦的功能相當的電池更換原有電池。請按製造商的說明處理廢舊電池。

#### 警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

#### Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

**Attention**

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

**¡Advertencia!**

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

**אזהרה!**

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת. סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة اسبدال البطارية بطريقة غير صحيحة فعلياً  
اسبدال البطارية  
فقط بنفس النوع أو ما يعادلها مما أوصت به الشركة المصنعة  
جخلص من البطاريات المسحمة وفقاً لتعليمات الشركة الصانعة

**경고!**

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

**Waarschuwing**

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

## Product Disposal



**Warning!** Ultimate disposal of this product should be handled according to all national laws and regulations.

### 製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

### 警告

本产品的废弃处理应根据所有国家的法律和规章进行。

### 警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

### Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

### ¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

### Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר

אזהרה!

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القوانين واللوائح الوطنية

### 경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

### Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

## Appendix D

### UEFI BIOS Recovery

**Warning:** Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

#### D.1 Overview

The Unified Extensible Firmware Interface (UEFI) provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism that will allow the UEFI OS loader stored in an add-on card to boot the system. The UEFI offers clean, hands-off management to a computer during system boot.

#### D.2 Recovering the UEFI BIOS Image

A UEFI BIOS flash chip consists of a recovery BIOS block and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is first turned on, the boot block codes execute first. Once this process is completed, the main BIOS code will continue with system initialization and the remaining POST (Power-On Self-Test) routines.



**Note 1:** Follow the BIOS recovery instructions below for BIOS recovery when the main BIOS boot crashes.

**Note 2:** When the BIOS boot block crashes, you will need to follow the procedures to make a Returned Merchandise Authorization (RMA) request. (For a RMA request, please see section 3.5 for more information). Also, you may use the Supermicro Update Manager (SUM) Out-of-Band (OOB) ([https://www.supermicro.com.tw/products/nfo/SMS\\_SUM.cfm](https://www.supermicro.com.tw/products/nfo/SMS_SUM.cfm)) to reflash the BIOS.


## D.3 Recovering the BIOS Block with a USB Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

The file system supported by UEFI is FAT (including FAT12, FAT16, and FAT32) which is installed on a bootable or non-bootable USB-attached device. However, the BIOS might need several minutes to locate the SUPER.ROM file if the media size becomes too large due to the huge volumes of folders and files stored in the device.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.

1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "" directory of a USB device or a writable CD/DVD.

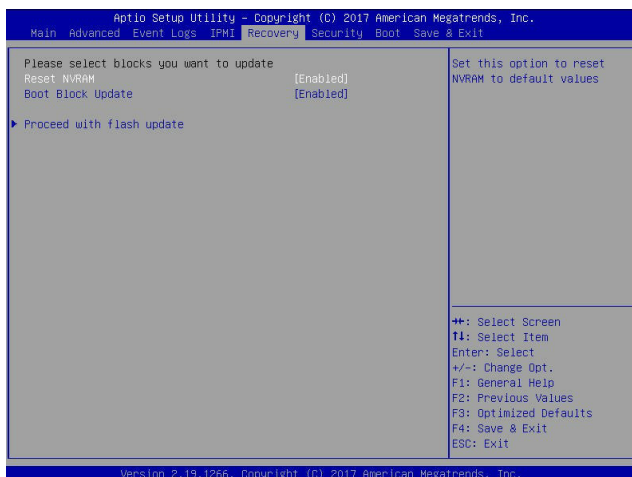
 **Note 1:** If you cannot locate the "Super.ROM" file in your driver disk, visit our website at [http://www.supermicro.com/ResourceApps/BIOS\\_IPMI\\_Intel.html](http://www.supermicro.com/ResourceApps/BIOS_IPMI_Intel.html) to download the BIOS image into a USB flash device and rename it "Super.ROM" for BIOS recovery use.


**Note 2:** Before recovering the main BIOS image, confirm that the "Super.ROM" binary image file you download is the same version or an old version intended for your motherboard.

2. Insert the USB device that contains the new BIOS image ("Super.ROM") into your USB drive and power on the system




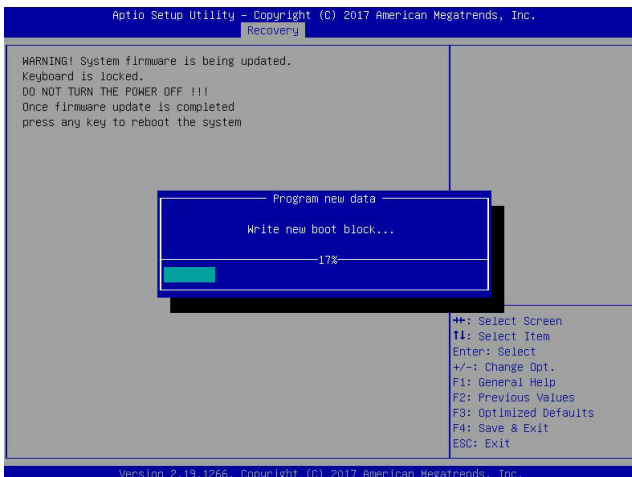
3. After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.



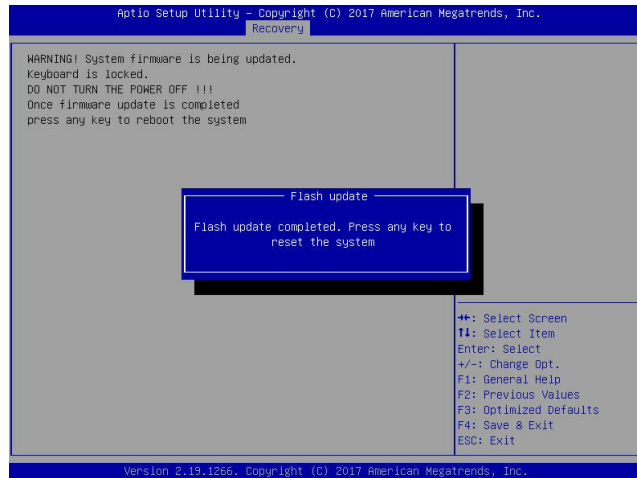
 **Note:** At this point, you may decide if you want to start the BIOS recovery. If you decide to proceed with BIOS recovery, follow the procedures below.

4. When the screen as shown above displays, use the arrow keys to select the item "Proceed with flash update" and press the <Enter> key. You will see the BIOS recovery progress as shown in the screen below.

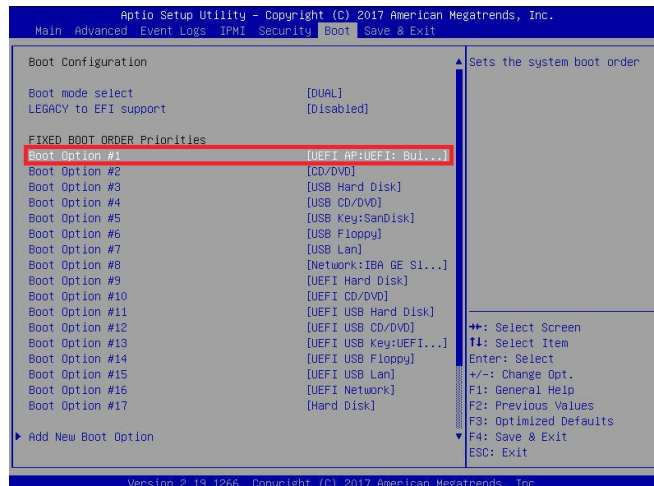
 **Note:** Do not interrupt the BIOS flashing process until it has completed.



- After the BIOS recovery process is completed, press the <Enter> key to reboot the system.



- Using a different system, extract the BIOS package into a USB flash drive.
- Press <Del> continuously during system boot to enter the BIOS setup utility. From the top of the tool bar, click on Boot and press <Enter> to enter the submenu. From the submenu list, select Boot Option #1 as shown below. Then, boot Option #1 to [UEFI AP:UEFI: Built-in EFI Shell]. Press <F4> to save the settings and exit the BIOS setup utility.





8. When the UEFI Shell prompt appears, type `fs#` to change the device directory path. Go to the directory that contains the BIOS package you extracted earlier from Step 7. Enter `flash.nsh BIOSname.###` at the prompt to start the BIOS update process.

```

UEFI Interactive Shell v2.1
EDK II
UEFI v2.50 (American Megatrends, 0x0005000C)
Mapping Table
FS0: Alias(s):HD(0):MBR:BLK1:
      PciRoot(0x0)/Pci(0x14,0x0)/USB(0x11,0x0)/HD(1,MBR,0x37910122,0x800,0x1
DR9592)
      BLK0: Alias(s):
      PciRoot(0x0)/Pci(0x14,0x0)/USB(0x11,0x0)
Press F8 in 1 seconds to skip startup.nsh or any other key to continue.
Shell> fs0:
FS0:\> cd \AFUDOS
FS0:\AFUDOS> cd \SNJPME2_03162017
FS0:\AFUDOS\SNJPME2_03162017> flash.nsh X110PU7_314

```



**Note:** Do not interrupt this process until the BIOS flashing is complete.

```

Done.
[ Access Cmos Port Ex ]
<Read>
Index 0x51: 0x10

Done.
*****
* Program BIOS and ME (including FDT) regions...
*****
| AMT Firmware Update Utility v5.09.01.1917
| Copyright (C)2017 American Megatrends Inc. All Rights Reserved.
|-----|
CPUID = 50652

Reading flash ..... done
- ME Data Size checking - ok
- FFS checksums ..... ok
- Check ROM layout ..... OK
Erasing Boot Block ..... done
Updating Boot Block ..... done
Verifying Boot Block ..... done
Erasing Main Block ..... 0x00132000 (0x)

```

9. The screen above indicates that the BIOS update process is completed. When you see the screen above, unplug the AC power cable from the power supply, clear CMOS, and plug the AC power cable in the power supply again to power on the system.

```

Verifying NDB Block ..... done
- Update success for FDR
- Update success for IE
- Successful Update Recovery Loader to OPRx11
- Successful Update MFSB11
- Successful Update FPR11
- Successful Update MFS, IVBI and IVB211
- Successful Update FLOG and UTDK11
- ME Entire Image update success !!
WARNING : System must power-off to have the changes take effect!
Moving FS0:\AFUDOS\SNJPME2_03162017\fdt\k64.efi -> FS0:\AFUDOS\SNJPME2_03162017\
d1.smc
- [ok]
Moving FS0:\AFUDOS\SNJPME2_03162017\afuef1k64.efi -> FS0:\AFUDOS\SNJPME2_0316201
7\afuef1.smc
- [ok]
*****
* Please ignore this 'Shell: Cannot read from file - Device Error'
* warning message due to it does not impact flashing process.
*****
Deleting "afuef1.smc"
Delete successful.
FS0:\>

```

10. Press `<Del>` continuously to enter the BIOS setup utility.
11. Press `<F3>` to load the default settings.
12. After loading the default settings, press `<F4>` to save the settings and exit the BIOS setup utility.