

GIGABYTE™

MZ33-CP0

Motherboard - AMD EPYC™ 9004 UP

User Manual

Rev. 1.0

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In order to assist in the use of this product, Giga Computing provides the following types of documentation:

- User Manual: detailed information & steps about the installation, configuration and use this product (e.g. motherboard, server barebones), covering hardware and BIOS.
- User Guide: detailed information about the installation & use of an add-on hardware or software component (e.g. BMC firmware, rail-kit) compatible with this product.
- Quick Installation Guide: a short guide with visual diagrams that you can reference easily for installation purposes of this product (e.g. motherboard, server barebones).

Please see the support section of the online product page to check the current availability of these documents

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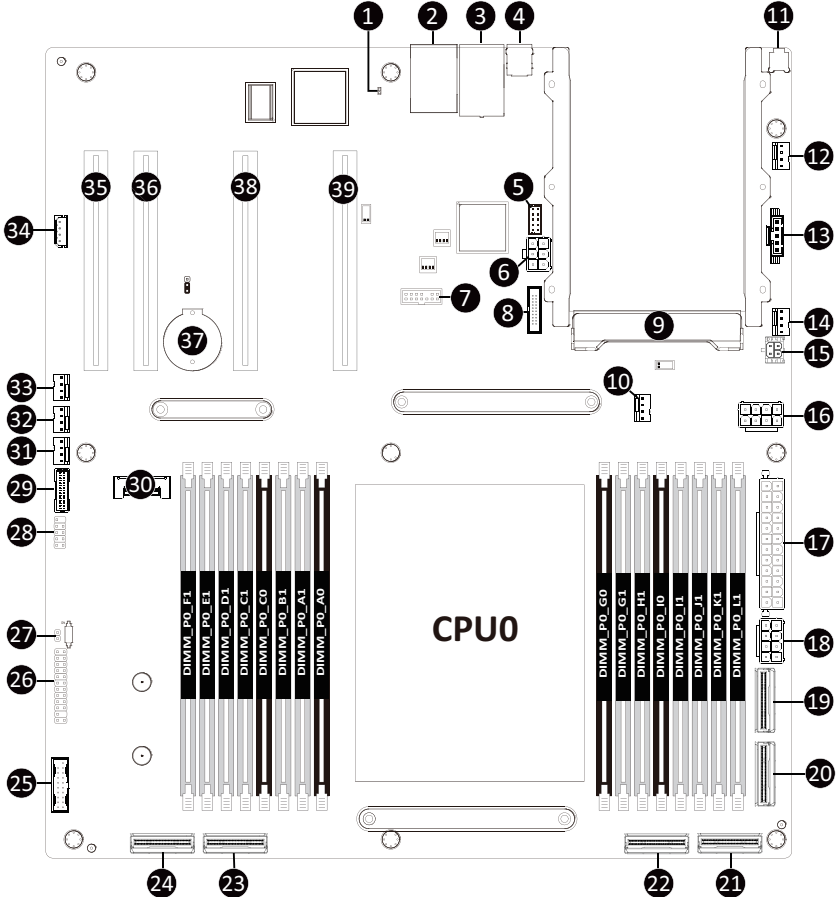
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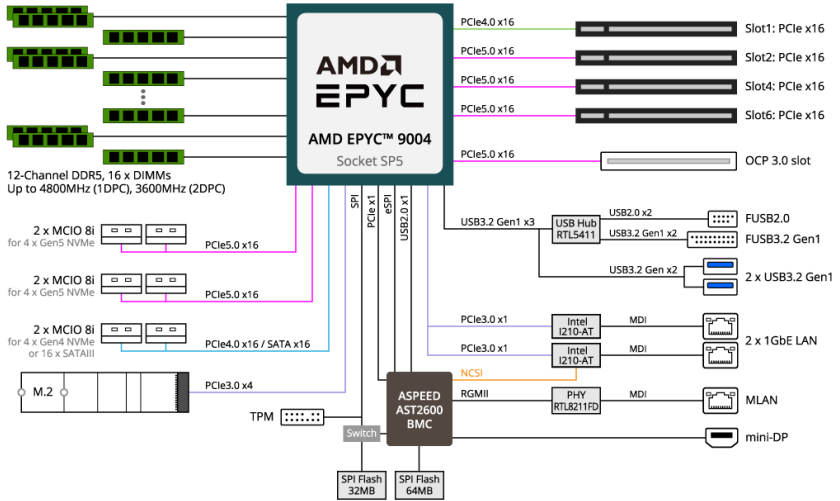
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MZ33-CP0 Motherboard Layout



Item	Code	Description
1	LED_BMC	BMC Firmware Readiness LED
2	USB3_MLAN	Server Management LAN Port (Top)/ USB3.2 Gen1 Ports (Bottom)
3	LAN_1_2	1GbE LAN Port #2 (Top)/ 1GbE LAN Port #1 (Bottom)
4	MINI_DP	Mini DisplayPort
5	COM	Serial Port Cable Connector
6	P12V_PCIE	2 x 3 Pin 12V PCIE Power Connector
7	SPI_TPM	TPM Connector
8	CN_NCSI	NCSI Connector
9	OCP	OCP 3.0 x16 Slot (Gen5 x16)
10	CPU0_FAN1	CPU Fan Connector
11	SW_ID	ID Button with LED
12	SYS_FAN4	System Fan Connector #4
13	PMBUS	PMBus Connector
14	SYS_FAN5	System Fan Connector #5
15	12V_STBY1	2 x 2 Pin 12V Standby Power Connector
16	P12V_AUX2	2 x 4 Pin 12V Power Connector
17	ATX1	2 x 12 Pin Main Power Connector
18	P12V_AUX1	2 x 4 Pin 12V Power Connector
19	U2_P0_G3A	MCIO Connector (PCIe Gen4/ SATA)
20	U2_P0_G3B	MCIO Connector (PCIe Gen4/ SATA)
21	U2_P0_G2A	MCIO Connector (PCIe Gen5)
22	U2_P0_G2B	MCIO Connector (PCIe Gen5)
23	U2_P0_G0A	MCIO Connector (PCIe Gen5)
24	U2_P0_G0B	MCIO Connector (PCIe Gen5)
25	F_USB3	Front Panel USB 3.2 Gen1 Connector
26	FP_1	Front Panel Header
27	CASE_OPEN	Case Open Intrusion Alert Header
28	F_USB2	Front Panel USB 2.0 Header
29	BP_1	HDD Backplane Board Connector
30	M2_0	M.2 slot (PCIe Gen3 x4, Support NGFF-2280/22110)
31	SYS_FAN1	System Fan Connector #1
32	SYS_FAN2	System Fan Connector #2
33	SYS_FAN3	System Fan Connector #3
34	IPMB	IPMB Connector
35	PCIE_1	PCIe x16 Slot #1 (Gen4 x16)
36	PCIE_2	PCIe x16 Slot #2 (Gen5 x16)
37	BAT	Battery Socket
38	PCIE_4	PCIe x16 Slot #4 (Gen5 x16)
39	PCIE_6	PCIe x16 Slot #6 (Gen5 x16)

Block Diagram



Chapter 1 Hardware Installation

1-1 Installation Precautions

The motherboard contains numerous delicate electronic circuits and components which can become damaged as a result of electrostatic discharge (ESD). Prior to installation, carefully read the user's manual and follow these procedures:

- Prior to installation, do not remove or break motherboard S/N (Serial Number) sticker or warranty sticker provided by your dealer. These stickers are required for warranty validation.
- Always remove the AC power by unplugging the power cord from the power outlet before installing or removing the motherboard or other hardware components.
- When connecting hardware components to the internal connectors on the motherboard, make sure they are connected tightly and securely.
- When handling the motherboard, avoid touching any metal leads or connectors.
- It is best to wear an electrostatic discharge (ESD) wrist strap when handling electronic components such as a motherboard, CPU or memory. If you do not have an ESD wrist strap, keep your hands dry and first touch a metal object to eliminate static electricity.
- Prior to installing the motherboard, please have it on top of an antistatic pad or within an electrostatic shielding container.
- Before unplugging the power supply cable from the motherboard, make sure the power supply has been turned off.
- Before turning on the power, make sure the power supply voltage has been set according to the local voltage standard.
- Before using the product, please verify that all cables and power connectors of your hardware components are connected.
- To prevent damage to the motherboard, do not allow screws to come in contact with the motherboard circuit or its components.
- Make sure there are no leftover screws or metal components placed on the motherboard or within the computer casing.
- Do not place the computer system on an uneven surface.
- Do not place the computer system in a high-temperature environment.
- Turning on the computer power during the installation process can lead to damage to system components as well as physical harm to the user.
- If you are uncertain about any installation steps or have a problem related to the use of the product, please consult a certified computer technician.

1-2 Product Specifications



NOTE:

We reserve the right to make any changes to the product specifications and product-related information without prior notice.

	Form Factor	<ul style="list-style-type: none"> ◆ E-ATX ◆ 304.8W x 330.2D mm
	CPU	<ul style="list-style-type: none"> ◆ AMD EPYC™ 9004 series processors ◆ AMD EPYC™ 9004 series processors with AMD 3D V-Cache™ Technology ◆ Single processor, 5nm technology ◆ Up to 128-core, 256 threads per processor ◆ cTDP up to 400W
	Socket	<ul style="list-style-type: none"> ◆ 1 x LGA 6096 ◆ Socket SP5
	Chipset	<ul style="list-style-type: none"> ◆ System on Chip
	Memory	<ul style="list-style-type: none"> ◆ 16 x DIMM slots ◆ DDR5 memory supported only ◆ 12-Channel memory architecture ◆ RDIMM modules up to 96GB supported ◆ 3DS RDIMM modules up to 256GB supported ◆ Memory speed: Up to 4800 MHz (1DPC), 3600 MHz (2DPC)
	LAN	<ul style="list-style-type: none"> ◆ 2 x 1Gb/s BASE-T LAN ports (2 x Intel® I210-AT) ◆ NCSI function supported (by switch) ◆ 1 x 10/100/1000 management LAN
	Onboard Graphics	<ul style="list-style-type: none"> ◆ Integrated in Aspeed® AST2600 ◆ 2D Video Graphic Adapter with PCIe bus interface ◆ 1920x1200@60Hz 32bpp, DDR4 SDRAM
	Storage Interface	<ul style="list-style-type: none"> ◆ 2x M.2 NVMe with 16 x SATA ports



Expansion Slots

- ◆ Slot_6: PCIe x16 (Gen5 x16 bus) slot
- ◆ Slot_4: PCIe x16 (Gen5 x16 bus) slot
- ◆ Slot_2: PCIe x16 (Gen5 x16 bus) slot
- ◆ Slot_1: PCIe x16 (Gen4 x16 bus) slot

- ◆ 1 x OCP 3.0 slot with PCIe Gen5 x16 bandwidth
 - NCSI function supported (by switch)

- ◆ 1 x M.2 slot:
 - M-key
 - PCIe Gen4 x4
 - Supports 2280/22110 cards

- ◆ *Total 5 x MCIO 8i connectors:
 - 4 x MCIO 8i connectors for 8 x Gen5 NVMe
 - 2 x MCIO 8i connectors for 4 x Gen4 NVMe or 16 x SATA 6Gb/s

***Please refer to the optional parts for qualified cables to ensure Gen5 signal quality.**



Internal I/O Connectors

- ◆ 1 x 24-pin ATX main power connector
- ◆ 2 x 8-pin ATX 12V power connectors
- ◆ 1 x 6-pin ATX 12V power connector
- ◆ 1 x CPU fan header
- ◆ 5 x System fan headers
- ◆ 1 x USB 3.2 Gen1 header
- ◆ 1 x USB 2.0 header
- ◆ 1 x TPM header
- ◆ 1 x Front panel header
- ◆ 1 x Backplane board header
- ◆ 1 x PMBus connector
- ◆ 1 x IPMB connector
- ◆ 1 x Clear CMOS jumper
- ◆ 1 x BIOS recovery jumper
- ◆ 1 x Buzzer





Rear I/O Connectors

- ◆ 2 x USB 3.2 Gen1
- ◆ 1 x Mini-DP
- ◆ 2 x RJ45
- ◆ 1 x MLAN
- ◆ 1 x ID button with LED



TPM

- ◆ 1 x TPM header with SPI interface
- ◆ Optional TPM2.0 kit: CTM010

	Board Management	<ul style="list-style-type: none"> ◆ Aspeed® AST2600 management controller ◆ GIGABYTE Management Console (AMI MegaRAC SP-X) web interface ◆ Dashboard ◆ HTML5 KVM ◆ Sensor Monitor (Voltage, RPM, Temperature, CPU Status ...etc.) ◆ Sensor Reading History Data ◆ FRU Information ◆ SEL Log in Linear Storage / Circular Storage Policy ◆ Hardware Inventory ◆ Fan Profile ◆ System Firewall ◆ Power Consumption ◆ Power Control ◆ Advanced power capping ◆ LDAP / AD / RADIUS Support ◆ Backup & Restore Configuration ◆ Remote BIOS/BMC/CPLD Update ◆ Event Log Filter ◆ User Management ◆ Media Redirection Settings ◆ PAM Order Settings ◆ SSL Settings ◆ SMTP Settings
	Operating Properties	<ul style="list-style-type: none"> ◆ Operating temperature: 10°C to 40°C ◆ Operating humidity: 8-80% (non-condensing) ◆ Non-operating temperature: -40°C to 60°C ◆ Non-operating humidity: 20%-95% (non-condensing)

1-3 Installing and Removing the CPU and Heat Sink



Read the following guidelines before you begin to install the CPU:

- Make sure that the motherboard supports the CPU.
- Always turn off the computer and unplug the power cord from the power outlet before installing the CPU to prevent hardware damage.
- Unplug all cables from the power outlets.
- Disconnect all telecommunication cables from their ports.
- Place the system unit on a flat and stable surface.
- Open the system according to the instructions.



WARNING!

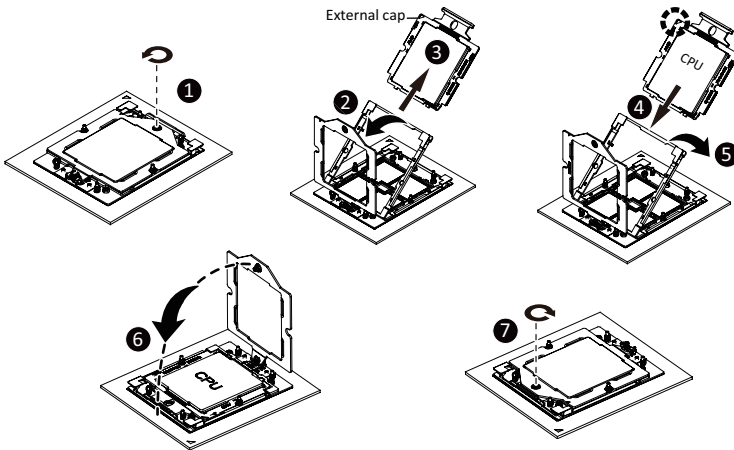
Failure to properly turn off the server before you start installing components may cause serious damage. Do not attempt the procedures described in the following sections unless you are a qualified service technician.

Follow these instructions to Install the CPU:

1. Loosen the captive screw securing the CPU cover.
2. Flip open the CPU cover.
3. Remove the CPU carrier from the CPU frame using the handle on the CPU carrier.
4. Using the handle on the CPU carrier insert the new CPU carrier with CPU installed into the CPU frame.

NOTE: Ensure the CPU is installed in the CPU carrier in the correct orientation, with the triangle on the CPU aligned to the top left corner of the CPU carrier.

5. Flip the CPU frame with CPU installed into place in the CPU socket.
6. Flip the CPU cover into place over the CPU socket.
7. Tighten the CPU cover screw to secure the CPU cover in place.



Note:

- Lock the CPU by using a Torx T20 screwdriver to tighten screw.
- The screw tightening torque: 12.5-15.0 kgf-cm.

1-4 Installing and Removing Memory

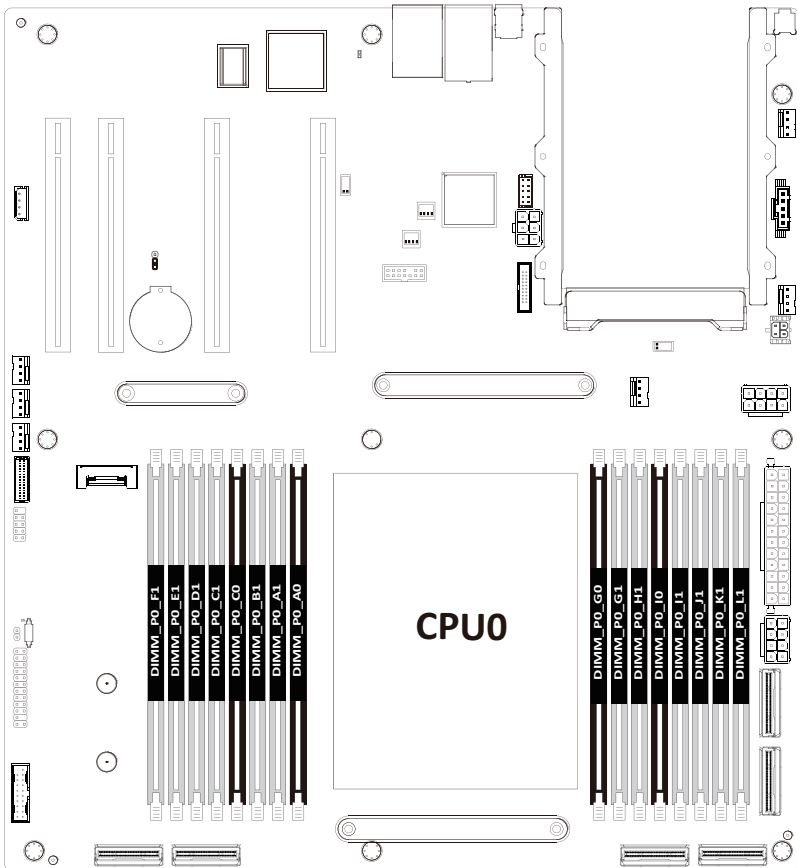


Read the following guidelines before you begin to install the memory:

- Make sure that the motherboard supports the memory. It is recommended that memory of the same capacity, brand, speed, and chips be used.
- Always turn off the computer and unplug the power cord from the power outlet before installing the memory to prevent hardware damage.
- Memory modules have a foolproof design. A memory module can be installed in only one direction. If you are unable to insert the memory, switch the direction.

1-4-1 12-Channel Memory Configuration

This motherboard provides 16 DDR5 memory slots and supports 12-Channel Technology. After the memory is installed, the BIOS will automatically detect the specifications and capacity of the memory.



1-4-2 Installing and Removing the Memory Module



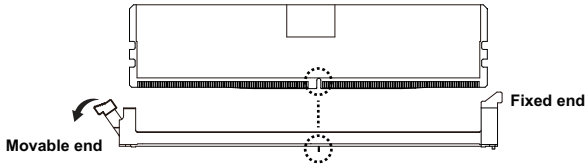
Before installing a memory module, make sure to turn off the computer and unplug the power cord from the power outlet to prevent damage to the memory module.

Be sure to install DDR5 DIMMs on this motherboard.

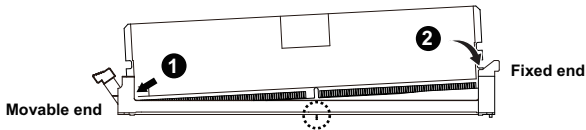
Make sure your DIMM slots have a single latch or a double latch.

Follow these instructions to install a DIMM module with Single Latch :

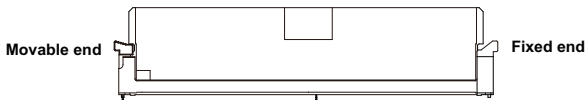
1. Open the plastic latch of the memory slot, then place the memory module as pre-inserted vertically position.



2. Hold it with both hands, insert the memory module into the movable end first, and then insert the memory module into the fixed end.



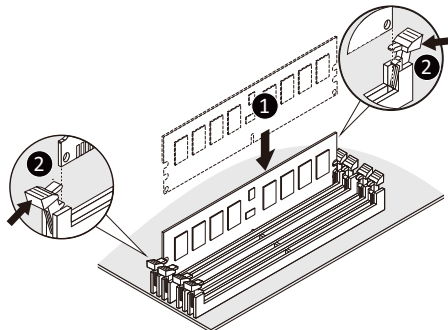
3. Then use both hands to insert the memory module vertically into the DIMM slot and push it down. Close the plastic latch at the edge of the DIMM slots to lock the memory module.



4. Reverse the installation steps when you want to remove the memory module.

Follow these instructions to install a DIMM module with Double Latch:

1. Insert the DIMM memory module vertically into the DIMM slot and push it down.
2. Close the plastic clip at both edges of the DIMM slots to lock the DIMM module.
3. Reverse the installation steps when you want to remove the DIMM module.



1-4-3 Processor and Memory Module Matrix Table

Memory Q'ty	CPU0															
	F1	E1	D1	C1	C0	B1	A1	A0	G0	G1	H1	I0	I1	J1	K1	L1
1 DIMM							v									
2 DIMM							v			v						
							v	v								
4 DIMM				v			v			v				v		
							v	v	v	v						
6 DIMM				v		v	v			v	v			v		
8 DIMM		v		v		v	v			v	v			v		v
				v	v		v	v	v	v			v	v		
10 DIMM		v	v	v		v	v			v	v			v	v	v
12 DIMM	v	v	v	v		v	v			v	v			v	v	v
16 DIMM	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v	v

1-4-4 Memory Population Table

EPYC Memory Speed based on DIMM Population (One DIMM per Channel)

DIMM Type	DIMM Population	Max EPYC 9004 DDR5 Frequency (MT/s)
	DIMM 0	
RDIMM	1R (1 Rank)	4800
	2R (2 Ranks)	4800
3DS RDIMM	2S2R (4 Ranks)	4800
	2S4R (8 Ranks)	4800
	2S8R (16 ranks)	4800

1-5 Installing the M.2 Device and Heat Sink



WARNING:

Installation of the thermal pad over the M.2 device is required when installing an M.2 device. Lack of the thermal pad may result in the system overheating and throttle the system performance.

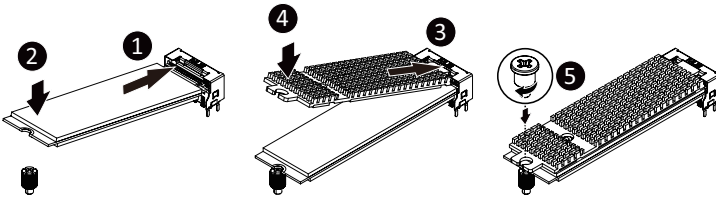


CAUTION

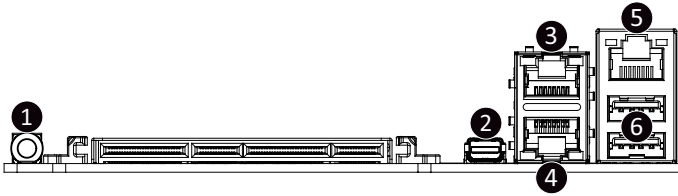
The position of the stand-off screw will depend on the size of the M.2 device. The stand-off screw is pre-installed for 22110 cards as standard. Refer to the size of the M.2 device and change the position of the stand-off screw accordingly.

Follow these instructions to install the M.2 device and heat sink:

1. Insert the M.2 device into the M.2 connector.
2. Press down on the M.2 device.
3. Install the thermal pad of the M.2 device to the M.2 device.
4. Press down on the thermal pad.
5. Secure the M.2 device and its thermal pad to the motherboard with a single screw.
6. Reverse steps 1-5 to remove the M.2 device.



1-6 Back Panel Connectors



1 ID button with LED

When the system identification is active, the ID LED on the front/ back panel glows blue.

2 Mini DisplayPort

Connect to a monitor device.

3 1GbE LAN Port #2

The LAN port provides Internet connection with data transfer speeds of 10/100/1000Mbps. See the section below for a description of the states of the LAN port LEDs.

4 1GbE LAN Port #1

The LAN port provides Internet connection with data transfer speeds of 10/100/1000Mbps. See the section below for a description of the states of the LAN port LEDs.

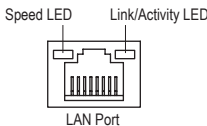
5 Server Management LAN Port

The LAN port provides Internet connection with data transfer speeds of 10/100/1000Mbps. This port is the dedicated LAN port for Server Management.

6 USB 3.2 Gen1 Ports

The USB port supports the USB 3.2 specification. Use this port for USB devices such as a USB keyboard/mouse, USB printer, USB flash drive etc.

LAN and ID Button LEDs



10/100/1000 LAN LED:

State	Description
Yellow On	1Gbps data rate
Green On	100Mbps data rate
Off	10Mbps data rate

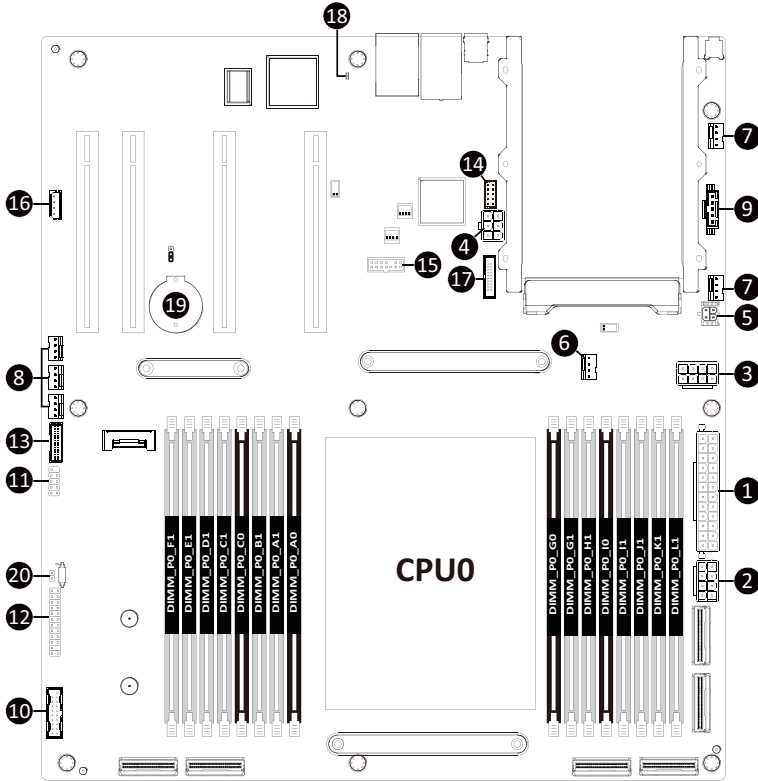
ID button/LED:

State	Description
Blue On	System identification is active
Off	System identification is disabled



- When removing the cable connected to a back panel connector, first remove the cable from your device and then remove it from the motherboard.
- When removing the cable, pull it straight out from the connector. Do not rock it side to side to prevent an electrical short inside the cable connector.

1-7 Internal Connectors



1) ATX1	11) F_USB2
2) P12V_AUX1	12) FP_1
3) P12V_AUX2	13) BP_1
4) P12V_PCIE	14) COM
5) 12V_STBY1	15) SPI_TPM
6) CPU0_FAN1	16) IPMB
7) SYS_FAN4/SYS_FAN5	17) CN_NSCI
8) SYS_FAN1/SYS_FAN2/SYS_FAN3	18) LED_BMC
9) PMBUS	19) BAT
10) F_USB3	20) CASE_OPEN



Read the following guidelines before connecting external devices:

- First make sure your devices are compliant with the connectors you wish to connect.
- Before installing the devices, be sure to turn off the devices and your computer. Unplug the power cord from the power outlet to prevent damage to the devices.
- After installing the device and before turning on the computer, make sure the device cable has been securely attached to the connector on the motherboard.

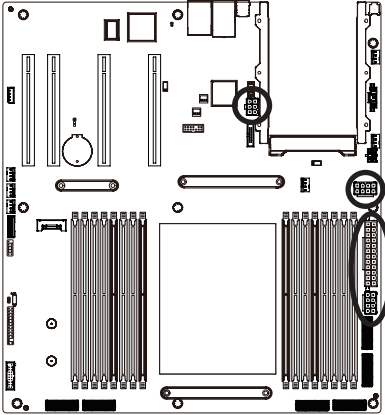
1/2/3/4) ATX1/P12V_AUX1/P12V_AUX2/P12V_PCIE/

(2x12 Main Power Connector and 2x4/2x3 12V Power Connector)

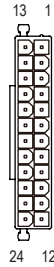
With the use of the power connector, the power supply can supply enough stable power to all the components on the motherboard. Before connecting the power connector, first make sure the power supply is turned off and all devices are properly installed. The power connector possesses a foolproof design. Connect the power supply cable to the power connector in the correct orientation. The 12V power connector mainly supplies power to the CPU. If the 12V power connector is not connected, the computer will not start.



To meet expansion requirements, it is recommended that a power supply that can withstand high power consumption be used (500W or greater). If a power supply is used that does not provide the required power, the result can lead to an unstable or unbootable system.

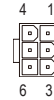


ATX1



Pin No.	Definition	Pin No.	Definition
1	3.3V	13	3.3V
2	3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power Good	20	NC
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	3.3V	24	GND

P12V_PCIE



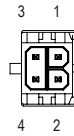
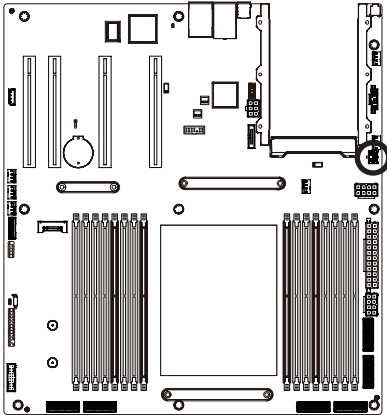
Pin No.	Definition
1	+12V
2	+12V
3	+12V
4	GND
5	GND
6	GND

P12V_AUX1/P12V_AUX2



Pin No.	Definition
1	GND
2	GND
3	GND
4	GND
5	+12V
6	+12V
7	+12V
8	+12V

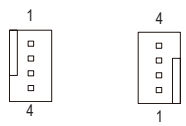
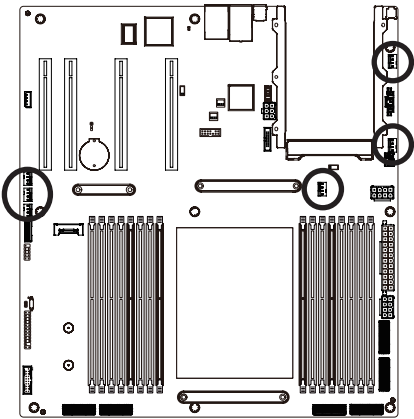
5) 12V_STBY1 (Standby Power Connector)



Pin No.	Definition
1	GND
2	GND
3	+12VSB
4	+12VSB

6) CPU_FAN1/SYS_FAN1/SYS_FAN2/SYS_FAN3/SYS_FAN4/SYS_FAN5 (FAN Headers)

The motherboard has one 4-pin CPU fan header (CPU_FAN), and five 4-pin (SYS_FAN) system fan headers. Most fan headers possess a foolproof insertion design. When connecting a fan cable, be sure to connect it in the correct orientation (the black connector wire is the ground wire). The motherboard supports CPU fan speed control, which requires the use of a CPU fan with fan speed control design. For optimum heat dissipation, it is recommended that a system fan be installed inside the chassis.



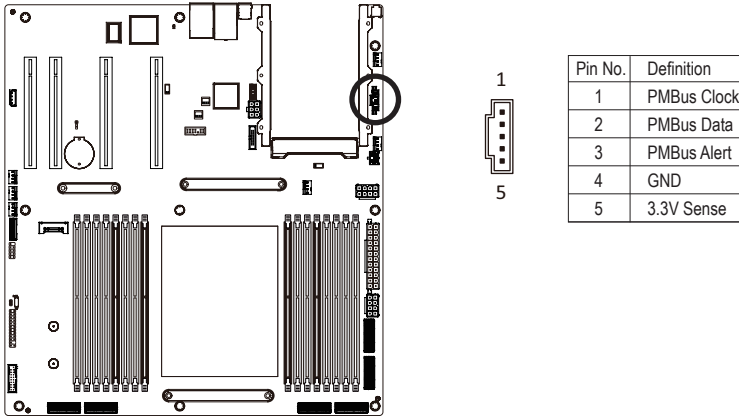
Pin No.	Definition
1	GND
2	+12V
3	Sense
4	Speed Control



- Be sure to connect fan cables to the fan headers to prevent your CPU and system from overheating. Overheating may result in damage to the CPU or the system may hang.
- These fan headers are not configuration jumper blocks. Do not place a jumper cap on the headers.

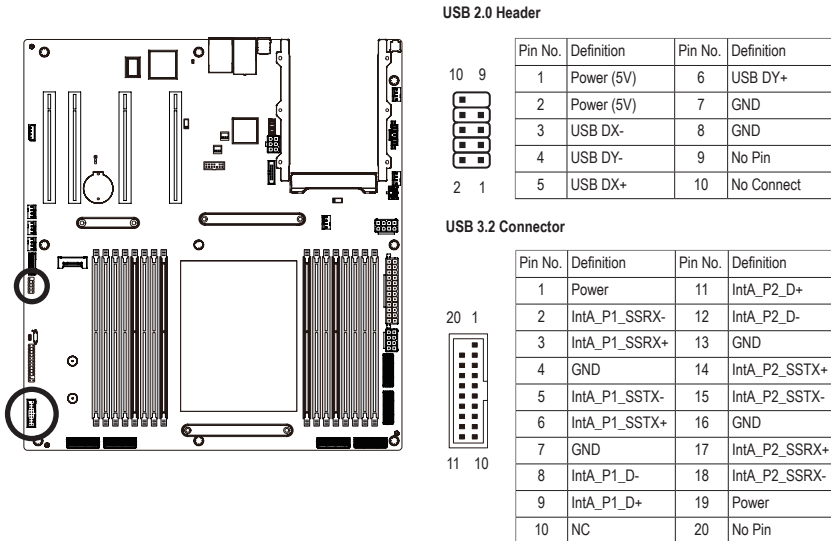
9) PMBus Connector

The Power Management Bus (PMBus) is a variant of the System Management Bus (SMBus) which is targeted at digital management of power supplies.



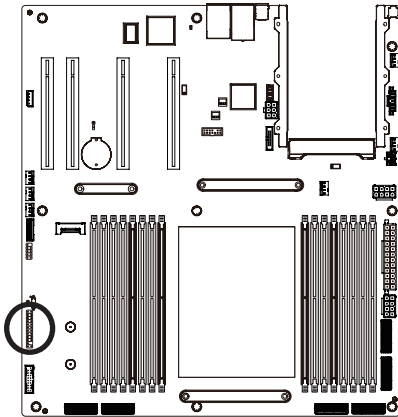
10/11) F_USB3/ F_USB2 (USB 3.2 Gen1 Connector/ 2.0 Header)

The connector/header conform to USB 2.0/ 3.0 specification. Each USB connector/header can provide two USB ports via an optional USB bracket. For purchasing the optional USB bracket, please contact the local dealer.



12) FP_1 (Front Panel Header)

Connect the power switch, reset switch, speaker, chassis intrusion switch/sensor and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.



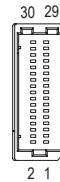
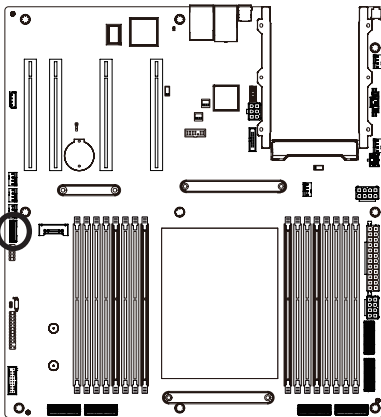
Pin No.	Definition	Pin No.	Definition
1	Power LED+	2	5V Standby
3	No Pin	4	ID LED+
5	Power LED-	6	ID LED-
7*	HDD LED+	8	System Status LED+
9*	HDD LED-	10	System Status LED-
11	Power Button	12	LAN1 Active LED+
13	GND	14	LAN1 Link LED-
15	Reset Button	16	SMBus Data
17	GND	18	SMBus Clock
19	ID Button	20	Case Open
21	GND	22	LAN2 Active LED+
23	NMI Switch	24	LAN2 Link LED-

*Note: Pin 7 & Pin 9 are reserved for Gigabyte systems.



The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assignments are matched correctly.

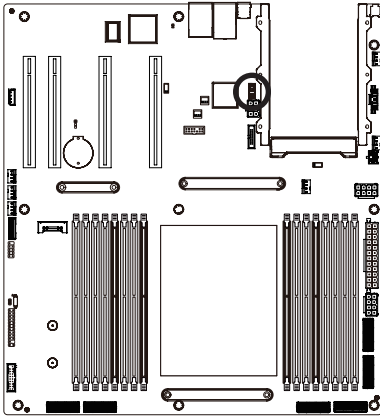
13) BP_1 (HDD Backplane Board Header)



Pin No.	Definition	Pin No.	Definition
1	Reserved	2	BPMI DIN/OUT
3	GND	4	BPMI DOUT/IN
5	BPMI_LOAD	6	GND
7	BPMI_CLK	8	PLD_Program_EN
9	GLED_AMB_N	10	GLED_GRN_N
11	FAN_IRQ_N	12	Reserved
13	BP_SCL	14	GND
15	BP_SDA	16	BP_RST_N
17	SMB_U2_TMP_SCL	18	GND
19	SMB_U2_TMP_SDA	20	I2C_DEV_RST
21	PH_HP_SCL0	22	GND
23	PH_HP_SDA0	24	GND
25	Reserved	26	GND
27	Reserved	28	GND
15	P3V3_AUX	30	P3V3_AUX

14) COM (Serial Port Header)

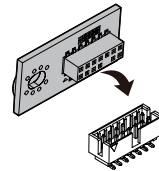
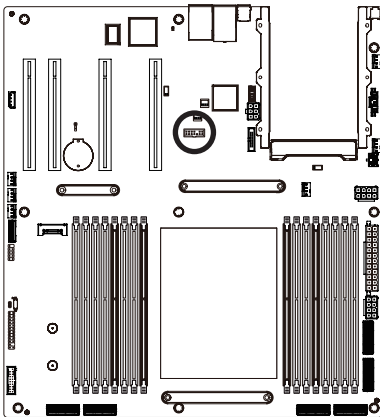
The COM header can provide one serial port via an optional COM port cable. For purchasing the optional COM port cable, please contact the local dealer.



Pin No.	Definition
1	NSINA
2	NDCDA_N
3	NDTRA_N
4	NSOUTA
5	NDSRA_N
6	GND
7	NCTSA_N
8	NRTSA_N
9	NA
10	NRIA_N

15) SPI_TPM (Trusted Platform Module Connector)

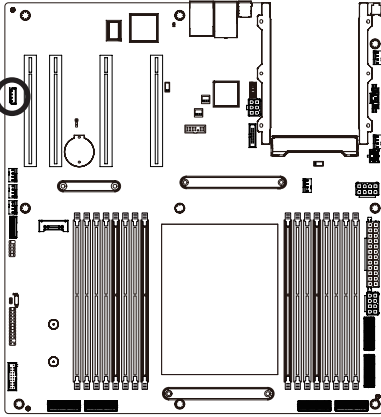
Trusted Platform Module (TPM) is an international standard for a secure cryptoprocessor, a dedicated microcontroller designed to secure hardware through integrated cryptographic keys.



Pin No.	Definition	Pin No.	Definition
1	Clock	8	NC
2	P_3V3_AUX	9	NC
3	LPC_RST	10	No Pin
4	NC	11	NC
5	SPI_MISO	12	GND
6	IRQ_SPI	13	SPI_CS_N
7	SPI_MOSI	14	GND

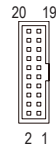
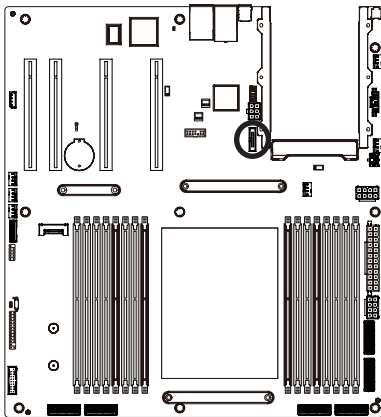
16) IPMB (Intelligent Platform Management Bus) Connector

The Intelligent Platform Management Bus Communications Protocol defines a byte-level transport for transferring Intelligent Platform Management Interface Specification (IPMI) messages between intelligent I2C devices.



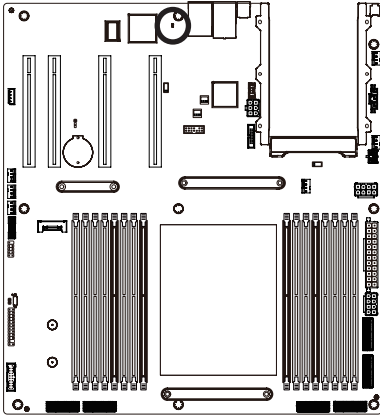
Pin No.	Definition
1	Clock
2	GND
3	Data
4	VCC

17) CN_NCSI (NCSI Connector)



Pin No.	Definition	Pin No.	Definition
1	NCSI_CLK	2	GND
3	NCSI_RX_D0	4	GND
5	NCSI_RX_D1	6	GND
7	NCSI_CRS_DV	8	GND
9	NCSI_RX_ER	10	GND
11	P3V3_AUX	12	GND
13	NCSI_TX_D1	14	GND
15	NCSI_TX_D0	16	GND
17	NCSI_TX_EN	18	GND
19	NCSI_PRESENT	20	P3V3_AUX

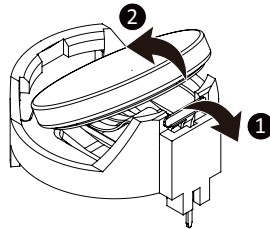
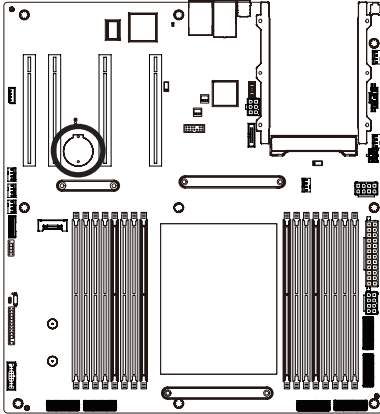
18) LED_BMC (BMC Firmware Readiness LED)



State	Description
On	BMC firmware is initial
Blink	BMC firmware is ready
Off	AC loss

19) BAT (Battery Socket)

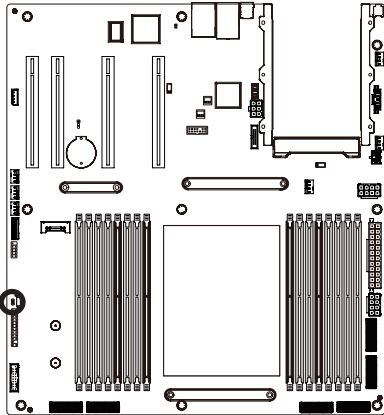
The battery provides power to keep the values (such as BIOS configurations, date, and time information) in the CMOS when the computer is turned off. Replace the battery when the battery voltage drops to a low level, or the CMOS values may not be accurate or may be lost.



- Always turn off your computer and unplug the power cord before replacing the battery.
- Replace the battery with an equivalent one. Danger of explosion if the battery is replaced with an incorrect model.
- Contact the place of purchase or local dealer if you are not able to replace the battery by yourself or uncertain about the battery model.
- Used batteries must be handled in accordance with local environmental regulations.

20) CASE_OPEN (Case Open Intrusion Alert Header)

This motherboard provides a chassis detection feature that detects if the chassis cover has been removed. This function requires a chassis with chassis intrusion detection design.



- Open: Normal Operation (Default)
- Closed: Active Chassis Intrusion Alert

1-8 Jumper Settings

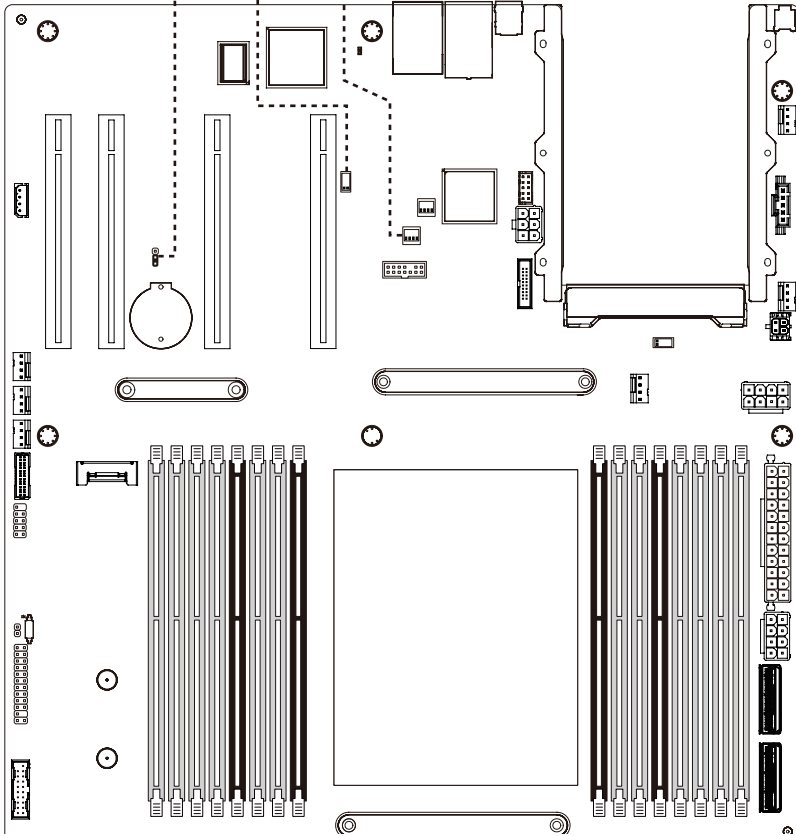
NCSI_SW

	SW1	SW2	
	OFF	OFF	LAN1
	ON	OFF	CN_NCSI
	ON	ON	OCF 3.0

Clear CMOS

3	2	1	Enable
			Default

J1		ON	OFF
1	HSMB_SEL	BIOS Defined	
2	PMBUS_SEL	BIOS Defined	
3	BIOS PWD	Clear supervisor password	Normal [Default]
4	BIOS RECOVERY	BIOS recovery mode	Normal [Default]



Chapter 2 BIOS Setup

BIOS (Basic Input and Output System) records hardware parameters of the system in the EFI on the motherboard. Its major functions include conducting the Power-On Self-Test (POST) during system startup, saving system parameters, loading the operating system etc. The BIOS includes a BIOS Setup program that allows the user to modify basic system configuration settings or to activate certain system features. When the power is turned off, the battery on the motherboard supplies the necessary power to the CMOS to keep the configuration values in the CMOS.

To access the BIOS Setup program, press the key during the POST when the power is turned on.



- BIOS flashing is potentially risky, if you do not encounter any problems when using the current BIOS version, it is recommended that you don't flash the BIOS. To flash the BIOS, do it with caution. Inadequate BIOS flashing may result in system malfunction.
- It is recommended that you not alter the default settings (unless you need to) to prevent system instability or other unexpected results. Inadequately altering the settings may result in system's failure to boot. If this occurs, try to clear the CMOS values and reset the board to default values. (Refer to the **Exit** section in this chapter or introductions of the battery/clearing CMOS jumper in Chapter 1 for how to clear the CMOS values.)

BIOS Setup Program Function Keys

<<-><->>	Move the selection bar to select the screen
<↑><↓>	Move the selection bar to select an item
<+>	Increase the numeric value or make changes
<->	Decrease the numeric value or make changes
<Enter>	Execute command or enter the submenu
<Esc>	Main Menu: Exit the BIOS Setup program Submenus: Exit current submenu
<F1>	Show descriptions of general help
<F3>	Restore the previous BIOS settings for the current submenus
<F9>	Load the Optimized BIOS default settings for the current submenus
<F10>	Save all the changes and exit the BIOS Setup program

■ **Main**

This setup page includes all the items of the standard compatible BIOS.

■ **Advanced**

This setup page includes all the items of AMI BIOS special enhanced features.

(ex: Auto detect fan and temperature status, automatically configure hard disk parameters.)

■ **AMD CBS**

This setup page includes the common items for configuration of AMD motherboard-related information.

■ **AMD PBS Option**

This setup page includes the common items for configuration of AMD CPM RAS related settings.

■ **Chipset**

This setup page includes all the submenu options for configuring the functions of the North Bridge.

■ **Server Management**

Server additional features enabled/disabled setup menus.

■ **Security**

Change, set, or disable supervisor and user password. Configuration supervisor password allows you to restrict access to the system and BIOS Setup.

A supervisor password allows you to make changes in BIOS Setup.

A user password only allows you to view the BIOS settings but not to make changes.

■ **Boot**

This setup page provides items for configuration of the boot sequence.

■ **Save & Exit**

Save all the changes made in the BIOS Setup program to the CMOS and exit BIOS Setup. (Pressing <F10> can also carry out this task.)

Abandon all changes and the previous settings remain in effect. Pressing <Y> to the confirmation message will exit BIOS Setup. (Pressing <Esc> can also carry out this task.)

2-1 The Main Menu

Once you enter the BIOS Setup program, the Main Menu (as shown below) appears on the screen. Use arrow keys to move among the items and press <Enter> to accept or enter other sub-menu.

Main Menu Help

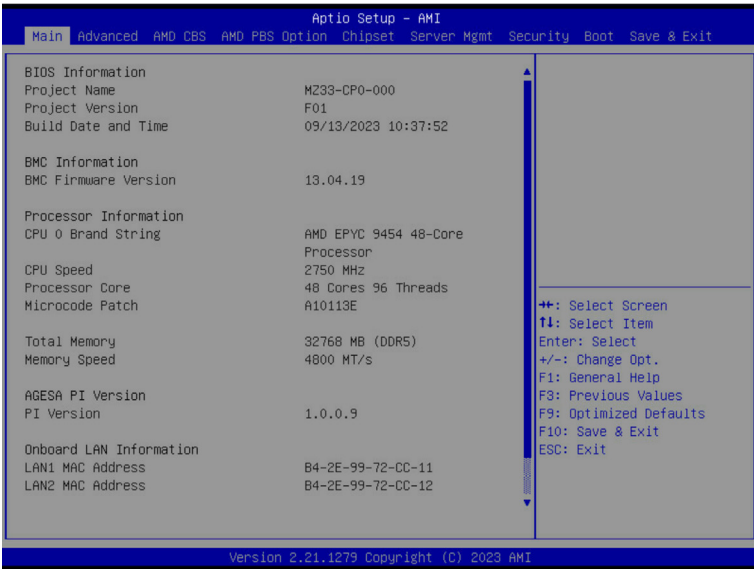
The on-screen description of a highlighted setup option is displayed on the bottom line of the Main Menu.

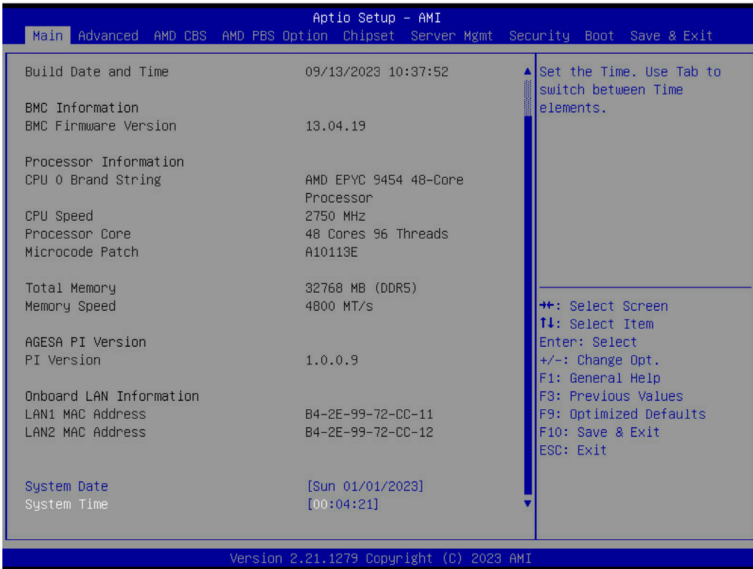
Submenu Help

While in a submenu, press <F1> to display a help screen (General Help) of function keys available for the menu. Press <Esc> to exit the help screen. Help for each item is in the Item Help block on the right side of the submenu.



- When the system is not stable as usual, select the **Restore Defaults** item to set your system to its defaults.
- The BIOS Setup menus described in this chapter are for reference only and may differ by BIOS version.





Parameter	Description
BIOS Information	
Project Name	Displays the project name information.
Project Version	Displays version number of the BIOS setup utility.
Build Date and Time	Displays the date and time when the BIOS setup utility was created.
BMC Information ^(Note1)	
BMC Firmware Version ^(Note1)	Displays BMC firmware version information.
Processor Information	
CPU Brand String / CPU Speed / Processor Core / Microcode Patch	Displays the technical specifications for the installed processor(s).
Total Memory ^(Note2)	Displays the total memory size of the installed memory.
Memory Speed ^(Note2)	Displays the frequency information of the installed memory.
VR Information Version	Displays VR version information.
AGESA PI Version	
PI Version	Displays AGESA PI version information.

(Note1) Functions available on selected models.

(Note2) This section will display capacity and frequency information of the memory that the customer has installed.

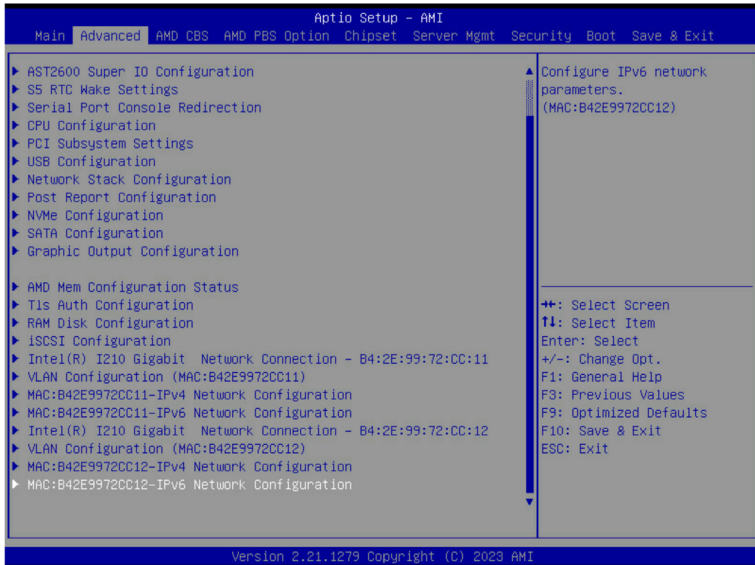
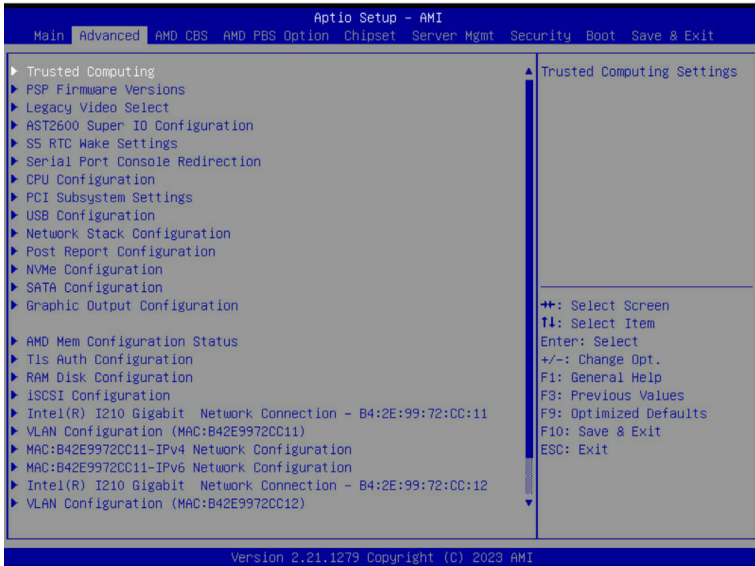
Parameter	Description
Onboard LAN Information	
LAN1/LAN2 MAC Address ^(Note)	Displays LAN MAC address information.
System Date	Sets the date following the weekday-month-day-year format.
System Time	Sets the system time following the hour-minute-second format.

(Note) The number of LAN ports listed will depend on the motherboard / system model.

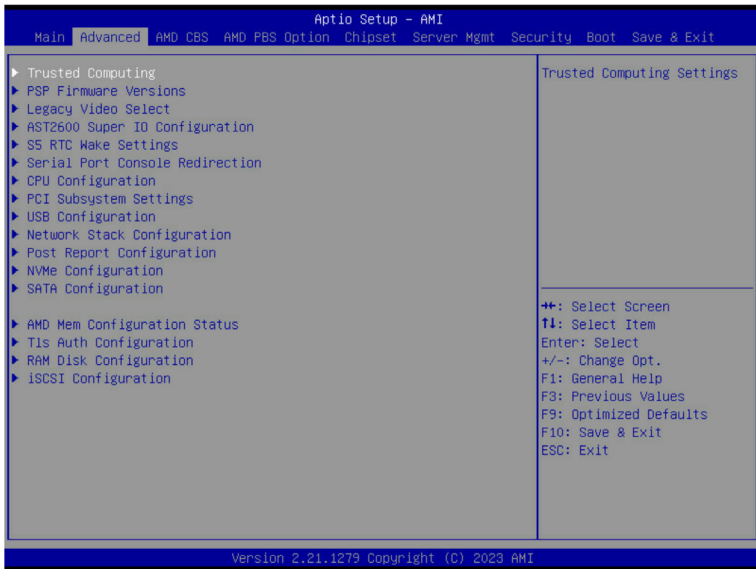
2-2 Advanced Menu

The Advanced Menu displays submenu options for configuring the function of various hardware components. Select a submenu item, then press <Enter> to access the related submenu screen.

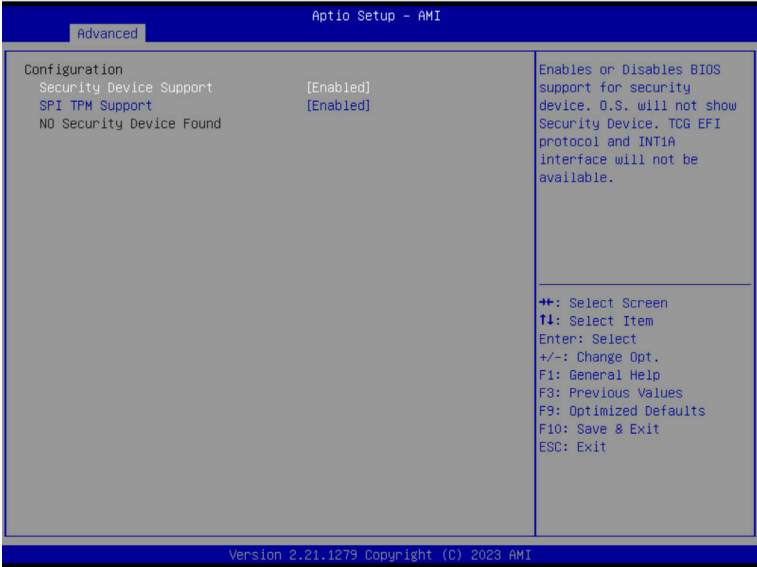
When Boot Mode Select is set to UEFI (Default)



When "Boot Mode Select" is set to Legacy in the Boot > Boot Mode Select section



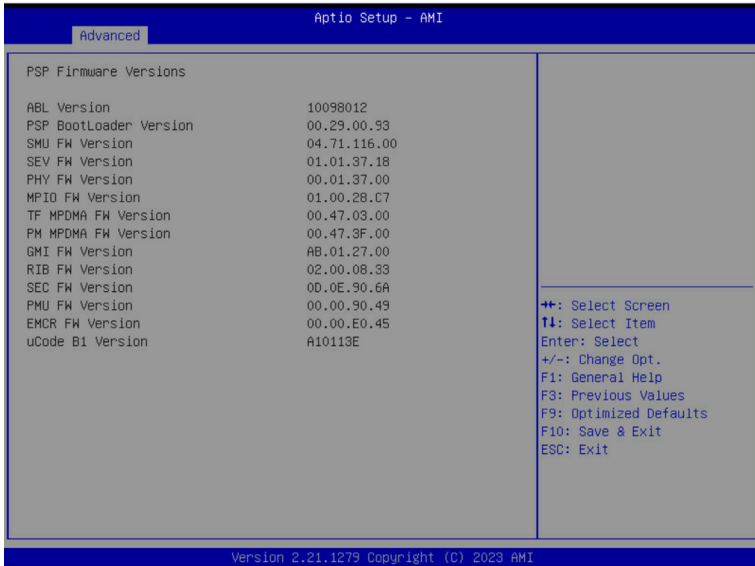
2-2-1 Trusted Computing



Parameter	Description
Configuration	
Security Device Support	<p>Enable/Disable BIOS support for security device. OS will not show security device. TCG EFI protocol and INT1A interface will not be available.</p> <p>Options available: Disabled, Enabled. Default setting is Enabled.</p>
SPI TPM Support	<p>Select Enable to activate TPM support feature.</p> <p>Options available: Disabled, Enabled. Default setting is Enabled.</p>

2-2-2 PSP Firmware Versions

The PSP Firmware Versions page displays the basic PSP firmware version information. Items on this window are non-configurable.



2-2-3 Legacy Video Select



Parameter	Description
OnBrd/Ext VGA Select	Selects between onboard or external VGA support. Options available: Auto, Onboard, External. Default setting is Auto .

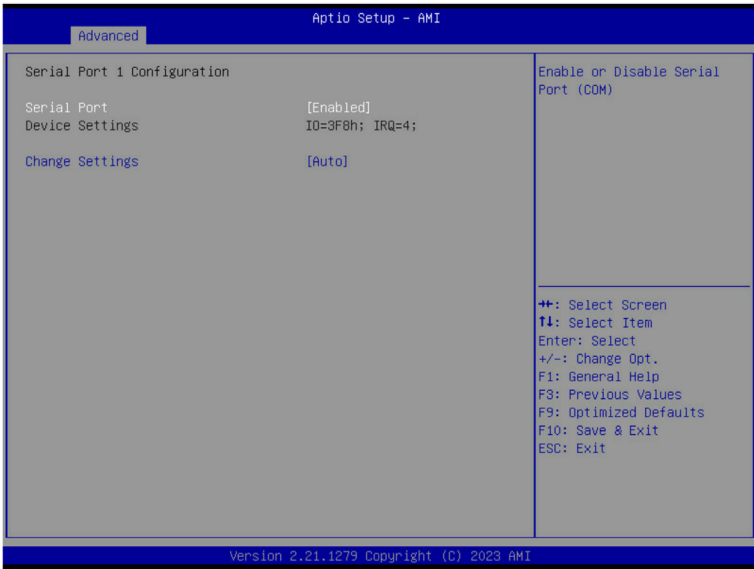
(Note) This configurable option will be displayed when "Boot Mode Select" is set to **Legacy** in the **Boot > Boot Mode Select** section.

2-2-4 AST2600 Super IO Configuration



Parameter	Description
AST2600 Super IO Configuration	
Super IO Chip	Displays the super IO chip information
Serial Port 1 Configuration	Press [Enter] for configuration of advanced items.

2-2-4-1 Serial Port 1 Configuration



Parameter	Description
Serial Port 1 Configuration	
Serial Port ^(Note)	Enable/Disable the Serial Port (COM). When set to Enabled allows you to configure the Serial port 1 settings. When set to Disabled, displays no configuration for the serial port. Options available: Disabled, Enabled. Default setting is Enabled .
Devices Settings	Displays the Serial Port 1 device settings.
Change Settings	Select an optimal settings for Super IO Device. Options available for Serial Port 1: Auto IO=3F8h; IRQ=4; IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; Default setting is Auto .

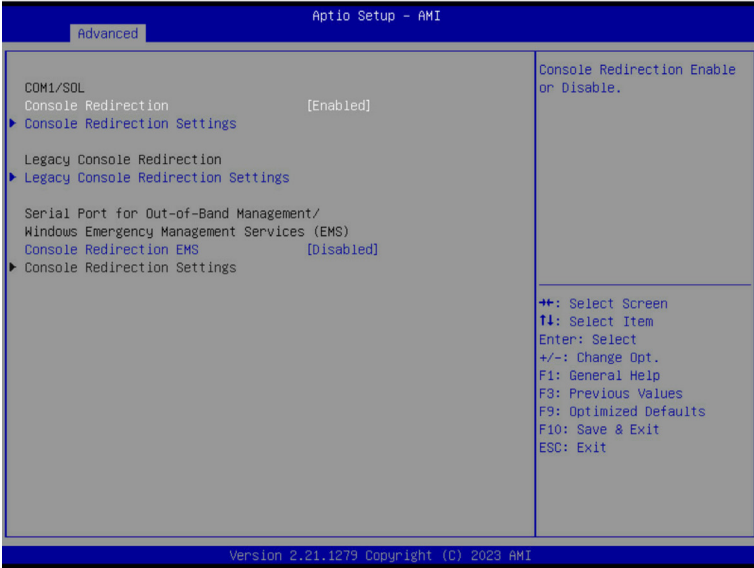
(Note) Advanced items prompt when this item is defined.

2-2-5 S5 RTC Wake Settings



Parameter	Description
Wake System from S5	Enable/Disable system wake on alarm event. Options available: Disabled, Fixed Time, Dynamic Time. When Fixed Time is selected, system will wake on the hr::min::sec specified. Default setting is Disabled .

2-2-6 Serial Port Console Redirection



Parameter	Description
COM1/Serial Over LAN Console Redirection ^(Note)	<p>Select whether to enable console redirection for specified device. Console redirection enables the users to manage the system from a remote location.</p> <p>Options available: Enabled, Disabled. Default setting is Disabled.</p>
COM1/Serial Over LAN Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p>Please note that this item is configurable when COM1/Serial Over LAN Console Redirection is set to Enabled.</p> <ul style="list-style-type: none"> ◆ Terminal Type <ul style="list-style-type: none"> – Selects a terminal type to be used for console redirection. – Options available: VT100, VT100Plus, ANSI, VT-UTF8. Default setting is VT100Plus. ◆ Bits per second <ul style="list-style-type: none"> – Selects the transfer rate for console redirection. – Options available: 9600, 19200, 38400, 57600, 115200. Default setting is 115200. ◆ Data Bits <ul style="list-style-type: none"> – Selects the number of data bits used for console redirection. – Options available: 7, 8. Default setting is 8.

(Note) Advanced items prompt when this item is defined.

Parameter	Description
COM1/Serial Over LAN Console Redirection Settings (continued)	<ul style="list-style-type: none"> ◆ Parity <ul style="list-style-type: none"> – A parity bit can be sent with the data bits to detect some transmission errors. – Even: parity bit is 0 if the num of 1's in the data bits is even. – Odd: parity bit is 0 if num of 1's in the data bits is odd. – Mark: parity bit is always 1. Space: Parity bit is always 0. – Mark and Space Parity do not allow for error detection. – Options available: None, Even, Odd, Mark, Space. Default setting is None. ◆ Stop Bits <ul style="list-style-type: none"> – Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. – Options available: 1, 2. Default setting is 1. ◆ Flow Control <ul style="list-style-type: none"> – Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. – Options available: None, Hardware RTS/CTS. Default setting is None. ◆ VT-UTF8 Combo Key Support <ul style="list-style-type: none"> – Enable/Disable the VT-UTF8 Combo Key Support. – Options available: Enabled, Disabled. Default setting is Enabled. ◆ Recorder Mode <ul style="list-style-type: none"> – When this mode enabled, only texts will be send. This is to capture Terminal data. – Options available: Enabled, Disabled. Default setting is Disabled. ◆ Resolution 100x31 <ul style="list-style-type: none"> – Enable/Disable extended terminal resolution. – Options available: Enabled, Disabled. Default setting is Enabled. ◆ Putty KeyPad <ul style="list-style-type: none"> – Selects Function Key and KeyPad on Putty. – Options available: VT100, LINUX, XTERMR6, SC0, ESCN, VT400. Default setting is VT100.

Parameter	Description
Legacy Console Redirection	
Legacy Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Redirection COM Port <ul style="list-style-type: none"> – Selects a COM port for Legacy serial redirection. – Default setting is COM1/SOL. ◆ Resolution <ul style="list-style-type: none"> – Selects the number of rows and columns used in Console Redirection for legacy OS support. – Options available: 80x24, 80x25. Default setting is 80x24. ◆ Redirect After POST <ul style="list-style-type: none"> – When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. – Options available: Always Enable, BootLoader. Default setting is Always Enable.
Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS) Console Redirection ^(Note)	<p>EMS console redirection allows the user to configure Console Redirection Settings to support Out-of-Band Serial Port management.</p> <p>Options available: Disabled, Enabled. Default setting is Disabled.</p>
Serial Port for Out-of-Band EMS Console Redirection Settings	<p>Press [Enter] to configure advanced items.</p> <p>Please note that this item is configurable when Serial Port for Out-of-Band Management EMS Console Redirection is set to Enabled.</p> <ul style="list-style-type: none"> ◆ Out-of-Band Mgmt Port <ul style="list-style-type: none"> – Microsoft Windows Emergency Management Service (EMS) allows for remote management of a Windows Server OS through a serial port. – Default setting is COM1/SOL. ◆ Terminal Type <ul style="list-style-type: none"> – Selects a terminal type to be used for console redirection. – Options available: VT100, VT100Plus, ANSI, VT-UTF8. Default setting is ANSI. ◆ Bits per second <ul style="list-style-type: none"> – Selects the transfer rate for console redirection. – Options available: 9600, 19200, 57600, 115200. Default setting is 115200.

(Note) Advanced items prompt when this item is defined.

Parameter	Description
Serial Port for Out-of-Band EMS Console Redirection Settings(continued)	<ul style="list-style-type: none">◆ Flow Control<ul style="list-style-type: none">– Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.– Options available: None, Hardware RTS/CTS, Software Xon/Xoff. Default setting is None.

2-2-7 CPU Configuration



Parameter	Description
SVM Mode	Enable/Disable the CPU Virtualization. Options available: Disabled, Enabled. Default setting is Enabled .
CPU 0 Information	Press [Enter] to view the memory information related to CPU 0.

2-2-8 PCI Subsystem Settings

Aptio Setup - AMI

Advanced

PCI Bus Driver Version	A5.01.28	▲ Change PCIe_2 PCIe lanes.
PCIe_2	[Auto]	
PCIe_2 I/O ROM	[Enabled]	
PCIe_2 Link Speed	[Auto]	
U2_P0_G0	[x4 x4 x4 x4]	
U2_P0_G0 I/O ROM	[Enabled]	
U2_P0_G0 Link Speed	[Auto]	
PCIe_4	[Auto]	
PCIe_4 I/O ROM	[Enabled]	
PCIe_4 Link Speed	[Auto]	
PCIe_1	[Auto]	
PCIe_1 I/O ROM	[Enabled]	
PCIe_1 Link Speed	[Auto]	
PCIe_6	[Auto]	
PCIe_6 I/O ROM	[Enabled]	
PCIe_6	[Auto]	
U2_P0_G2	[x4 x4 x4 x4]	
U2_P0_G2 I/O ROM	[Enabled]	
U2_P0_G2	[Auto]	

▲ Select Screen
 T1: Select Item
 Enter: Select
 +/-: Change Opt.
 F1: General Help
 F3: Previous Values
 F9: Optimized Defaults
 F10: Save & Exit
 ESC: Exit

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Aptio Setup - AMI

Advanced

U2_P0_G2 I/O ROM	[Enabled]	▲ Enables or Disables PCI Express Device Relaxed Ordering.
U2_P0_G2	[Auto]	
OCp	[Auto]	
OCp I/O ROM	[Enabled]	
OCp Link Speed	[Auto]	
U2_P0_G3A	[SATA]	
U2_P0_G3A I/O ROM	[Enabled]	
U2_P0_G3A Link Speed	[Auto]	
U2_P0_G3B	[SATA]	
U2_P0_G3B I/O ROM	[Enabled]	
U2_P0_G3B Link Speed	[Auto]	
Onboard LAN Controller	[Enabled]	
Onboard LAN1 I/O ROM	[Enabled]	
Onboard LAN 2 Controller	[Enabled]	
Onboard LAN2 I/O ROM	[Enabled]	
PCI Devices Common Settings:		
Above 4G Decoding	[Enabled]	
SR-IOV Support	[Enabled]	
Relaxed Ordering	[Enabled]	

▲ Select Screen
 T1: Select Item
 Enter: Select
 +/-: Change Opt.
 F1: General Help
 F3: Previous Values
 F9: Optimized Defaults
 F10: Save & Exit
 ESC: Exit

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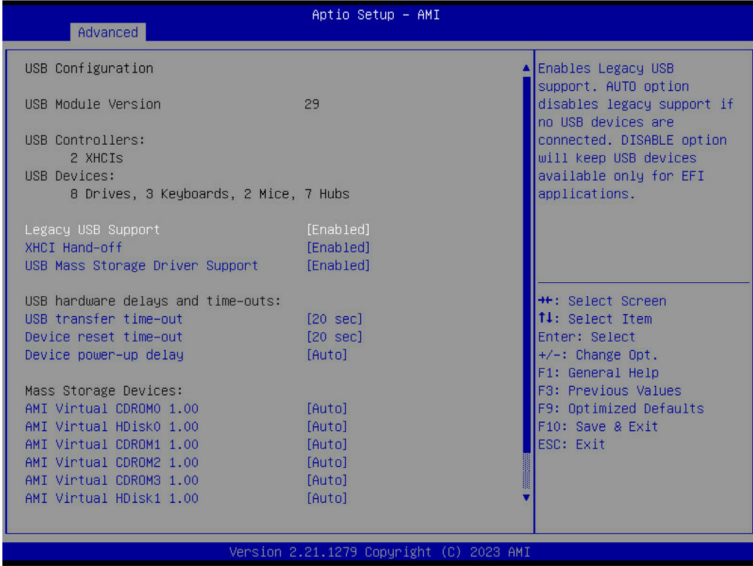
Parameter	Description
PCI Bus Driver Version	Displays the PCI Bus Driver version information.
PCIE_# ^(Note1)	Change PCIe lanes. Options available: Disabled, Auto, x16, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is Auto .
PCIE_# I/O ROM ^(Note1)	When enabled, this setting will initialize the device expansion ROM for the related devices. Options available: Disabled, Enabled. Default setting is Enabled .
PCIE_# Link Speed ^(Note1)	Configure MCIO slot max link speed. Options available: Auto, Gen4, Gen3, Gen2, Gen1. Default setting is Auto .
U2_P0_G0/ U2_P0_G2	Change MCIO PCIe lanes. Options available: Disabled, Auto, x16, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is x4x4x4x4 .
U2_P0_G0/ U2_P0_G2 I/O ROM	When enabled, this setting will initialize the device expansion ROM for the related devices. Options available: Disabled, Enabled. Default setting is Enabled .
U2_P0_G0/ U2_P0_G2 Link Speed	Configure MCIO slot max link speed. Options available: Auto, Gen4, Gen3, Gen2, Gen1. Default setting is Auto .
OCP	Change OCP PCIe lanes. Options available: Disabled, Auto, x16, x8x8, x8x4x4, x4x4x8, x4x4x4x4. Default setting is Auto .
OCP I/O ROM	When enabled, this setting will initialize the device expansion ROM for the related PCI-E slot. Options available: Disabled, Enabled. Default setting is Enabled .
OCP Link Speed	Configure OCP slot max link speed. Options available: Auto, Gen4, Gen3, Gen2, Gen1. Default setting is Auto .
U2_P0_G3A/ U2_P0_G3B	Change MCIO PCIe lanes. Options available: SATA, x4x4. Default setting is x4x4 .
U2_P0_G3A/ U2_P0_G3B I/O ROM	When enabled, this setting will initialize the device expansion ROM for the related devices. Options available: Disabled, Enabled. Default setting is Enabled .
U2_P0_G3A/ U2_P0_G3B Link Speed	Configure MCIO slot max link speed. Options available: Auto, Gen4, Gen3, Gen2, Gen1. Default setting is Auto .
Onboard LAN# Controller ^(Note2)	Enable/Disable the onboard LAN devices. Options available: Disabled, Enabled. Default setting is Enabled .
Onboard LAN# I/O ROM ^(Note2)	Enable/Disable the onboard LAN devices, and initializes device expansion ROM. Options available: Disabled, Enabled. Default setting is Enabled .

(Note1) This section is dependent on the available MCIO connector.

(Note2) This section is dependent on the available LAN controller.

Parameter	Description
PCI Devices Common Settings	
Above 4G Decoding	Enable/Disable memory mapped I/O to 4GB or greater address space (Above 4G Decoding). Options available: Disabled, Enabled. Default setting is Enabled .
SR-IOV Support	If the system has SR-IOV capable PCIe devices, this item Enable/Disable Single Root IO Virtualization Support. Options available: Disabled, Enabled. Default setting is Enabled .
Relaxed Ordering	Enable/Disable PCI express device relaxed ordering. Options available: Disabled, Enabled. Default setting is Enabled .

2-2-9 USB Configuration

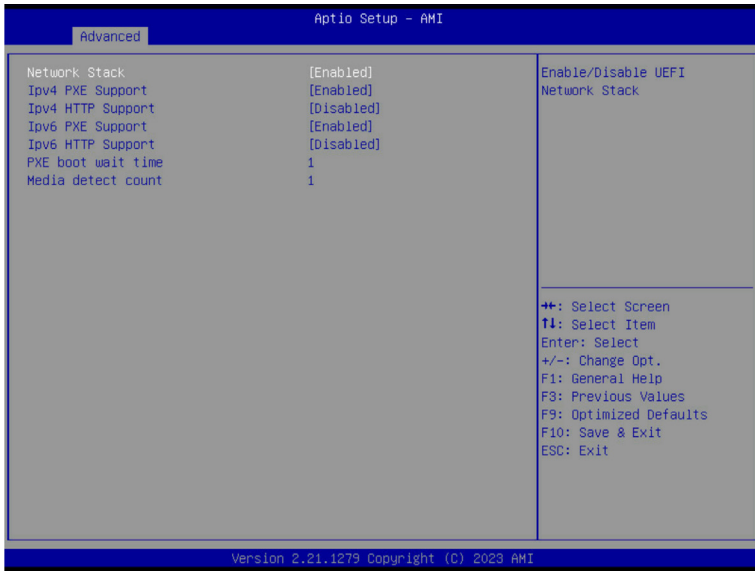


Parameter	Description
USB Configuration	
USB Module Version	Displays the USB module version information.
USB Controllers	Displays the supported USB controllers.
USB Devices:	Displays the USB devices connected to the system.
Legacy USB Support	Enable/Disable the Legacy USB support function. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications. Options available: Enabled, Disabled, Auto. Default setting is Enabled .
XHCI Hand-off	Enable/Disable the XHCI Hand-off support. Options available: Enabled, Disabled. Default setting is Enabled .
USB Mass Storage Driver Support ^(Note)	Enable/Disable the USB Mass Storage Driver Support. Options available: Disabled, Enabled. Default setting is Enabled .
USB hardware delays and time-outs	
USB transfer time-out	Selects the time-out value for USB Control/Bulk/Interrupt transfers. Options available: 1 sec, 5 sec, 10 sec, 20 sec. Default setting is 20 sec .

(Note) This item is present only if you attach USB devices.

Parameter	Description
Device reset time-out	Selects the time-out value during a USB mass storage device reset. Options available: 10 sec, 20 sec, 30 sec, 40 sec. Default setting is 20 sec .
Device power-up delay	Maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses default value: for a Root port it is 100 ms, for a Hub port the delay is taken from Hub descriptor. Options available: Auto, Manual. Default setting is Auto .
Mass Storage Devices	Displays the mass storage devices available on the system.

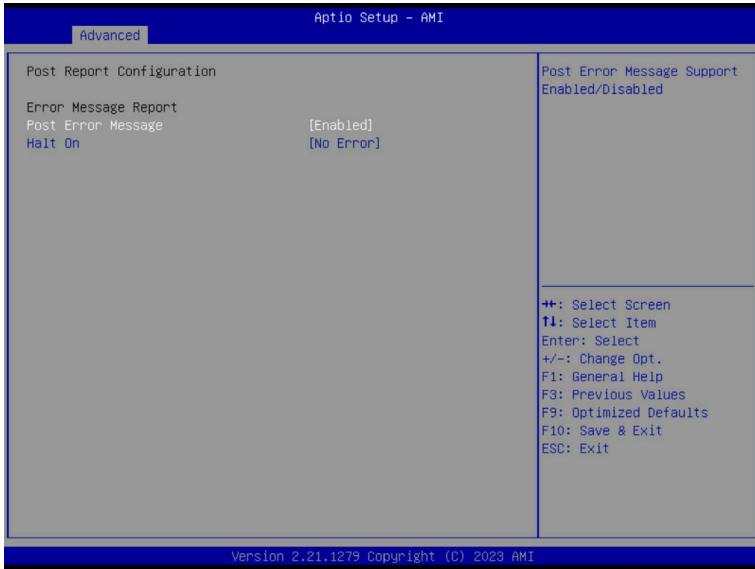
2-2-10 Network Stack Configuration



Parameter	Description
Network Stack	Enable/Disable the UEFI network stack. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 PXE Support ^(Note)	Enable/Disable the Ipv4 PXE feature. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv4 HTTP Support ^(Note)	Enable/Disable the Ipv4 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
Ipv6 PXE Support ^(Note)	Enable/Disable the Ipv6 PXE feature. Options available: Enabled, Disabled. Default setting is Enabled .
Ipv6 HTTP Support ^(Note)	Enable/Disable the Ipv6 HTTP feature. Options available: Enabled, Disabled. Default setting is Disabled .
PXE boot wait time ^(Note)	Wait time in seconds to press ESC key to abort the PXE boot. Press the <+> / <-> keys to increase or decrease the desired values.
Media detect count ^(Note)	Number of times the presence of media will be checked. Press the <+> / <-> keys to increase or decrease the desired values.

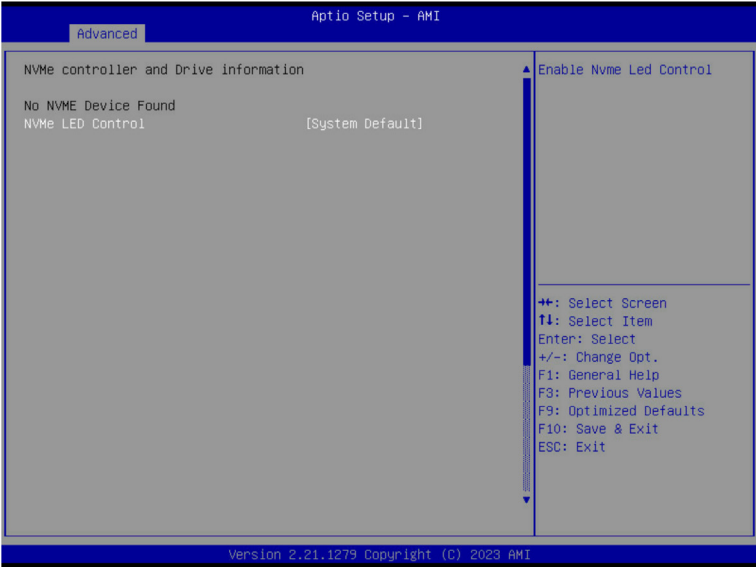
(Note) This item appears when **Network Stack** is set to **Enabled**.

2-2-11 Post Report Configuration



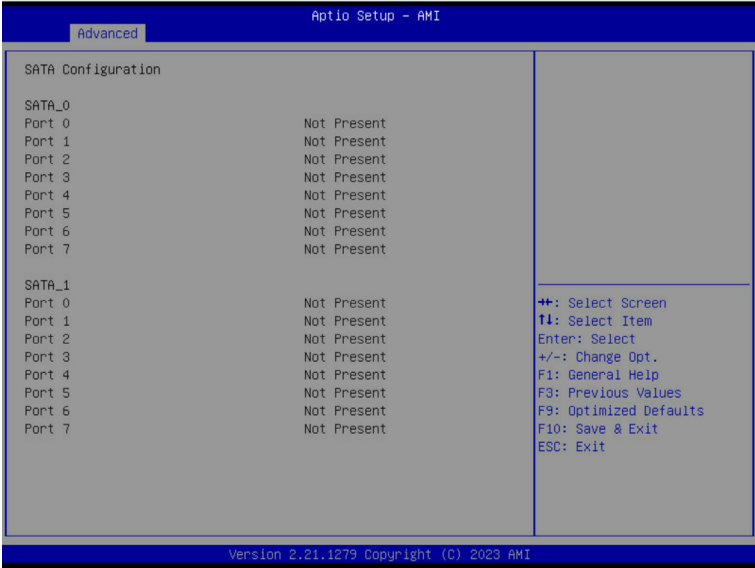
Parameter	Description
Post Report Configuration	
Error Message Report	
Post Error Message	Enable/Disable the POST Error Message support. Options available: Enabled, Disabled. Default setting is Enabled .
Halt On	Options available: No Error, All Error. Default setting is No Error .

2-2-12 NVMe Configuration



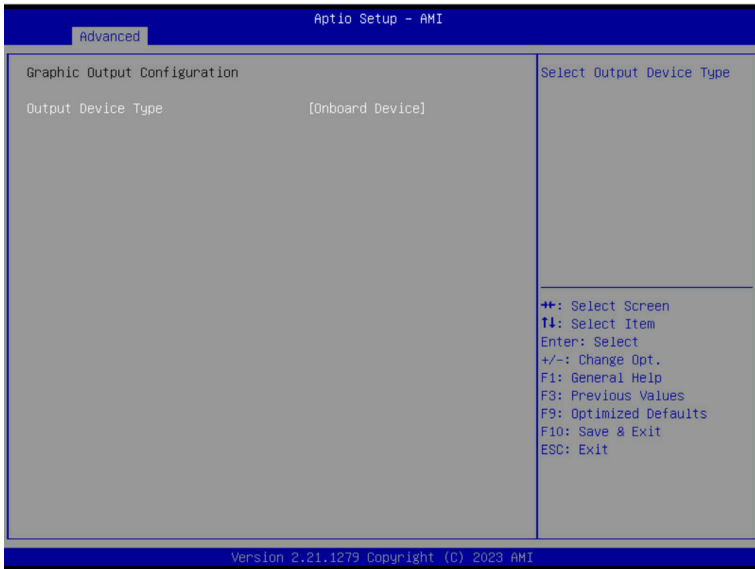
Parameter	Description
NVMe Configuration	Displays the NVMe devices connected to the system.
NVMe LED Control	Enable/Disable NVMe LED Control. Options available: System Default, Disabled, Enabled. Default setting is System Default .

2-2-13 SATA Configuration



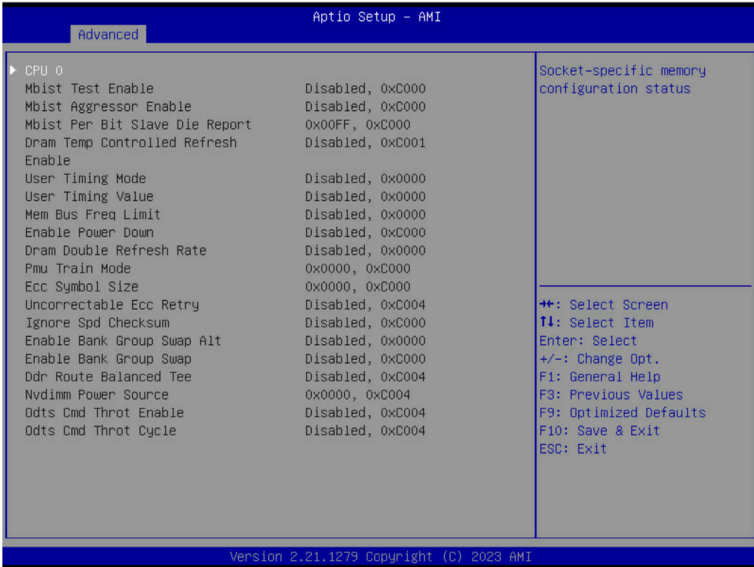
Parameter	Description
SATA Configuration	Displays the installed HDD devices information. System will automatically detect HDD type.

2-2-14 Graphic Output Configuration



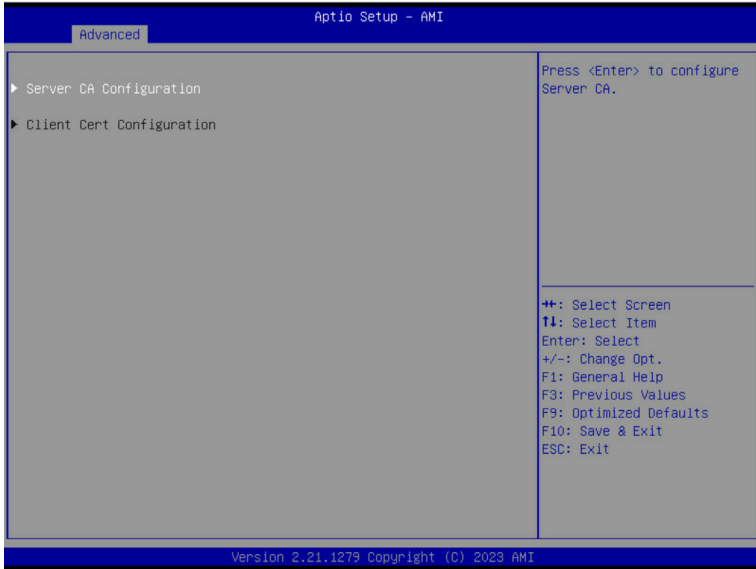
Parameter	Description
Output Device Type	Selects output device type. Options available: First loaded Device, Onboard Device, External Device, Specific Device. Default setting is Onboard Device .

2-2-15 AMD Mem Configuration Status



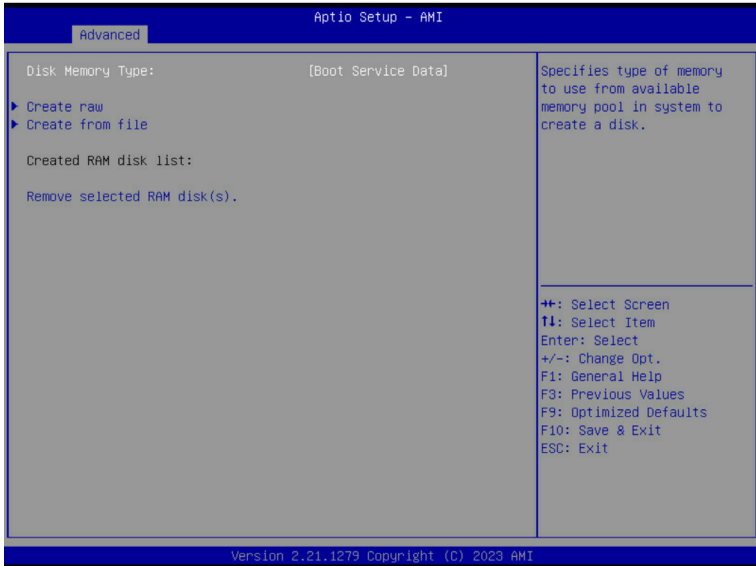
Parameter	Description
CPU 0	Press [Enter] to view the memory configuration status related to CPU 0.

2-2-16 Tls Auth Configuration



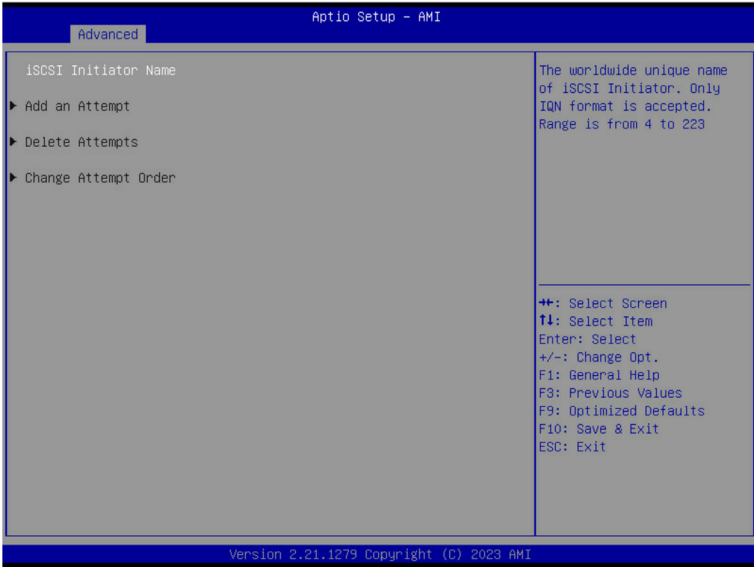
Parameter	Description
Server CA Configuration	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> ◆ Enroll Cert <ul style="list-style-type: none"> – Press [Enter] to enroll a certificate <ul style="list-style-type: none"> • Enroll Cert Using File • Cert GUID <p>Input digit character in 1111111-2222-3333-4444-1234567890ab format.</p> <ul style="list-style-type: none"> – Commit Changes and Exit – Discard Changes and Exit <ul style="list-style-type: none"> ◆ Delete Cert
Client Cert Configuration	Press [Enter] for configuration of advanced items.

2-2-17 RAM Disk Configuration



Parameter	Description
Disk Memory Type	Specifies the type of memory to use from available memory pool in system to create a disk. Options available: Boot Service Data, Reserved. Default setting is Boot Service Data .
Create Raw	Creates a raw RAM disk. <ul style="list-style-type: none"> ◆ Size (Hex) <ul style="list-style-type: none"> – Input a valid RAM disk size that should be multiple of the RAM disk block size. ◆ Create & Exit ◆ Discard & Exit
Create from file	Creates a RAM disk from a given file.
Created RAM disk list	
Remove selected RAM disk(s)	Selects the RAM disk(s) to remove.

2-2-18 iSCSI Configuration



Parameter	Description
iSCSI Initiator Name	Press [Enter] and name iSCSI Initiator. Only IQN format is accepted. Range: from 4 to 223
Add an Attempt	Press [Enter] to configure advanced items.
Delete Attempts	Press [Enter] to configure advanced items.
Change Attempt Order	Press [Enter] to configure advanced items.

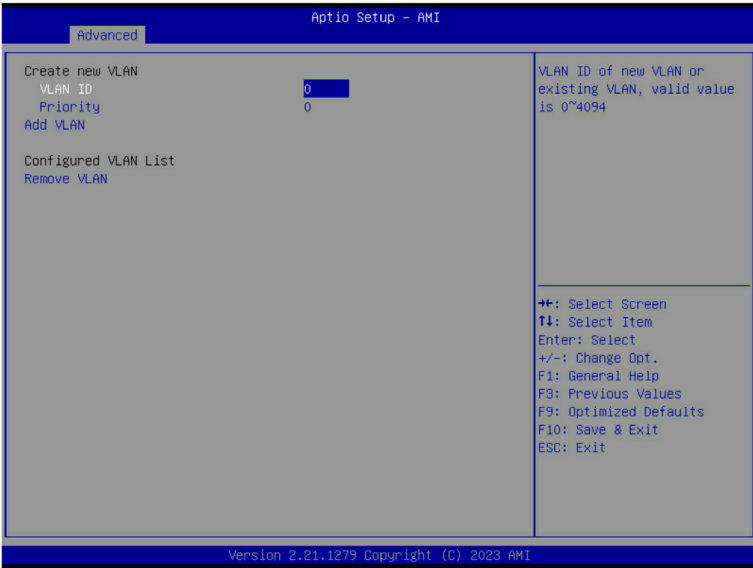
2-2-19 Intel(R) I210 Gigabit Network Connection

Aptio Setup - AMI		
Advanced		
<p>▶ NIC Configuration</p> <p>Blink LEDs 0</p> <p>UEFI Driver Intel(R) PRO/1000 8.5.21 PCI-E</p> <p>Adapter PBA 140724-006</p> <p>Device Name Intel(R) I210 Gigabit Network Connection</p> <p>Chip Type Intel i210</p> <p>PCI Device ID 1533</p> <p>PCI Address C5:00:00</p> <p>Link Status [Connected]</p> <p>MAC Address B4:2E:99:72:DC:11</p> <p>Virtual MAC Address 00:00:00:00:00:00</p>		<p>Click to configure the network device port.</p> <hr/> <p> ++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit </p>
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Aptio Setup - AMI		
Advanced		
<p>Link Speed [Auto Negotiated]</p> <p>Wake On LAN [Enabled]</p>		<p>Specifies the port speed used for the selected boot protocol.</p> <hr/> <p> ++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F3: Previous Values F9: Optimized Defaults F10: Save & Exit ESC: Exit </p>
Version 2.21.1279 Copyright (C) 2023 AMI		

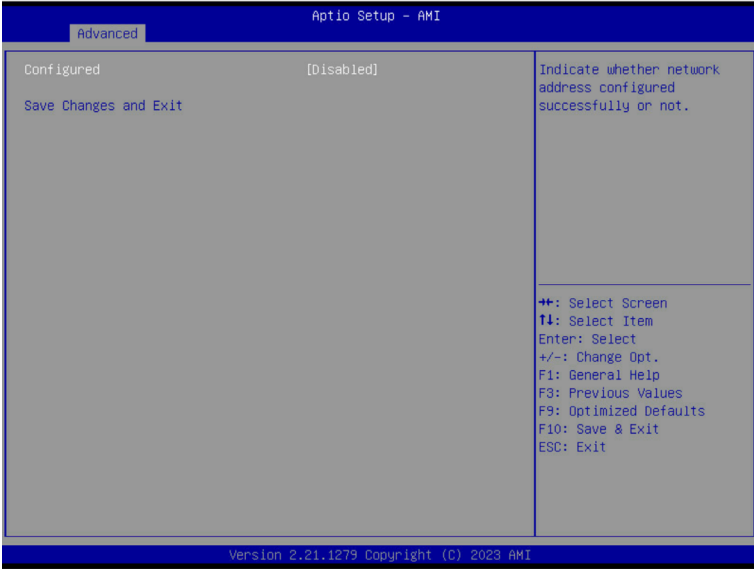
Parameter	Description
NIC Configuration	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Link Speed <ul style="list-style-type: none"> – Allows for automatic link speed adjustment. – Options available: Auto Negotiated, 10 Mbps Half, 10 Mbps Full, 100 Mbps Half, 100 Mbps Full. Default setting is Auto Negotiated. ◆ Wake On LAN <ul style="list-style-type: none"> – Enables power on of the system via LAN. Note that configuring Wake on LAN in the operating system does not change the value of this setting, but does override the behavior of Wake on LAN in OS controlled power states. – Options available: Disabled, Enabled. Default setting is Enabled.
Blink LEDs	<p>Identifies the physical network port by blinking the associated LED. Press the numeric keys to adjust desired values.</p>
UEFI Driver	Displays the technical specifications for the Network Interface Controller.
Adapter PBA	Displays the technical specifications for the Network Interface Controller.
Device Name	Displays the technical specifications for the Network Interface Controller.
Chip Type	Displays the technical specifications for the Network Interface Controller.
PCI Device ID	Displays the technical specifications for the Network Interface Controller.
PCI Address	Displays the technical specifications for the Network Interface Controller.
Link Status	Displays the technical specifications for the Network Interface Controller.
MAC Address	Displays the technical specifications for the Network Interface Controller.
Virtual MAC Address	Displays the technical specifications for the Network Interface Controller.

2-2-20 VLAN Configuration



Parameter	Description
Enter Configuration Menu	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Create new VLAN ◆ VLAN ID <ul style="list-style-type: none"> – Sets VLAN ID for a new VLAN or an existing VLAN. – Press the <+> / <-> keys to increase or decrease the desired values. – The valid range is from 0 to 4094. ◆ Priority <ul style="list-style-type: none"> – Sets 802.1Q Priority for a new VLAN or an existing VLAN. – Press the <+> / <-> keys to increase or decrease the desired values. – The valid range is from 0 to 7. ◆ Add VLAN <ul style="list-style-type: none"> – Press [Enter] to create a new VLAN or update an existing VLAN. ◆ Configured VLAN List ◆ Remove VLAN <ul style="list-style-type: none"> – Press [Enter] to remove an existing VLAN.

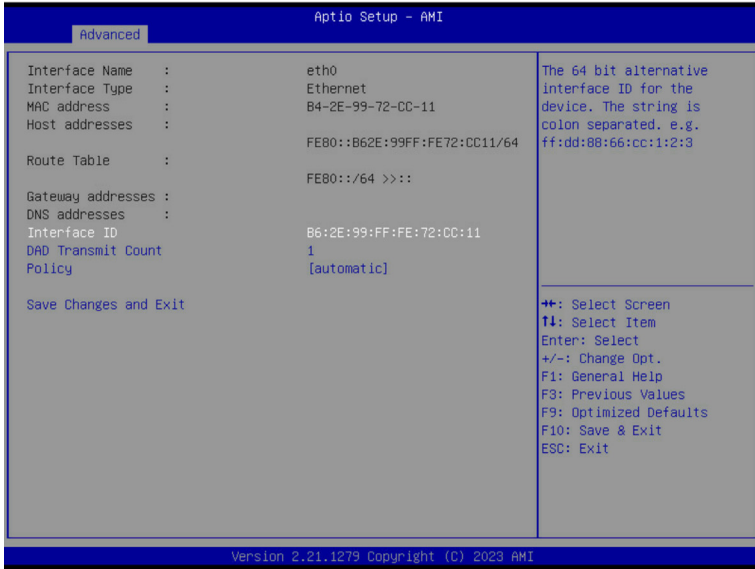
2-2-21 MAC IPv4 Network Configuration



Parameter	Description
Configured	Indicates whether network address is configured successfully or not. Options available: Enabled, Disabled. Default setting is Disabled .
Enable DHCP ^(Note)	Options available: Enabled, Disabled. Default setting is Disabled .
Local IP Address ^(Note)	Press [Enter] to configure local IP address.
Local NetMask ^(Note)	Press [Enter] to configure local NetMask.
Local Gateway ^(Note)	Press [Enter] to configure local Gateway
Local DNS Servers ^(Note)	Press [Enter] to configure local DNS servers
Save Changes and Exit	Press [Enter] to save all configurations.

(Note) This item appears when **Configured** is set to **Enabled**.

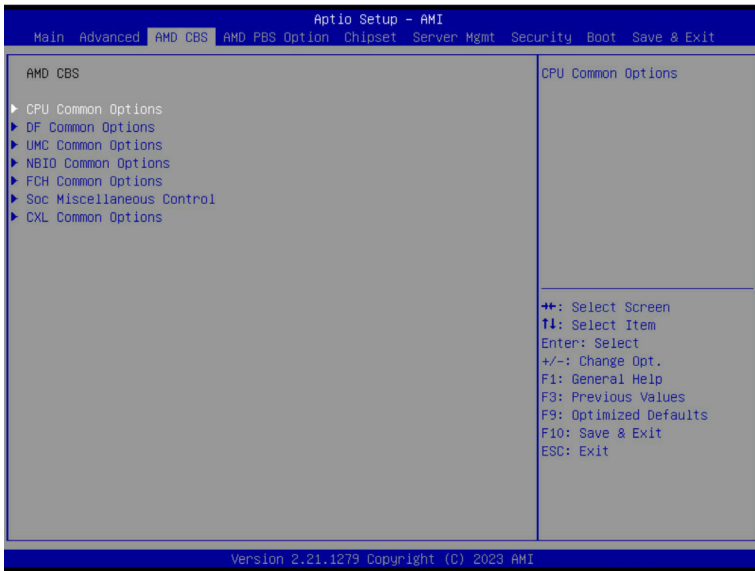
2-2-22 MAC IPv6 Network Configuration



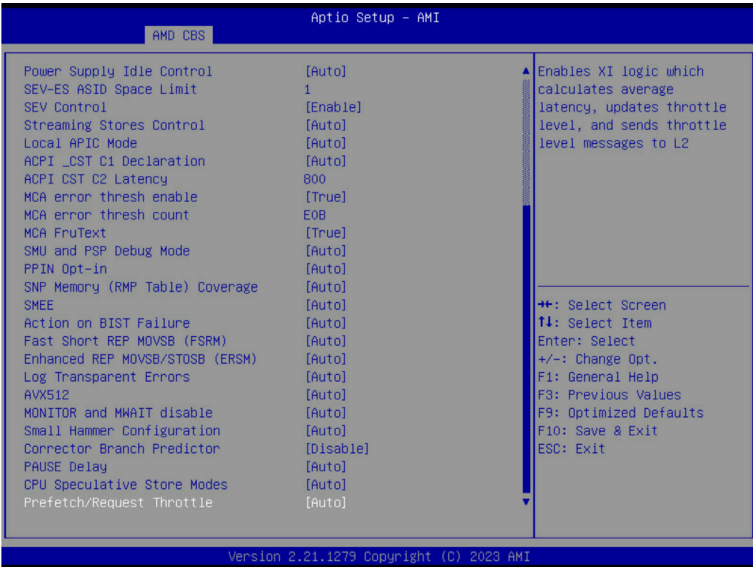
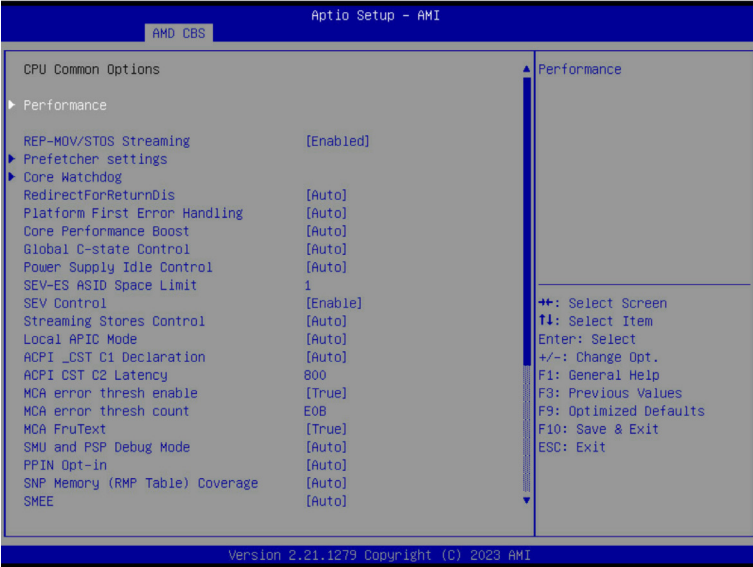
Parameter	Description
Enter Configuration Menu	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Displays the MAC Address information. ◆ Interface ID <ul style="list-style-type: none"> – The 64 bit alternative interface ID for the device. The string is colon separated. e.g. ff:dd:88:66:cc:1:2:3. ◆ DAD Transmit Count <ul style="list-style-type: none"> – The number of consecutive Neighbor solicitation messages sent while performing Duplicate Address Detection on a tentative address. A value of zero indicates that Duplicate Address Detection is not performed. ◆ Policy <ul style="list-style-type: none"> – Options available: automatic, manual. Default setting is automatic. ◆ Save Changes and Exit <ul style="list-style-type: none"> – Press [Enter] to save all configurations.

2-3 AMD CBS Menu

AMD CBS menu displays submenu options for configuring the CPU-related information that the BIOS automatically sets. Select a submenu item, then press [Enter] to access the related submenu screen.



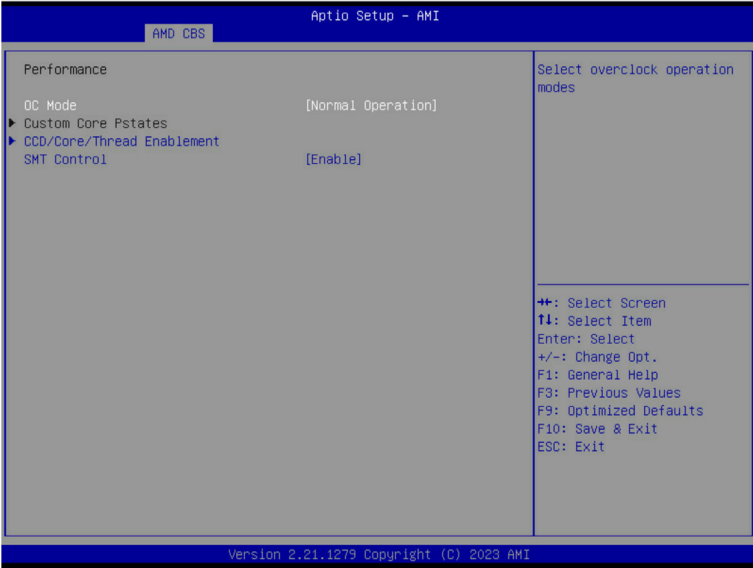
2-3-1 CPU Common Options



Parameter	Description
CPU Common Options	
Performance	Press [Enter] for configuration of advanced items.
REP-MOV/STOS Streaming	Allow REP-MOV/STOS to use non-caching streaming stores for large sizes. Options available: Disabled, Enabled. Default setting is Enabled .
Prefetcher settings	Press [Enter] for configuration of advanced items.
Core Watchdog	Press [Enter] for configuration of advanced items.
RedirectForReturnDis	From a workaround for GCC/C000005 issue for XV Core on CZ A0, setting MSRC001_1029 Decode Configuration (DE_CFG) bit 14 [DecfgNoRdrctForReturns] to 1. Options available: Auto, 1, 0. Default setting is Auto .
Platform First Error Handling	Enable/Disable PFEH, cloak individual banks, and mask deferred error interrupts from each bank. Options available: Enabled, Disabled, Auto. Default setting is Auto .
Core Performance Boost	Enable/Disable the Core Performance Boost function. Options available: Disabled, Auto. Default setting is Auto .
Global C-state Control	Controls the IO based C-state generation and DF C-states. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Power Supply Idle Control	Configures the Power Supply Idle Control. Options available: Low Current Idle, Typical Current Idle, Auto. Default setting is Auto .
SEV-ES ASID Space Limit	Configures the Space limit for SEV-ES ASIDs. Default setting is 1 .
SEV Control	Enable/Disable SEV control. Options available: Enable, Disable. Default setting is Enable .
Streaming Stores Control	Enable/Disable the Streaming Stores functionality. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Local APIC Mode	Sets the Local APIC Mode. Options available: Compatibility, xAPIC, x2APIC, Auto. Default setting is Auto .
ACPI_CST C1 Declaration	Determines whether or not to declare the C1 state to the OS.. Options available: Disabled, Enabled, Auto. Default setting is Auto .
ACPI CST C2 Latency	Enter in microseconds (decimal value).
MCA error thresh enable	Enable MCA error thresholding. Options available: False, True, Auto. Default setting is True .
MCA error thresh count	Effective error threshold count = 0xFFF(4095) - <this value> (e.g. the default value of 0xFF5(4085) results in a threshold of 0xA (10)).
MCA FruText	Enable MCA FruText. Options available: False, True. Default setting is True .
SMU and PSP Debug Mode	When this option is enabled, specific uncorrected errors detected by the PSP FW or SMU FW will hand and not reset the system. Options available: Disabled, Enabled, Auto. Default setting is Auto .

Parameter	Description
PPIN Opt-in	Enable/Disable the PPIN feature. Options available: Disabled, Enabled, Auto. Default setting is Auto .
SNP Memory (RMP Table) Coverage	Enabled: Enter system memory is covered. Options available: Disabled, Enabled, Custom, Auto. Default setting is Auto .
SMEE	Controls the Secure Memory Encryption Enable (SMEE) function. Options available: Disable, Enable, Auto. Default setting is Auto .
Action on BIST Failure	Action to take when a CCD BIST failure is detected. Options available: Do nothing, Down-CCD, Auto. Default setting is Auto .
Fast Short REP MOVSB (FSRM)	Options available: Disabled, Enabled, Auto. Default setting is Auto .
Enhanced REP MOVSB/STOSB (ERSM)	Options available: Disabled, Enabled, Auto. Default setting is Auto .
Log Transparent Errors	Enable/Disable the log Transparent errors function. Options available: Auto, Disabled, Enabled. Default setting is Auto .
AVX512	Enable/Disable AVX512. Options available: Disabled, Enabled, Auto. Default setting is Auto .
MONITOR and MWAIT disable	The MONITOR, MWAIT, MONITORX and MWAITX opcodes become invalid when enabled. Options available: Enabled, Disabled, Auto. Default setting is Auto
Small Hammer Configuration	Options available: Disabled, Enabled, Auto. Default setting is Auto .
Corrector Branch Predictor	Options available: Disable, Enable. Default setting is Disable .
PAUSE Delay	Number a cycles thread will be idle after a PAUSE instruction. Options available: Auto, Disable, 16 cycles, 32 cycles, 64 cycles, 128 cycles. Default setting is Auto .
CPU Speculative Store Modes	Select the CPU speculative store modes. Options available: Balanced, More Speculative, Less Speculative, Auto. Default setting is Auto .
Prefetch/Request Throttle	Enables XI logic which calculates average latency, updates throttle level, and sends throttle level messages to L2. Options available: Disable, Enable, Auto. Default setting is Auto .

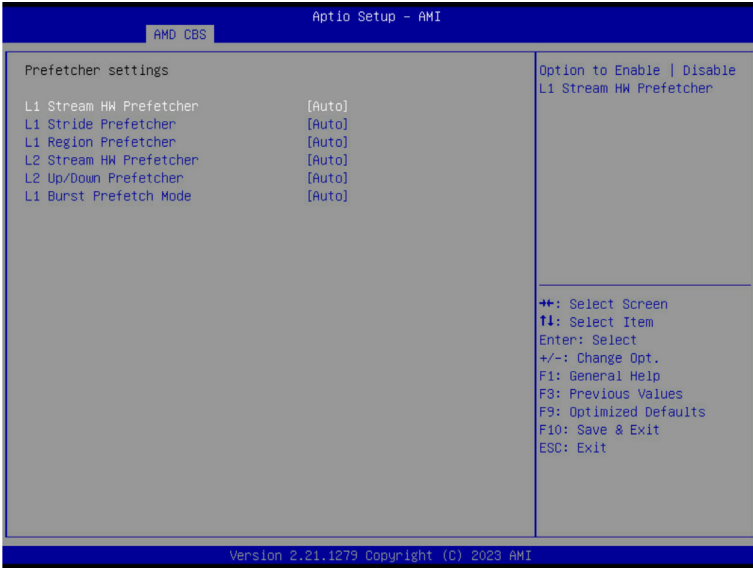
2-3-1-1 Performance



Parameter	Description
Performance	
OC Mode ^(Notes)	Options available: Normal Operation, Customized. Default setting is Normal Operation .
Custom Core Pstates	Allows you to accept or decline enabling Custom Core Pstates. When accepted, you can disable or customize core pstates.
CCD/Core/Thread Enablement	<p>Allows you to accept or decline enabling CCDs, processor cores and threads. When accepted, you can control the number of CCDs to be used, and the number of cores to be used.</p> <ul style="list-style-type: none"> ◆ CCD Control <ul style="list-style-type: none"> – Options available: Auto, 2 CCDs, 4 CCDs, 6 CCDs. Default setting is Auto. ◆ Core Control <ul style="list-style-type: none"> – Options available: Auto, ONE(1+0), TWO(2+0), THREE(3+0) FOUR(4+0), FIVE(5+0). – Default setting is Auto.
SMT Control	<p>Can be used to disable symmetric multithreading. To re-enable SMT, a POWER CYCLE is needed after select the 'Enable' option. Select 'Auto' base on BIOS PCD. (PcdAmdSmtMode) default setting.</p> <p>Options available: Disable, Enable, Auto. Default setting is Enable.</p>

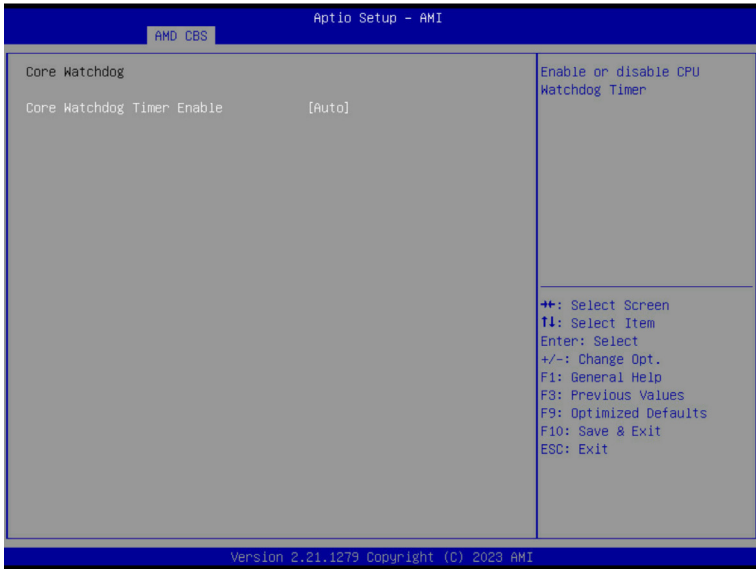
(Note) Advanced items are configurable when this item is defined.

2-3-1-2 Prefetcher Settings



Parameter	Description
Prefetcher settings	
L1 Stream HW Prefetcher	Enable/Disable L1 Stream HW Prefetcher. Options available: Disable, Enable, Auto. Default setting is Auto .
L1 Stride Prefetcher	Use memory access history of individual instructions to fetch additional lines when each access is a constant distance from the previous. Enable/Disable L1 Stride Prefetcher. Options available: Disable, Enable, Auto. Default setting is Auto .
L1 Region Prefetcher	Use memory access history to fetch additional lines when the data access for a given instruction tends to be followed by other data accesses. Enable/Disable L1 Region Prefetcher. Options available: Disable, Enable, Auto. Default setting is Auto .
L2 Stream HW Prefetcher	Enable/Disable L2 Stream HW Prefetcher. Options available: Disable, Enable, Auto. Default setting is Auto .
L2 Up/Down Prefetcher	Use memory access history to determine whether to fetch the next or previous line for all memory accesses. Enable/Disable L2 Up/Down Prefetcher. Options available: Disable, Enable, Auto. Default setting is Auto .
L1 Burst Prefetch Mode	Enable/Disable L1 Burst Prefetch Mode. Options available: Disable, Enable, Auto. Default setting is Auto .

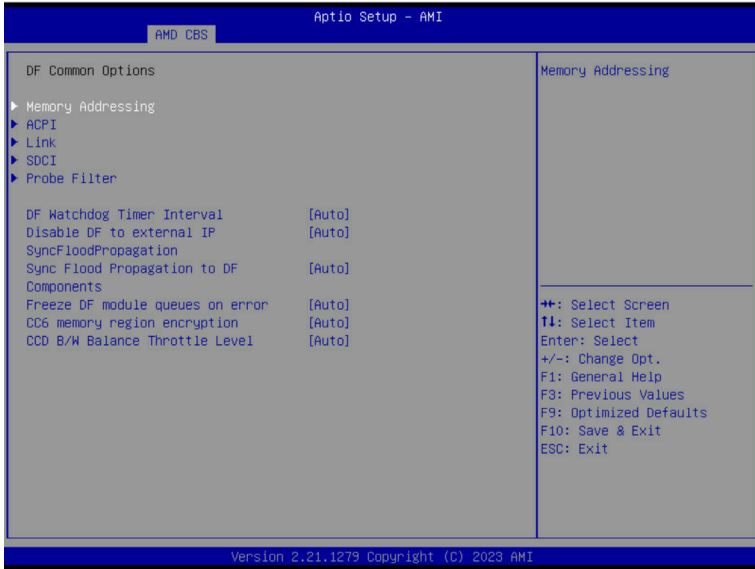
2-3-1-3 Core Watchdog



Parameter	Description
Core Watchdog	
Core Watchdog Timer Enable ^(Note)	Enable/Disable CPU Watchdog Timer. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Core Watchdog Timer Interval	Select the CPU Watchdog Timer interval. Options available: 2.681s, 1.340s, 669.41ms, 334.05ms, 166.37ms, 82.53ms, 40.61ms, 20.970ms, 10.484ms, 5.241ms, 2.620ms, 1.309ms, 654.08us, 326.4us, 162.56us, 80.64us, 39.68us, Auto. Default setting is Auto .
Core Watchdog Timer Severity	Options available: No Error, Transparent, Corrected, Deferred, Uncorrected, Fatal, Auto. Default setting is Auto .

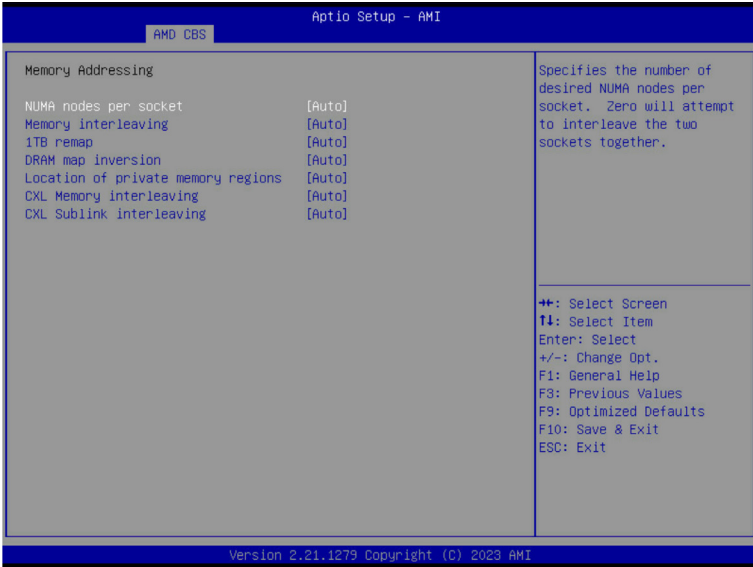
(Note) Advanced items prompt when this item is defined.

2-3-2 DF Common Options



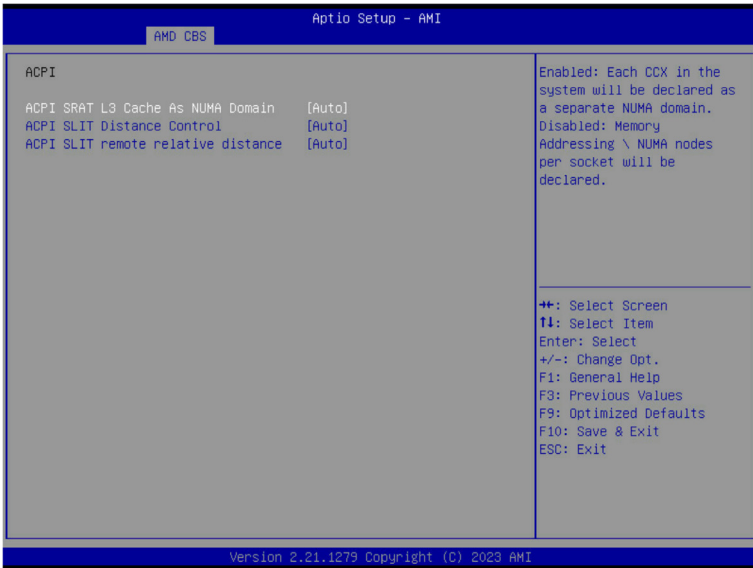
Parameter	Description
DF Common Options	
Memory Addressing	Press [Enter] for configuration of advanced items.
ACPI	Press [Enter] for configuration of advanced items.
Link	Press [Enter] for configuration of advanced items.
SDCI	Press [Enter] for configuration of advanced items.
Probe Filter	Press [Enter] for configuration of advanced items.
DF Watchdog Timer Interval	Configures the Data Fabric watchdog timer interval. Options available: Auto, 41ms, 166ms, 334ms, 669ms, 1.34 seconds, 2.68 seconds, 5.36 seconds. Default setting is Auto .
Disable DF to external IP sync flood propagation	Enable/Disable SyncFlood to UMC & downstream slaves. Options available: Sync flood disabled, Sync flood enabled, Auto. Default setting is Auto .
Sync flood propagation to DF Components	Enable/Disable DF Sync Flood propagation. Options available: Sync flood disabled, Sync flood enabled, Auto. Default setting is Auto .
Freeze DF module queues on error	Options available: Disabled, Enabled, Auto. Default setting is Auto .
CC6 memory region encryption	Controls whether or not the CC6 save/restor memory is encrypted. Options available: Disabled, Enabled, Auto. Default setting is Auto .
CCD B/W Balance Throttle Level	Options available: Auto, Level 0, Level 1, Level 2, Level 3, Level 4. Default setting is Auto .

2-3-2-1 Memory Addressing



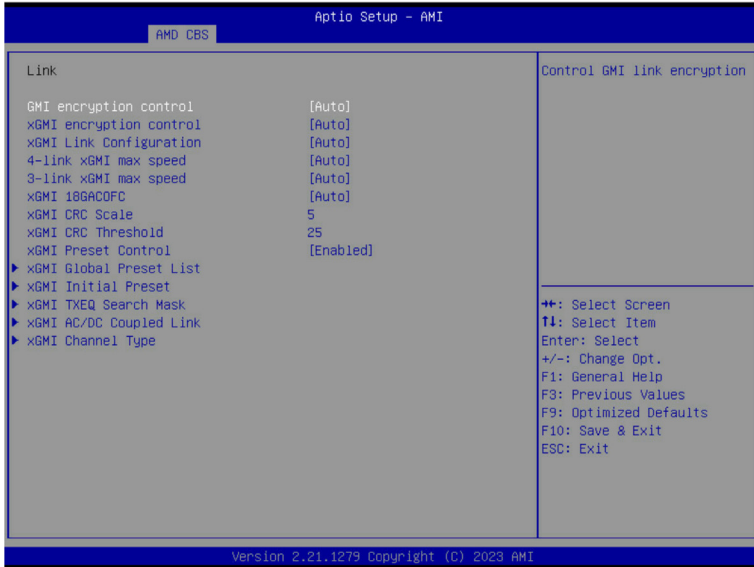
Parameter	Description
Memory Addressing	
NUMA nodes per socket	<p>Specifies the number of desired NUMA nodes per socket. Options available: NPS0, NPS1, NPS2, NPS4, Auto. Default setting is Auto. NOTE!</p> <ul style="list-style-type: none"> • Available options may vary by system configuration. • Only dual processor configuration supports NPS0.
Memory interleaving	<p>Enable/Disable the Memory interleaving feature. Options available: Disabled, Auto, Enabled. Default setting is Auto.</p>
1TB remap	<p>Enable/Disable to remap DRAM out of the space just below the 1TB boundary. The ability to remap depends on DRAM configuration, NPS, and interleaving selection, and may not always be possible. Options available: Do not remap, Attempt to remap, Auto. Default setting is Auto.</p>
DRAM map inversion	<p>Enable/Disable the DRAM map inversion function. Options available: Disabled, Enabled, Auto. Default setting is Auto.</p>
Location of private memory regions	<p>Controls whether or not the private memory regions (PSP, SMU and CC6) are at the top of DRAM or distributed. Options available: Distributed, Consolidated, Auto. Default setting is Auto.</p>
CXL Memory interleaving	Options available: Disabled, Enabled, Auto. Default setting is Auto .
CXL Sublink interleaving	Options available: Enable, Disable, Auto. Default setting is Auto .

2-3-2-2 ACPI



Parameter	Description
ACPI	
ACPI SRAT L3 Cache As NUMA Domain	Enable/Disable report each L3 cache as a NUMA Domain to the OS. Options available: Disabled, Enabled, Auto. Default setting is Auto .
ACPI SLIT Distance Control	Determines how the SLIT distances are declared. Options available: Manual, Auto. Default setting is Auto .
ACPI SLIT remote relative distance	Sets the remote socket distance for 2P systems as near (2.8) or far (3.2). Options available: Near, Far, Auto. Default setting is Auto .

2-3-2-3 Link

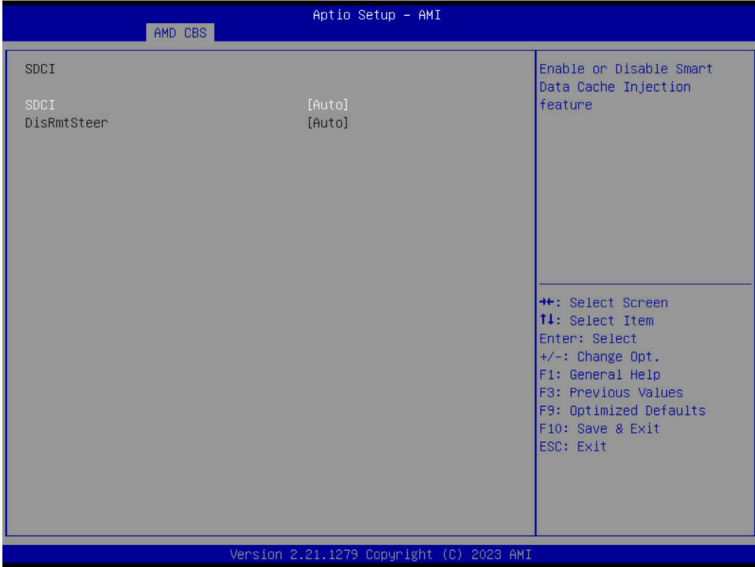


Parameter	Description
GMI encryption control	Enable/Disable GMI link encryption. Options available: Disabled, Enabled, Auto. Default setting is Auto .
xGMI encryption control	Enable/Disable xGMI link encryption. Options available: Disabled, Enabled, Auto. Default setting is Auto .
xGMI Link Configuration	Configures the number of xGMI2 links used on a multi-socket system. Options available: Auto, 3 xGMI Links, 4 xGMI Links, 2 xGMI Links + 2 PCI Links. Default setting is Auto .
4-link xGMI max speed	Specifies the max speed of 4-link xGMI. Options available: 12Gbps, 16Gbps, 17Gbps, 18Gbps, 20Gbps, 22Gbps, 23Gbps, 24Gbps, 25Gbps, 26Gbps, 27Gbps, 30Gbps, 32Gbps, Auto. Default setting is Auto .
3-link xGMI max speed	Specifies the max speed of 3-link xGMI. Options available: 12Gbps, 16Gbps, 17Gbps, 18Gbps, 20Gbps, 22Gbps, 23Gbps, 24Gbps, 25Gbps, 26Gbps, 27Gbps, 30Gbps, 32Gbps, Auto. Default setting is Auto .
xGMI 18GACOFc	Configures xGMI 18GACOFc. Options available: Auto, Enable, Disable. Default setting is Auto .
xGMI CRC Scale	Configures leaky bucket scale for xGMI and WAFL CRC errors. Every scale milliseconds an error will leak from the CRC counter. Default setting is 5.
xGMI CRC Threshold	Configures leaky bucket threshold for xGMI and WAFL CRC errors. If link CRC counter exceeds this threshold, an error will be logged. Default setting is 25.
xGMI Preset Control	Enable/Disable xGMI Preset control. Options available: Disabled, Enabled, Auto. Default setting is Enabled .

Parameter	Description
xGMI Global Preset List	Press [Enter] to configure the xGMI Preset list.
xGMI Initial Preset	Press [Enter] to configure the xGMI Initial Preset CPU0/1 link.
xGMI TXEQ Search Mask	Press [Enter] to configure the xGMI TXEQ Search Mask CPU0/1 link.
xGMI AC/DC Coupled Link	<p>Press [Enter] to configure the xGMI AC/DC Coupled link.</p> <ul style="list-style-type: none"> ◆ xGMI AC/DC Coupled Link Control^(Note) <ul style="list-style-type: none"> – Options available: Manual, Auto. Default setting is Auto.
xGMI Channel Type	<p>Press [Enter] to configure the xGMI Channel Type.</p> <ul style="list-style-type: none"> ◆ xGMI Channel Type Control^(Note) <ul style="list-style-type: none"> – Options available: Manual, Auto. Default setting is Auto.

(Note) Advanced items prompt when this item is defined.

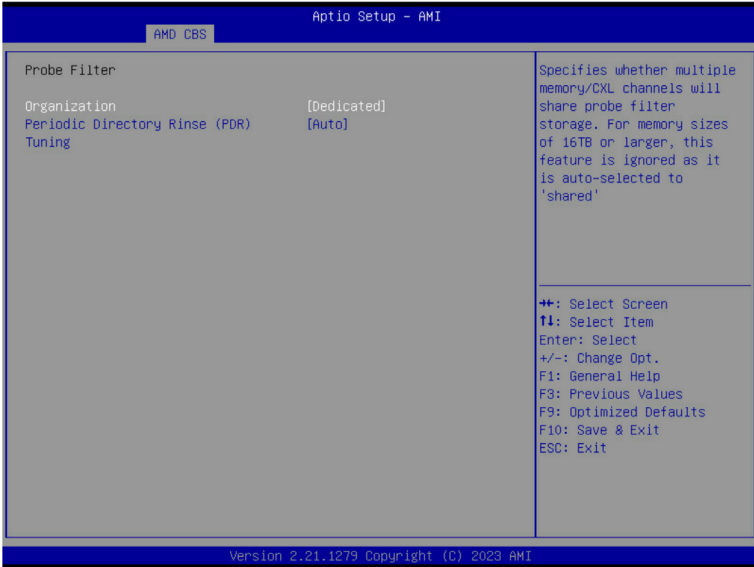
2-3-2-4 SDCI



Parameter	Description
SDCI ^(Note)	Options available: Disabled, Enabled, Auto. Default setting is Auto .
DisRmSteer	Options available: Disabled, Enabled, Auto. Default setting is Auto .

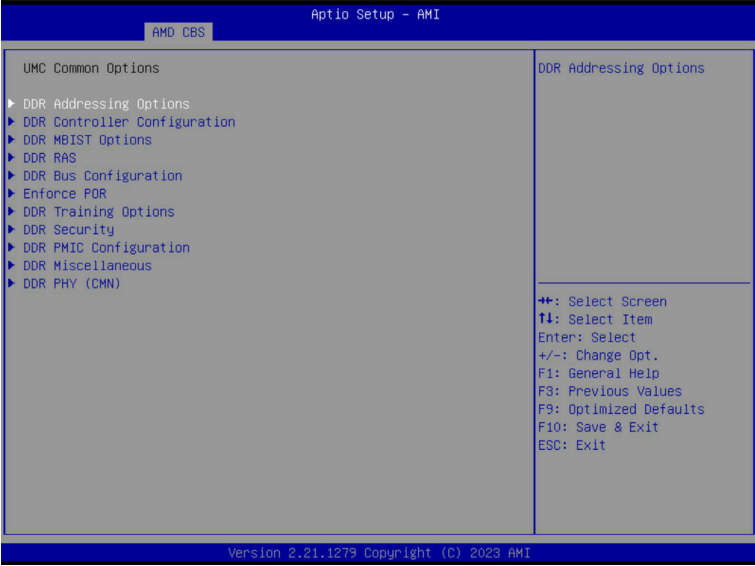
(Note) Advanced items prompt when this item is defined.

2-3-2-5 Probe Filter



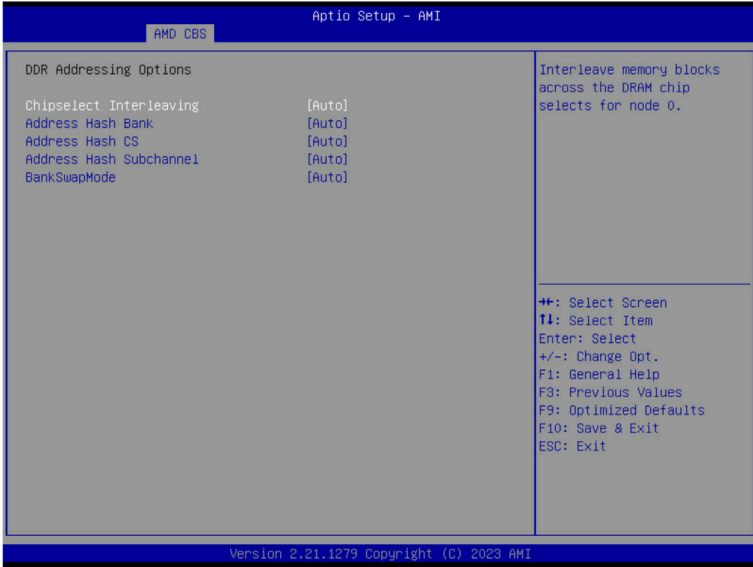
Parameter	Description
Organization	Specifies whether multiple memory/CXL channels will share probe filter storage. Options available: Auto, Dedicated, Shared. Default setting is Dedicated .
Periodic Directory Rinse (PDR) Tuning	Controls PDR settings that may impact performance by workload and/or processor. Options available: Memory-Sensitive, Cache-Bound, Neutral, Adaptive, Auto. Default setting is Auto .

2-3-3 UMC Common Options



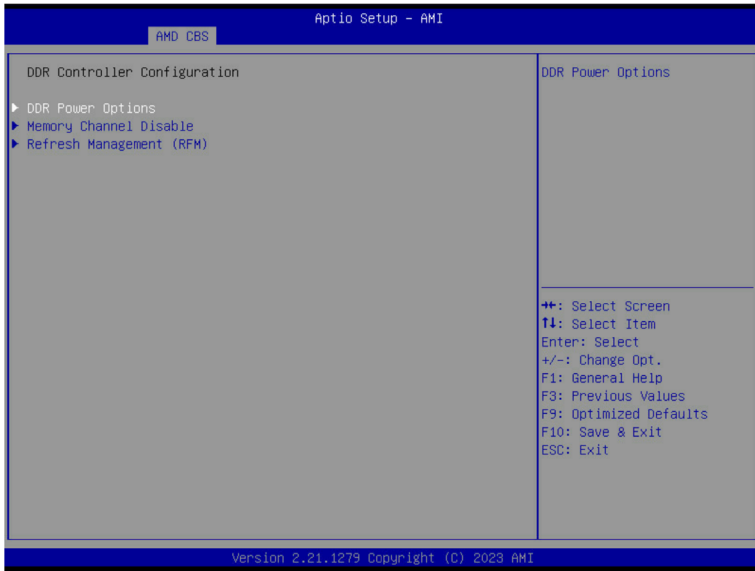
Parameter	Description
UMC Common Options	
DDR Addressing Options	Press [Enter] for configuration of advanced items.
DDR Controller Configuration	Press [Enter] for configuration of advanced items.
DDR MBIST Options	Press [Enter] for configuration of advanced items.
DDR RAS	Press [Enter] for configuration of advanced items.
DDR Bus Configuration	Press [Enter] for configuration of advanced items.
Enforce POR	Press [Enter] for configuration of advanced items.
DDR Training Options	Press [Enter] for configuration of advanced items.
DDR Security	Press [Enter] for configuration of advanced items.
DDR PMIC Configuration	Press [Enter] for configuration of advanced items.
DDR Miscellaneous	Press [Enter] for configuration of advanced items.
DDR PHY (CMN)	Press [Enter] for configuration of advanced items.

2-3-3-1 DDR Addressing Options



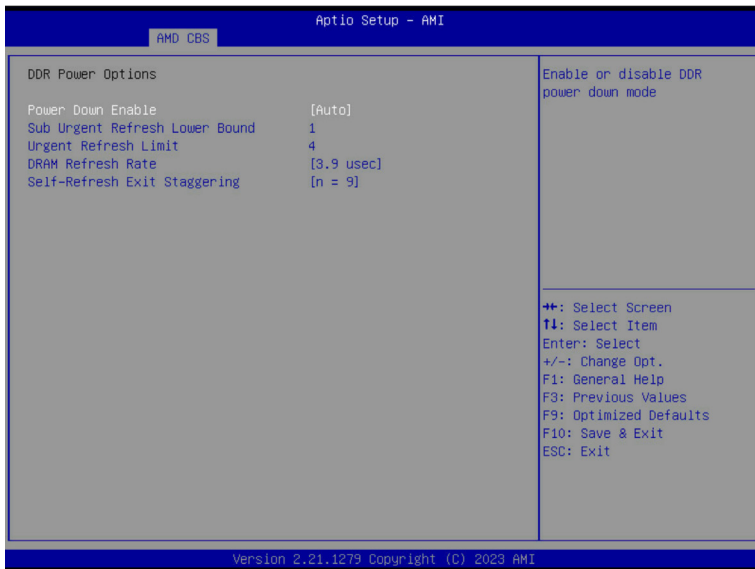
Parameter	Description
DDR Addressing Options	
Chipselect Interleaving	Interleaves memory blocks across the DRAM chip selects for node 0. Options available: Disabled, Auto. Default setting is Auto .
Address Hash Bank	Enable or disable bank addressing hashing. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Address Hash CS	Enable or disable CS addressing hashing. Options available: Auto, Enabled, Disabled. Default setting is Auto .
Address Hash RM	Enable or disable RM address hashing for 3DS DIMMs. Options available: Auto, Enabled, Disabled. Default setting is Auto .
Address Hash Subchannel	Enable or disable sub-channel addressing hashing. Options available: Auto, Enabled, Disabled. Default setting is Auto .
BankSwapMode	Options available: Auto, Disabled, Swap CPU. Default setting is Auto .

2-3-3-2 DDR Controller Configuration



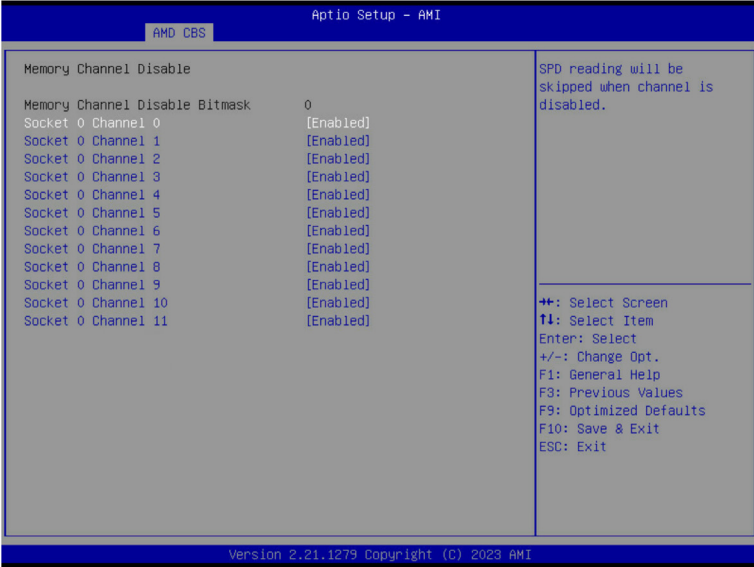
Parameter	Description
DDR Controller Configuration	
DDR Power Options	Press [Enter] for configuration of advanced items.
Memory Channel Disable	Press [Enter] for configuration of advanced items.
Refresh Management (RFM)	Press [Enter] for configuration of advanced items.

2-3-3-2-1 DDR Power Options



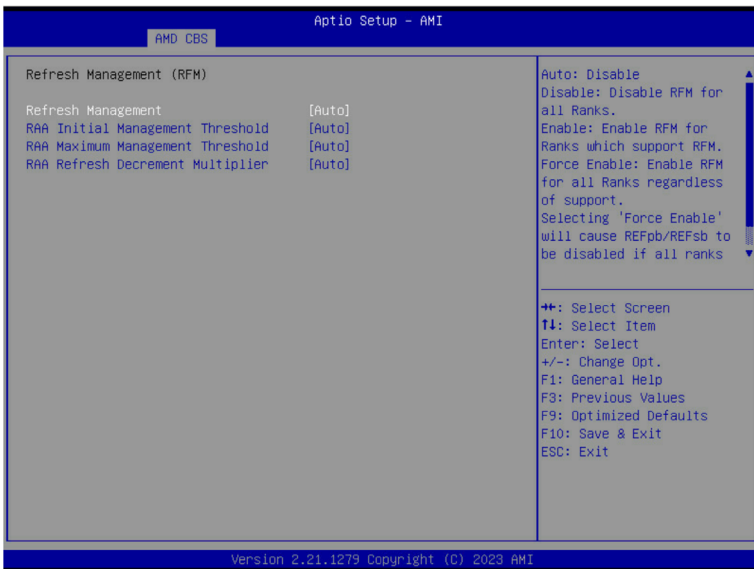
Parameter	Description
DDR Power Options	
Power Down Enable	Enable or disable DDR power down mode. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Sub Urgent Refresh Lower Bound	Specifies the stored refresh limit required to enter sub-urgent refresh mode.
Urgent Refresh Limit	Specifies the stored refresh limit required to enter urgent refresh mode.
DRAM Refresh Rate	DRAM refresh rate: 1.95us or 3.9us. Options available: 3.9 usec, 1.95 usec. Default setting is 3.9 usec .
Self-Refresh Exit Staggering	Options available: Disabled, n=1~9. Default setting is n=9 .

2-3-3-2-2 Memory Channel Disable



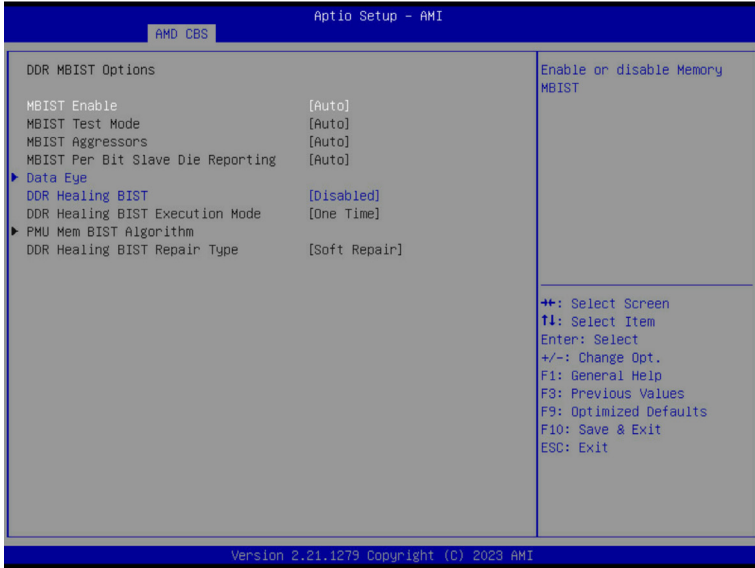
Parameter	Description
Memory Channel Disable	
Memory Channel Disable Bitmask	
CPU0 Channel_#	Press [Enter] to enable/disable specific memory channel.

2-3-3-2-3 Refresh Management (RFM)



Parameter	Description
Refresh Management (RFM)	
Refresh Management	Configure Refresh Management. Options available: Enable, Disable, Auto, Force Enable. Default setting is Auto .
RAA Initial Management Threshold	Override Rolling Accumulated ACT Initial Management Threshold. Options available: 32, 40, 48, 56, 64, 72, 80, Auto. Default setting is Auto .
RAA Maximum Management Threshold	Override Rolling Accumulated ACT Maximum Management Threshold. Options available: 3X, 4X, 5X, 6X, Auto. Default setting is Auto .
RAA Refresh Decrement Multiplier	Override RAA Refresh Decrement Multiplier. Options available: 0.5, 1, Auto. Default setting is Auto .

2-3-3-3 DDR MBIST Options



Parameter	Description
DDR MBIST Options	
MBIST Enable	Enable/Disable the Memory MBIST function. Options available: Disabled, Enabled, Auto. Default setting is Auto .
MBIST Test Mode ^(Note1)	Selects MBIST Test Mode. Interface Mode: Tests Single and Multiple CS transactions and Basic Connectivity. Data Eye Mode: Measures Voltage vs. Timing. Options available: Auto, Both, Interface Mode, Data Eye Mode. Default setting is Auto .
MBIST Aggressors ^(Note1)	Enable/Disable MBIST Aggressor test. Options available: Auto, Enabled, Disabled. Default setting is Auto .
MBIST Per Bit Slave Die Reporting ^(Note1)	Enable/Disable to report 2D data eye results in ABL log for each DQ, Chipselect, and Channel. Options available: Auto, Enabled, Disabled. Default setting is Auto .
Data Eye	Press [Enter] to configure advanced items.
Memory Healing BIST	Enable/Disable memory healing BIST. Options available: Disabled, PMU Mem BIST, Self-Healing Mem BIST, PMU and Self-Healing Mem BIST. Default setting is Disabled .

(Note1) This item appears when **MBIST Enable** is set to **Enabled**.

Parameter	Description
DDR Healing BIST Execution Mode ^(Note2)	Options available: One Time, Every boot. Default setting is One Time .
PMU Mem BIST Algorithm ^(Note2)	Press [Enter] to enable/disable PMU Mem BIST Algorithm.
DDR Healing BIST Repair Type ^(Note2)	For DRAM errors found in the BIOS memory BIST select the repair type. Options available: Soft Repair, Hard Repair, No Repairs -Test only. Default setting is Soft Repair .

(Note2) This item appears when **DDR Healing BIST** is defined.

2-3-3-3-1 Data Eye



Parameter	Description
Data Eye	
Pattern Select	Options available: PRBS, SSO, Both. Default setting is PRBS .
Pattern Length	Determines the pattern length. The possible options are N=3....12.
Aggressor Channel	This item helps read the aggressors channels. Options available: One Sub-Channel, Half Channels, All Channels. Default setting is All Channels .
Aggressor Static Lane Control	Enable/Disable the Aggressor Static Lane Control function. Options available: Enabled, Disabled. Default setting is Disabled .
Aggressor Static Lane Select Upper 32 bits	This item is configurable when Aggressor Static Lane Control is set to Enabled .
Aggressor Static Lane Select Lower 32 bits	This item is configurable when Aggressor Static Lane Control is set to Enabled .
Aggressor Static Lane Select ECC	This item is configurable when Aggressor Static Lane Control is set to Enabled .
Aggressor Static Lane Value	This item is configurable when Aggressor Static Lane Control is set to Enabled .
Target Static Lane Control	Enable/Disable the Target Static Lane Control function. Options available: Enabled, Disabled. Default setting is Disabled .

Parameter	Description
Target Static Lane Select Upper 32 bits	This item is configurable when Target Static Lane Control is set to Enabled .
Target Static Lane Select Lower 32 bits	This item is configurable when Target Static Lane Control is set to Enabled .
Target Static Lane Select ECC	This item is configurable when Target Static Lane Control is set to Enabled .
Target Static Lane Value	This item is configurable when Target Static Lane Control is set to Enabled .
Worst Case Margin Granularity	Configures Worst Case Margin Granularity. Options available: Per Chip Select, Per Nibble. Default setting is Per Chip Select .
Read Voltage Sweep Step Size	Configures the step size for read Data Eye voltage sweep. Options available: 1, 2, 4. Default setting is 1.
Read Timing Sweep Step Size	Configures the step size for read Data Eye timing sweep. Options available: 1, 2, 4. Default setting is 1.
Write Voltage Sweep Step Size	Configures the step size for write Data Eye voltage sweep. Options available: 1, 2, 4. Default setting is 1.
Write Timing Sweep Step Size	Configures the step size for write Data Eye timing sweep. Options available: 1, 2, 4. Default setting is 1.
Silent Execution	Execute MBIST Data Eye silently without ABL log output. Options available: Enabled, Disabled. Default setting is Disabled .

2-3-3-4 DDR RAS



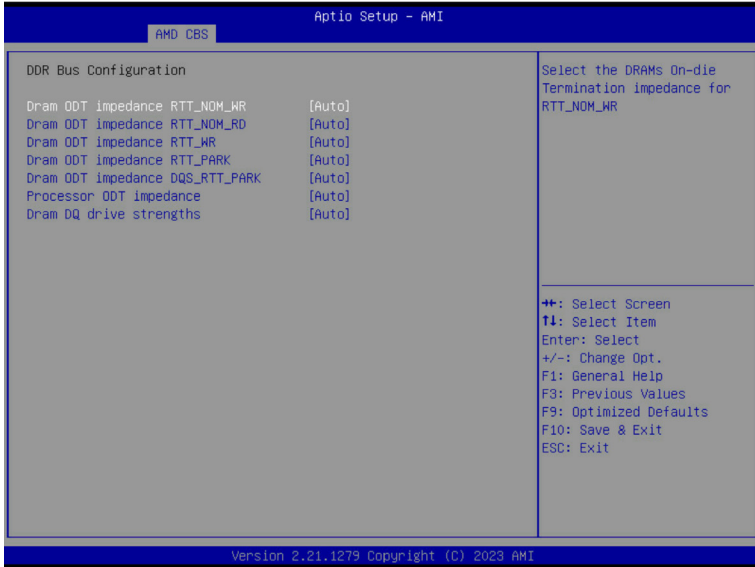
Parameter	Description
DDR RAS	
Data Poisoning	Enable/Disable the Data Poisoning function. Options available: Auto, Enabled, Disabled. Default setting is Auto .
DRAM Boot Time Post Package Repair	Enable/Disable the DRAM Boot Time Post Package Repair function. Options available: Enable, Disable. Default setting is Disable .
DRAM Runtime Post Package Repair	Enable/Disable the DRAM Runtime Post Package Repair function. Options available: Enable, Disable. Default setting is Disable .
RCD Parity	Enable/Disable the RCD Parity function. Options available: Auto, Enabled, Disabled. Default setting is Enabled .
Max RCD Parity Error Replay	Default setting is 8 .
Disable Memory Error Injection	Options available: False, True, Auto. Default setting is Auto .
ECC Configuration	Press [Enter] to configure advanced items. <ul style="list-style-type: none"> ◆ DRAM ECC Symbol Size <ul style="list-style-type: none"> – Configures the DRAM ECC Symbol Size. – Options available: Auto, x4, x16. Default setting is Auto. ◆ DRAM ECC Enable <ul style="list-style-type: none"> – Enable/Disable DRAM ECC. When set to Auto, it will set ECC to enable. – Options available: Auto, Enabled, Disabled. Default setting is Auto.

Parameter	Description
ECC Configuration (continued)	<ul style="list-style-type: none"> ◆ DRAM UECC Retry <ul style="list-style-type: none"> – Enable/Disable DRAM UECC Retry. – Options available: Auto, Enabled, Disabled. Default setting is Disabled. ◆ Max DRAM UECC Error Replay^(Note) <ul style="list-style-type: none"> – Default setting is 8. ◆ Memory Clear <ul style="list-style-type: none"> – Options available: Auto, Enabled, Disabled. Default setting is Auto. ◆ Address X0R after ECC <ul style="list-style-type: none"> – Options available: Auto, Enabled, Disabled. Default setting is Auto.
	<p data-bbox="397 498 732 522">Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ DRAM ECS Mode <ul style="list-style-type: none"> – Options available: Auto, AutoECS, ManualECS, DisableECS. Default setting is Auto. ◆ DRAM Redirect Scrubber Enable <ul style="list-style-type: none"> – Options available: Auto, Enabled, Disabled. Default setting is Auto. ◆ DRAM Scrub Redirection Limit <ul style="list-style-type: none"> – Options available: Auto, 8 Scrubs, 4 Scrubs, 2 Scrubs, 1 Scrub. Default setting is Auto. ◆ DRAM Scrub Time <ul style="list-style-type: none"> – Options available: Disabled, 1 hour, 4 hours, 6 hours, 8 hours, 12 hours, 16 hours, 24 hours, 48 hours. Default setting is 24 Hours. <p data-bbox="107 911 245 934">DRAM Scrubbers</p> <ul style="list-style-type: none"> ◆ DRAM Error Threshold Count <ul style="list-style-type: none"> – Options available: Auto, ETC_4, ETC_16, ETC_64, ETC_256, ETC_1024, ETC_4096. Default setting is Auto. ◆ DRAM ECS Count Mode <ul style="list-style-type: none"> – Options available: Auto, Row Count Mode, Code Word Count Mode. Default setting is Auto. ◆ DRAM AutoEcs during Self Refresh <ul style="list-style-type: none"> – Options available: Auto, AutoEcs Disabled, AutoEcs Enabled. Default setting is Auto. ◆ DRAM ECS WriteBack Suppression <ul style="list-style-type: none"> – Options available: Auto, Enable, Disable. Default setting is Auto. ◆ DRAM X4 WriteBack Suppression <ul style="list-style-type: none"> – Options available: Auto, Enable, Disable. Default setting is Auto.

(Note) This item available when **DRAM UECC Retry** is set to **Enabled**.

Parameter	Description
DRAM Corrected Error Counter Enable	Configure DRAM Corrected Error Counter function. Options available: Disable, NoLeakMode, LeakMode. Default setting is LeakMode .
DRAM Corrected Error Counter Interrupt Enable	Enable SMI when DRAM corrected Error Counter count exceeds the threshold value. Options available: False, True. Default setting is True .
DRAM Corrected Counter Leak Rate	Program Rate value for DRAM Corrected Error Counter function. Default setting is 7 .
DRAM Corrected Error Counter Start Count	Program starting value for DRAM Corrected Error Counter function. Default setting is FFF5 .

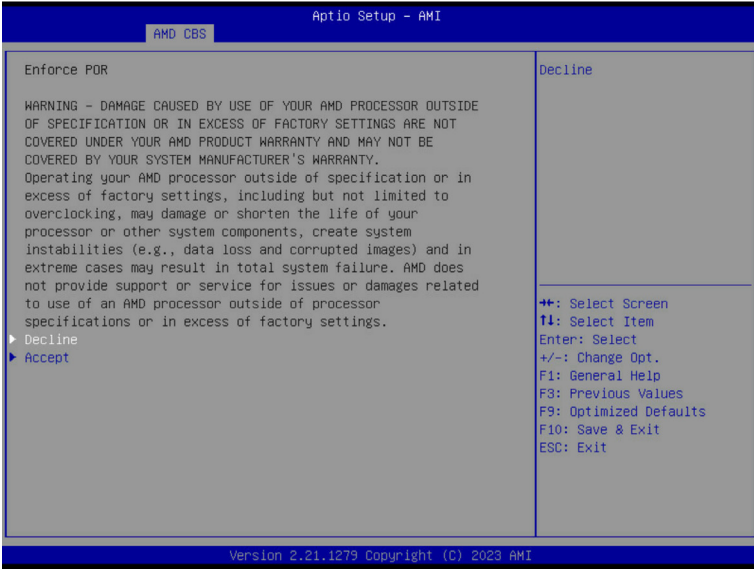
2-3-3-5 DDR Bus Configuration



Parameter	Description
DDR Bus Configuration	
Dram ODT impedance RTT_NOM_WR	Select the DRAMs On-die Termination impedance for RTT_NOM_WR. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80), RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is Auto .
Dram ODT impedance RTT_NOM_RD	Select the DRAMs On-die Termination impedance for RTT_NOM_RD. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80), RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is Auto .
Dram ODT impedance RTT_WR	Select the DRAMs On-die Termination impedance for RTT_WR. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80), RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is Auto .
Dram ODT impedance RTT_PARK	Select the DRAMs On-die Termination impedance for RTT_PARK. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80), RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is Auto .
Dram ODT impedance DQS_RTT_PARK	Select the DRAMs On-die Termination impedance for DQS_RTT_PARK. Options available: Auto, RTT_OFF, RZQ (240), RZQ/2 (120), RZQ/3 (80), RZQ/4 (60), RZQ/5(48), RZQ/6(40), RZQ/7(34). Default setting is Auto .

Parameter	Description
Processor ODT impedance	Select the ODT impedance for all DBYTE IOs. Options available: Auto, High Impedance, 480 ohm, 240 ohm, 160 ohm, 120 ohm, 96 ohm, 80 ohm, 68.6 ohm, 60 ohm, 53.3 ohm, 48 ohm, 43.6 ohm, 40 ohm, 36.9 ohm, 34.3 ohm, 32 ohm, 30 ohm, 28.2 ohm, 26.7 ohm, 25.3 ohm. Default setting is Auto .
Dram DQ drive strengths	Select the Dram Pull-up and Pull-Down Output Driver Impedance for all DQ and DMI IOs. Options available: Auto, 48 ohm, 40 ohm, 34 ohm, Default setting is Auto .

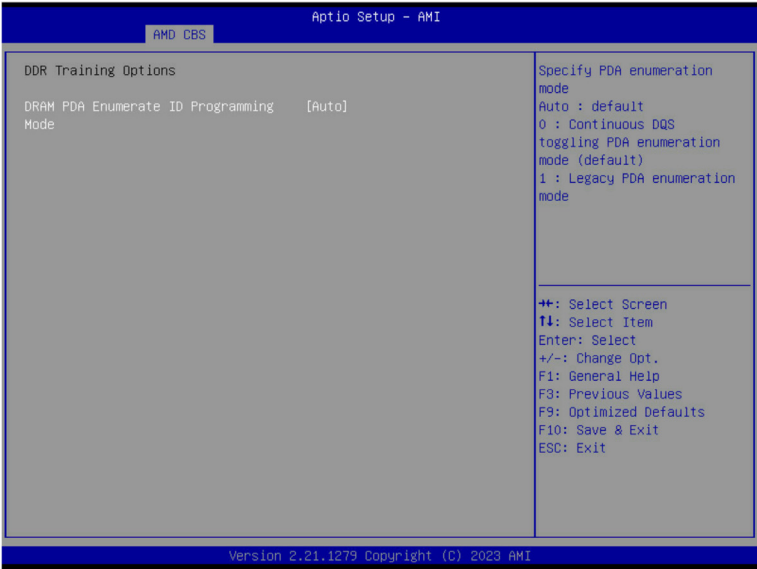
2-3-3-6 Enforce POR



Parameter	Description
Enforce POR	Decline/Accept to configure the advanced items.
Accept	
Active Memory Timing Settings ^(Note)	Active memory Timing Settings. Options available: Auto, Enabled. Default setting is Auto .
Memory Target Speed	Specifies the memory target speed in MT/s. Options available: Auto, DDR3200, DDR3600, DDR4000, DDR4400, DDR4800, DDR5200, DDR5600. Default setting is Auto .
SPD Timing	Press [Enter] to configure advanced items.
Non-SPD Timing	Press [Enter] to configure advanced items.

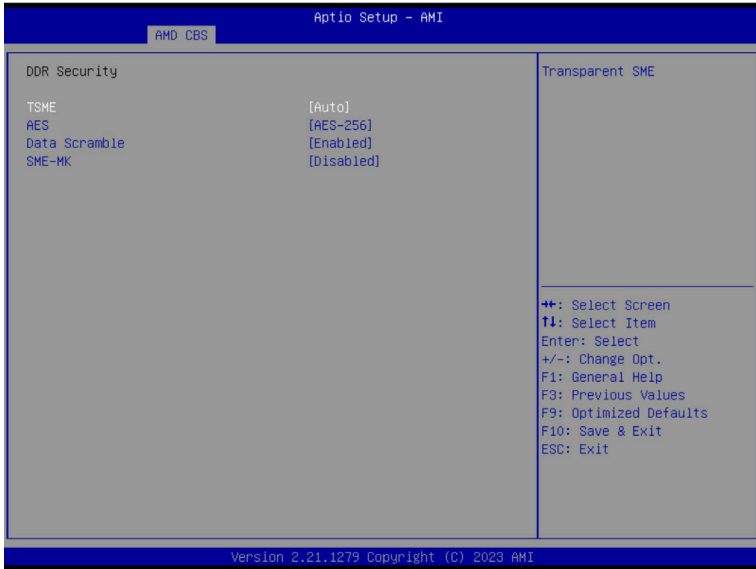
(Note) Advanced items prompt when this item is defined.

2-3-3-7 DDR Training Options



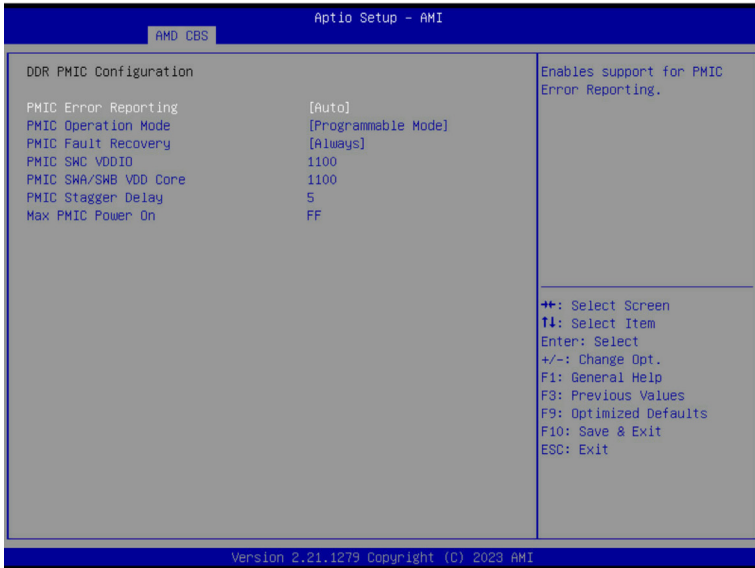
Parameter	Description
DDR Training Options	
DRAM PDA Enumerate ID Programming Mode	Specify PDA enumeration mode. Options available: Auto, Toggling PDA enumeration mode, Legacy PDA enumeration mode. Default setting is Auto .

2-3-3-8 DDR Security



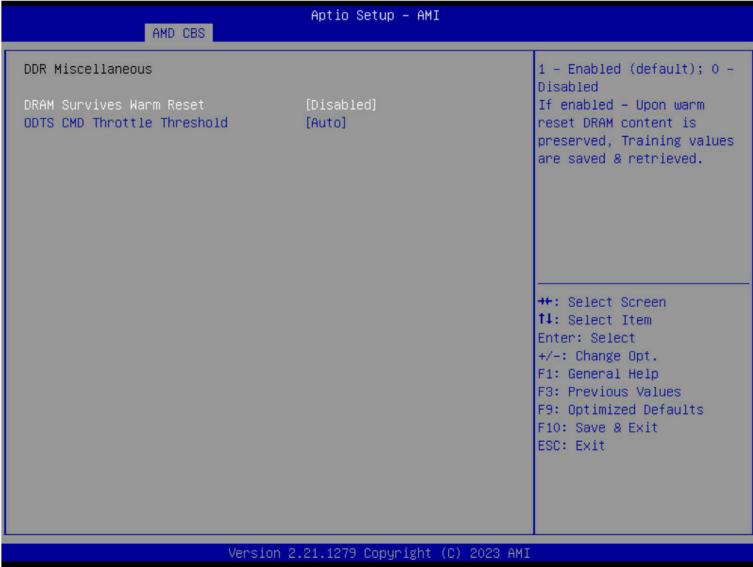
Parameter	Description
Security	
TSME	Enable/Disable Transparent SME. Options available: Auto, Enabled, Disabled. Default setting is Auto .
AES	Options available: AES-128, AES-256. Default setting is AES-256 .
Data Scramble	Enable/Disable Data Scrambling. Options available: Enabled, Disabled. Default setting is Enabled .
SME-MK	Options available: Enabled, Disabled. Default setting is Disabled .

2-3-3-9 DDR PMIC Configuration



Parameter	Description
DDR PMIC Configuration	
PMIC Error Reporting	Enables support for PMIC Error Reporting. Options available: Auto, False, True. Default setting is Auto .
PMIC Operation Mode	Options available: Secure Mode, Programmable Mode. Default setting is Programmable Mode .
PMIC Fault Recovery	Options available: Always, Never, Once. Default setting is Always .
PMIC SWC VDDIO	Default setting is 1100 .
PMIC SWA/SWB VDD Core	Default setting is 1100 .
PMIC Stagger Delay	Default setting is 5 .
Max PMIC Power On	Default setting is FF .

2-3-3-10 DDR Miscellaneous



Parameter	Description
DDR Miscellaneous	
DRAM Survives Warm Reset	Options available: Enabled, Disabled. Default setting is Disabled .
ODTS CMD Throttle Threshold	Options available: Auto, > 85°C, > 90°C, > 95°C. Default setting is Auto .

2-3-3-11 DDR PHY (CMN)



Parameter	Description
DDR PHY (CMN)	
Periodic Training ^(Note)	Options available: Auto, Enabled, Disabled. Default setting is Auto .
Periodic Training Interval	Specifies the Periodic Training interval in millisecond.

(Note) Advanced items prompt when this item is defined.

2-3-4 NBIO Common Options

Aptio Setup - AMI

AMD CBS

NBIO Common Options		Enable/Disable IOMMU
IOMMU	[Enabled]	
DHAR Support	[Auto]	
DMA Protection	[Auto]	
DRTM Virtual Device Support	[Auto]	
DRTM Memory Reservation	[Auto]	
ACS Enable	[Auto]	
PCIe ARI Support	[Auto]	
PCIe ARI Enumeration	[Auto]	
PCIe Ten Bit Tag Support	[Auto]	
▶ SMU Common Options		
▶ NBIO RAS Common Options		
Enable AER Cap	[Auto]	
Early Link Speed	[Auto]	
Hot Plug Handling mode	[Auto]	
Hot Plug Allow FF in Synchronous	[Disabled]	
Presence Detect Select mode	[Auto]	
Data Link Feature Cap	[Auto]	
CV test	[Auto]	
SEV-SNP Support	[Auto]	
Allow Compliance	[Auto]	
SRIS	[Auto]	
Multi Upstream Auto Speed Change	[Auto]	

++: Select Screen
 ↑↓: Select Item
 Enter: Select
 +/-: Change Opt.
 F1: General Help
 F3: Previous Values
 F9: Optimized Defaults
 F10: Save & Exit
 ESC: Exit

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Aptio Setup - AMI

AMD CBS

PCIe ARI Enumeration	[Auto]	Enable this setting to use 2 symbols per clock for devices at Gen 4 speed.
PCIe Ten Bit Tag Support	[Auto]	
▶ SMU Common Options		
▶ NBIO RAS Common Options		
Enable AER Cap	[Auto]	
Early Link Speed	[Auto]	
Hot Plug Handling mode	[Auto]	
Hot Plug Allow FF in Synchronous	[Disabled]	
Presence Detect Select mode	[Auto]	
Data Link Feature Cap	[Auto]	
CV test	[Auto]	
SEV-SNP Support	[Auto]	
Allow Compliance	[Auto]	
SRIS	[Auto]	
Multi Upstream Auto Speed Change	[Auto]	
Multi Auto Speed Change On Last Rate	[Auto]	
PCIe Link Speed Capability	[Auto]	
RTM Margining Support	[Auto]	
EQ Bypass To Highest Rate	[Auto]	
Non-PCIe Compliant Support	[Auto]	
PCIe Idle Power Setting	[Optimize for Perf/Power]	
▶ nBif Common Options		
▶ Link EQ Preset Options		
Enable 2 SPC (Gen 4)	[Auto]	

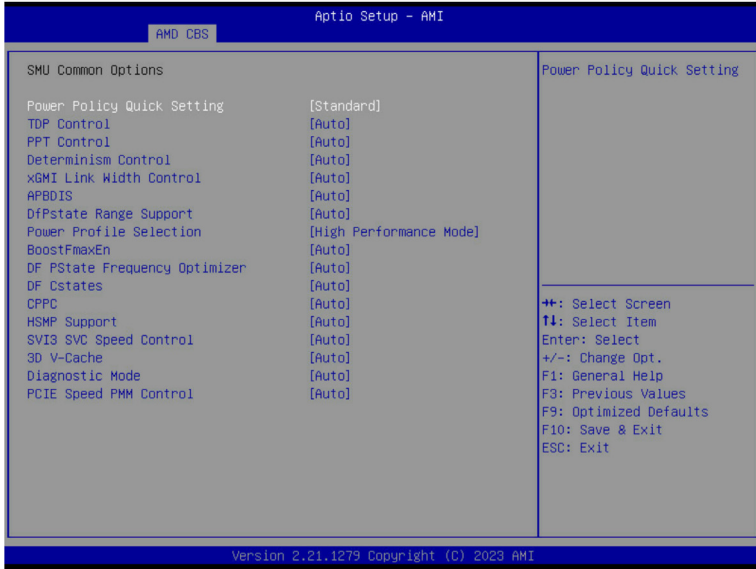
++: Select Screen
 ↑↓: Select Item
 Enter: Select
 +/-: Change Opt.
 F1: General Help
 F3: Previous Values
 F9: Optimized Defaults
 F10: Save & Exit
 ESC: Exit

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Parameter	Description
NBIO Common Options	
IOMMU	Enable/Disable the IOMMU function. Options available: Disabled, Enabled. Default setting is Enabled .
DMAr Support	Enable/Disable DMAr system protection during POST. Options available: Disabled, Enabled, Auto. Default setting is Auto .
DMA Protection	Enable/Disable DMA remap support in IVRS IVinfo Field. Options available: Auto, Enabled, Disabled. Default setting is Auto .
DRTM Virtual Device Support	Enable/Disable DRTM ACPI virtual device. Options available: Disabled, Enabled, Auto. Default setting is Auto .
DRTM Memory Reservation	Enable/Disable DRTM Memory reservation. Options available: Disabled, Enabled, Auto. Default setting is Auto .
ACS Enable	Enable/Disable ACS. Options available: Enable, Disabled, Auto. Default setting is Auto .
PCIe ARI Support	Enable/Disable Alternative Routing-ID Interpretation. Options available: Disable, Enable, Auto. Default setting is Auto .
PCIe ARI Enumeration	ARI Forwarding Enable for each downstream port. Options available: Disable, Enable, Auto. Default setting is Auto .
PCIe Ten Bit Tag Support	Enable/Disable PCIe ten bit tags for supported devices. (Auto=Disabled) Options available: Disable, Enable, Auto. Default setting is Auto .
SMU Common Options	Press [Enter] for configuration of advanced items.
NBIO RAS Common Options	Press [Enter] for configuration of advanced items.
Enable AER Cap	Enable/Disable Advanced Error Reporting Capability. Options available: Enable, Disabled, Auto. Default setting is Auto .
Early Link Speed	Configures Early Link Speed. Options available: Auto, Gen1, Gen2. Default setting is Auto .
Hot Plug Handling mode	Controls the Hot Plug Handling mode. Options available: OS First, Firmware First/EDR if OS supports, Firmware First but allow OS First, System Firmware Intermediary, Auto. Default setting is Auto .
Hot Plug Allow FF in Synchronous	Allows firmware first hot plug handling mode to operate in mode A and mode B synchronous mappings. Options available: Disabled, Enabled. Default setting is Disabled .
Presence Detect Select mode	Controls the Presence Detect Select mode. Options available: OR, AND, Auto. Default setting is Auto .

Parameter	Description
Data Link Feature Cap	Enable/Disable the data link feature capability. Options available: Enabled, Disabled, Auto. Default setting is Auto .
CV test	Enable/Disable the running PCIE CV tool support. Options available: Auto, Enabled, Disabled. Default setting is Auto .
SEV-SNP Support	Enable/Disable the SEV-SNP support. Options available: Disable, Enable. Default setting is Disable .
Allow Compliance	When enabled, allows the PCIe RP to enter Polling.Compliance state. Options available: Auto, Disable, Enable. Default setting is Auto .
SRIS	Options available: Auto, Disable, Enable. Default setting is Auto .
Multi Upstream Auto Speed Change	Defines the setting of this feature for all PCIe devices. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Multi Auto Speed Change On Last Rate	Options available: Disable, Enable, Auto. Default setting is Auto .
PCIE Link Speed Capability	Options available: Maximum speed, Gen1, Gen2, Gen3, Gen4, Gen5, Auto. Default setting is Auto .
RTM Margining Support	Options available: Disable, Enable, Auto. Default setting is Auto .
EQ Bypass To Highest Rate	Options available: Disable, Enable, Auto. Default setting is Auto .
Non-PCIe Compliant Support	Options available: Disable, Enable, Auto. Default setting is Auto .
PCIE Idle Power Setting	Modify PCIE Power Savings Features that can impact lightly loaded latency. Options available: Optimize for Latency, Optimize for Perf/Power. Default setting is Optimize for Latency .
nBif Common Options	Press [Enter] for configuration of advanced items.
Link EQ Preset Options	Press [Enter] for configuration of advanced items.
Enable 2 SPC (Gen4)	Enable this setting to use 2 symbols per clock for devices at Gen4 speed. Options available: Auto, Enabled. Default setting is Auto .

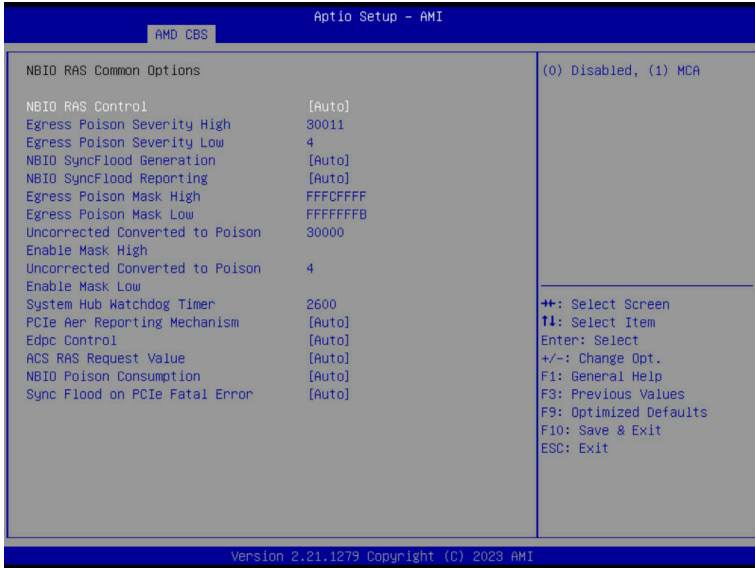
2-3-4-1 SMU Common Options



Parameter	Description
SMU Common Options	
Power Policy Quick Setting	Options available: Standard, Best Performance, Energy Efficient. Default setting is Standard .
TDP Control	Options available: Manual, Auto. Default setting is Auto .
PPT Control	Options available: Manual, Auto. Default setting is Auto .
Determinism Control	Selects use the fused Determinism or set customized Determinism. Options available: Manual, Auto. Default setting is Auto .
xGMI Link Width Control	Options available: Manual, Auto. Default setting is Auto .
APBDIS	Options available: 0, 1, Auto. Default setting is Auto .
DfPstate Range Support	Options available: Disable, Enable, Auto. Default setting is Auto .
Power Profile Selection	Options available: High Performance Mode, Efficiency Mode, Maximum IO Performance Mode. Default setting is High Performance Mode .
BoostFmaxEn	Options available: Manual, Auto. Default setting is Auto .
DF PState Frequency Optimizer	Options available: Auto, Enabled, Disabled. Default setting is Auto .
DF Cstates	Options available: Disabled, Enabled, Auto. Default setting is Disabled .

Parameter	Description
CPPC	Enable/Disable the CPPC feature. Options available: Disabled, Enabled, Auto. Default setting is Auto .
HSMP Support	Enable/Disable the HSMP support. Options available: Disabled, Enabled, Auto. Default setting is Auto .
SVI3 SVC Speed Control	Options available: Auto, Manual. Default setting is Auto .
3D V-Cache	Options available: Auto, Disable, 1 stack, 2 stack, 4 stack. Default setting is Auto .
Diagnostic Mode	Options available: Disabled, Enabled, Auto. Default setting is Auto .
PCIe Speed PMM Control	Options available: Dynamic link speed determined by Power Management functionality, Static Target Link Speed (GEN4), Static Target Link Speed (GEN5), Auto. Default setting is Auto .

2-3-4-2 NBIO RAS Common Options



Parameter	Description
NBIO RAS Common Options	
NBIO RAS Control	Options available: Disabled, MCA, Auto. Default setting is Auto .
Egress Poison Severity High	Configures the Egress Poison High Severity. Each bit set to 1 enables High severity on the associated IOHC egress port. A bit of 0 indicates LOW severity.
Egress Poison Severity Low	Configures the Egress Poison Low Severity. Each bit set to 1 enables High severity on the associated IOHC egress port. A bit of 0 indicates LOW severity.
NBIO SyncFlood Generation	The value may be used to mask SyncFlood caused by NBIO RAS options. Options available: Enabled, Disabled, Auto. Default setting is Auto .
NBIO SyncFlood Reporting	The value may be used to enable SyncFlood reporting to APML. Options available: Enabled, Disabled, Auto. Default setting is Auto .
Egress Poison Mask High	Enables mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions.
Egress Poison Mask Low	Enables mask for masking of errors logged in EGRESS_POISON_STATUS. For each bit set to 1, errors are masked. For each bit set to 0, errors trigger response actions.

Parameter	Description
Uncorrected Converted to Poison Enable Mask High	Enables mask for masking of uncorrectable parity errors on internal arrays.
Uncorrected Converted to Poison Enable Mask Low	Enables mask for masking of uncorrectable parity errors on internal arrays.
System Hub Watchdog Timer	Specifies the timer interval of the SYSHUB Watchdog timer in milliseconds.
PCIe Aer Reporting Mechanism	Selects the method of reporting AER errors from PCI Express. Options available: Firmware First, Firmware First but allow OS First, OS First, Auto. Default setting is Auto .
Edpc Control	Options available: Disabled, Enabled, Auto. Default setting is Auto .
ACS RAS Request Value	Options available: Direct Request Access Enabled, Request Blocking Enabled, Request Redirect Enabled, Auto. Default setting is Auto .
NBIO Poison Consumption	Options available: Auto, Enabled, Disabled. Default setting is Auto .
Sync Flood on PCIe Fatal Error	Options available: Auto, True, False. Default setting is Auto .

2-3-4-3 nBif Common Options



Parameter	Description
MPDMA-TF	<ul style="list-style-type: none"> ◆ SRIOV <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ ARI <ul style="list-style-type: none"> – Options available: Auto/Default, Disabled, Enabled. Default setting is Auto/Default. ◆ AER <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ ACS <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ ATS <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ PASID <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ RTR <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ PAGE_REQ <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ PWR <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ ATC_ENABLE <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto.

Parameter	Description
MPDMA-TF (continued)	<ul style="list-style-type: none"> ◆ SDXI Class Code <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ PASID Control <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ ATOMICOP_REQUEST <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto.
RCC_DEV0	<ul style="list-style-type: none"> ◆ ACS Rcc_Dev0 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ AER Rcc_Dev0 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ DlfEnableStrap1 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ Phy16GTStrap1 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ MarginEnStrap1 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ SourceValStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ TranslationalBlockingStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ P2pReq ACS Control <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ P2pCompStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ UpstreamFwdStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ P2PEgressStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ DirectTranslatedStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ SsidEnStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ PriEnPageReq <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ PriResetPageReq <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ SourceVal ACS cntl <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ TranslationalBlocking ACS Control <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ P2pComp ACS Control <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ UpstreamFwd ACS Control <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto.

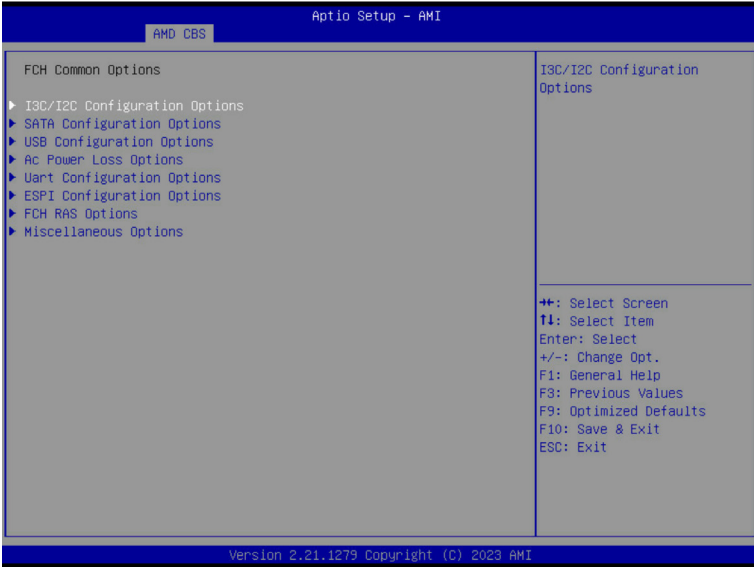
Parameter	Description
RCC_DEVO (continued)	<ul style="list-style-type: none"> ◆ P2PEgress ACS Control <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ P2pReqStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ E2E_PREFIX <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ EXTENDED_FMT <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto. ◆ AtomicRoutingStrap5 <ul style="list-style-type: none"> – Options available: Auto, Disabled, Enabled. Default setting is Auto.

2-3-4-4 Link EQ Preset Options



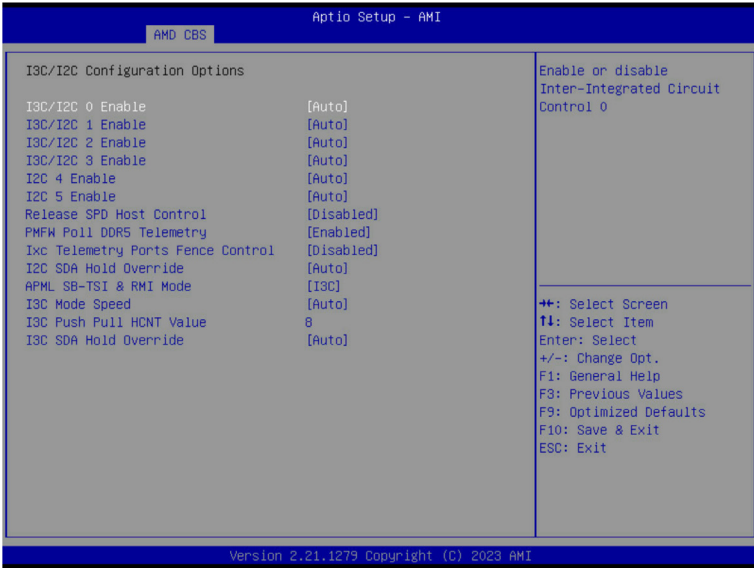
Parameter	Description
GEN3/4/5	<p>Press [Enter] to configure advanced items.</p> <ul style="list-style-type: none"> ◆ Preset Search Mask Configuration <ul style="list-style-type: none"> – Options available: Custom, Auto. Default setting is Auto.

2-3-5 FCH Common Options



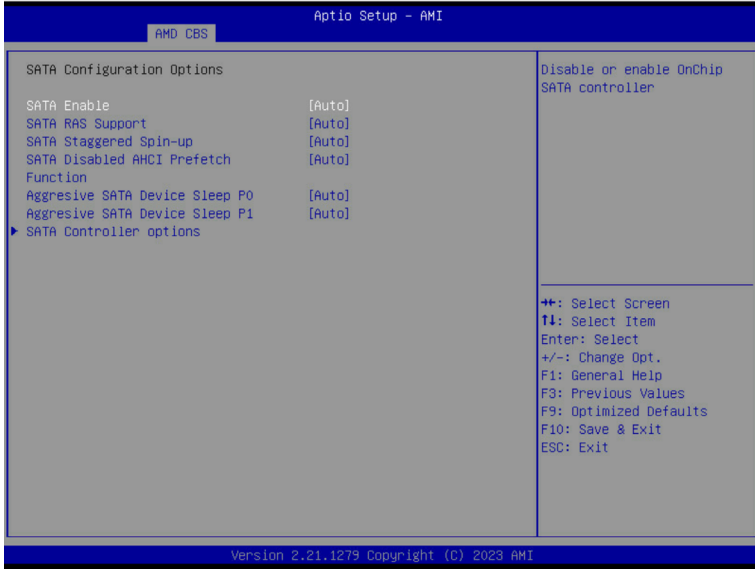
Parameter	Description
FCH Common Options	
I3C/I2C Configuration Options	Press [Enter] for configuration of advanced items.
SATA Configuration Options	Press [Enter] for configuration of advanced items.
USB Configuration Options	Press [Enter] for configuration of advanced items.
AC Power Loss Options	Press [Enter] for configuration of advanced items.
Uart Configuration Options	Press [Enter] for configuration of advanced items.
ESPI Configuration Options	Press [Enter] for configuration of advanced items.
FCH RAS Options	Press [Enter] for configuration of advanced items.
Miscellaneous Options	Press [Enter] for configuration of advanced items.

2-3-5-1 I3C/I2C Configuration Options



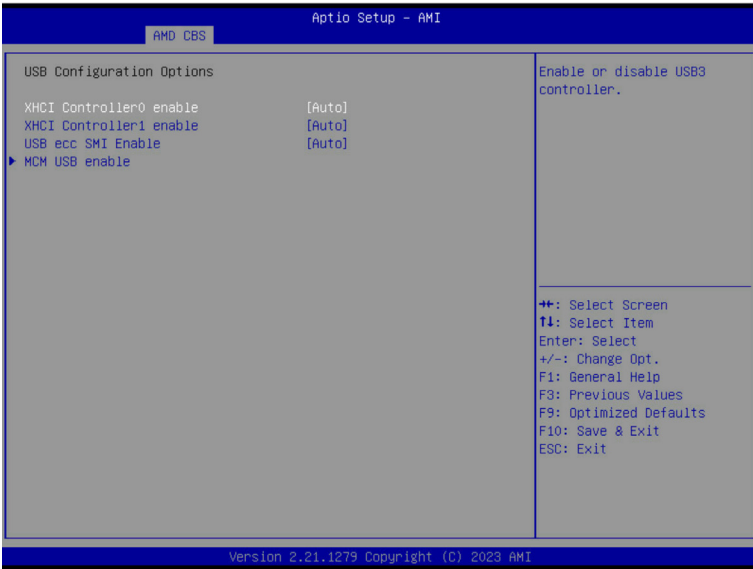
Parameter	Description
I3C/I2C Configuration Options	
I3C/I2C 0/1/2/3 Enable	Options available: Both Disabled, I3C Enabled, I2C Enabled, Auto. Default setting is Auto .
I2C 4/5 Enable	Options available: Disabled, Enabled, Auto. Default setting is Auto .
Release SPD Host Control	Options available: Disabled, Enabled. Default setting is Disabled .
PMFW Poll DDR5 Telemetry	Options available: Disabled, Enabled. Default setting is Enabled .
Ixc Telemetry Ports Fence Control	Options available: Disabled, Enabled. Default setting is Disabled .
I2C SDA Hold Override	Options available: Disabled, Enabled, Auto. Default setting is Auto .
APML SB-TSI & RMI Mode	Options available: I3C, I2C. Default setting is I3C .
I3C Mode Speed	Options available: SDR2(6MHz), SDR0(12.5MHz), Auto. Default setting is Auto .
I3C Push Pull HCNT Value	SCL push-pull High count for I3C transfers targeted to I3C devices.
I3C SDA Hold Override	Override I3C SDA Hold value. Options available: Disabled, Enabled, Auto. Default setting is Auto .

2-3-5-2 SATA Configuration Options



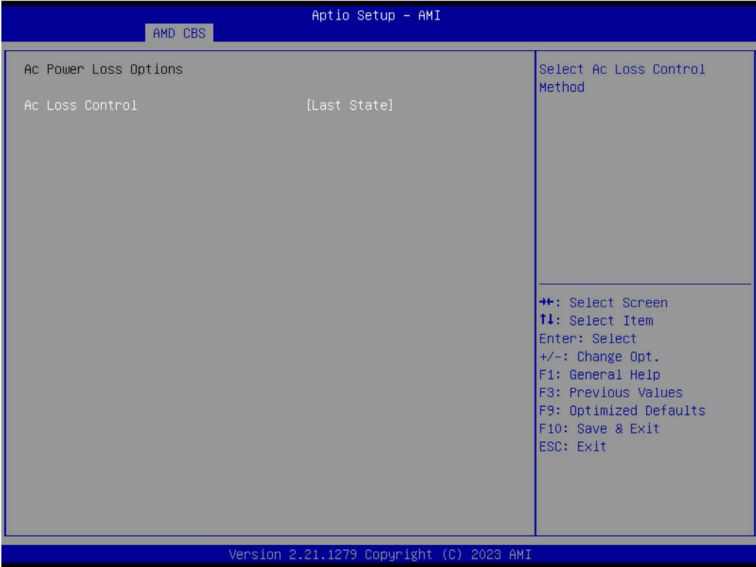
Parameter	Description
SATA Configuration Options	
SATA Enable	Enable/Disable OnChip SATA controller. Options available: Disabled, Enabled, Auto. Default setting is Auto .
SATA RAS Support	Options available: Disabled, Enabled, Auto. Default setting is Auto .
SATA Staggered Spin-up	Options available: Disabled, Enabled, Auto. Default setting is Auto .
SATA Disabled AHCI Prefetch Function	Options available: Disabled, Enabled, Auto. Default setting is Auto .
Aggressive SATA Device Sleep P0/P1	Options available: Disabled, Enabled, Auto. Default setting is Auto .
SATA Controller options	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> ◆ SATA Controller Enable ◆ SATA Controller eSATA ◆ SATA Controller DevSlp ◆ SATA Controller SGPIO

2-3-5-3 USB Configuration Options



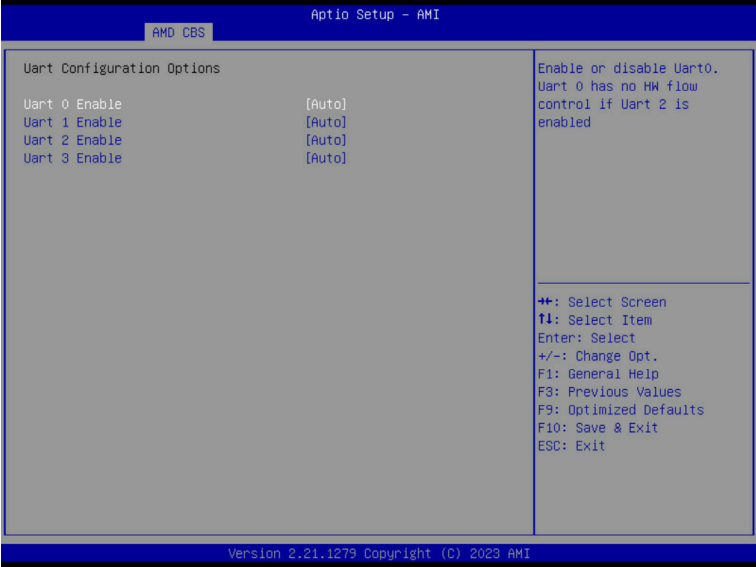
Parameter	Description
USB Configuration Options	
XHCI Controller0/1 enable	Enable/Disable USB controller. Options available: Enabled, Disabled, Auto. Default setting is Auto .
USB ecc SMI Enable	Options available: Enable, Off, Auto. Default setting is Auto .
MCM USB enable	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> ◆ XHCI2/ XHCI3 enable (Socket1) <ul style="list-style-type: none"> – Options available: Enabled, Disabled, Auto. Default setting is Auto.

2-3-5-4 AC Power Loss Options



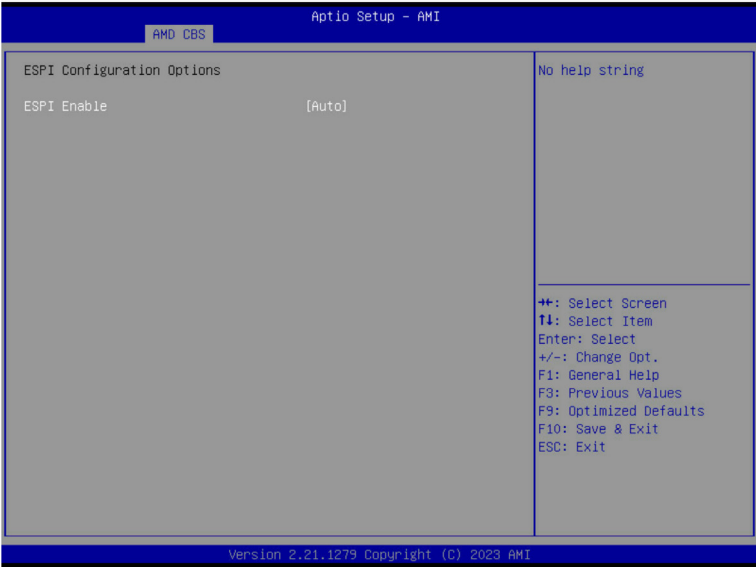
Parameter	Description
AC Power Loss Options	
AC Loss Control	Selects the AC Loss Control Method. Options available: Power Off, Power On, Last State. Default setting is Last State .

2-3-5-5 Uart Configuration Options



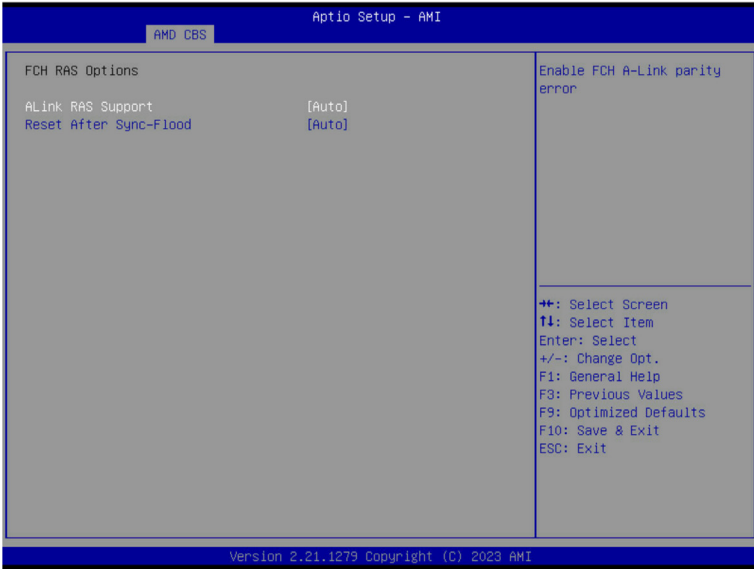
Parameter	Description
Uart Configuration Options	
Uart 0/1/2/3 Enable	Options available: Disabled, Enabled, Auto. Default setting is Auto .

2-3-5-6 ESPI Configuration Options



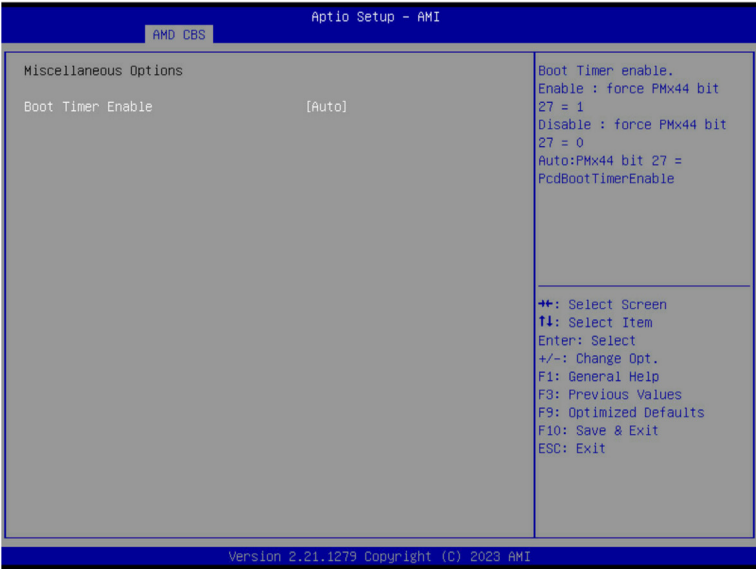
Parameter	Description
ESPI Configuration Options	
ESPI Enable	Options available: Disabled, Enabled, Auto. Default setting is Auto .

2-3-5-7 FCH RAS Options



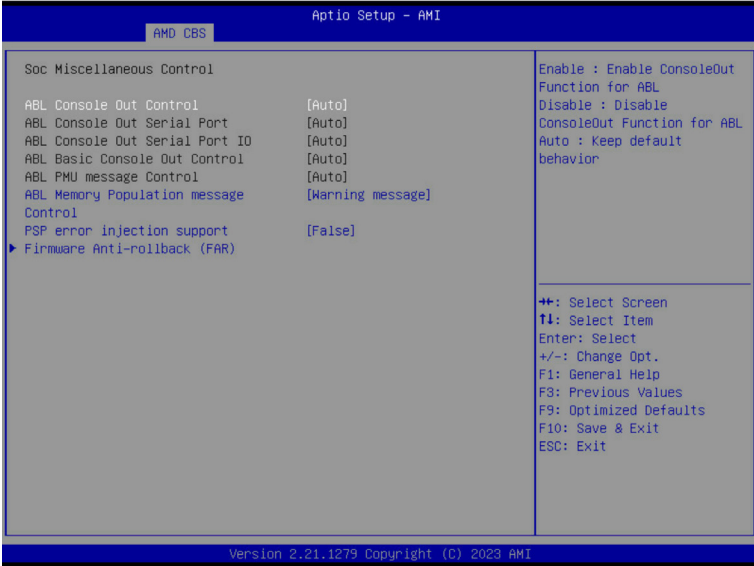
Parameter	Description
FCH RAS Options	
ALink RAS Support	Enable/Disable the ALink RAS Support. Options available: Disabled, Enabled, Auto. Default setting is Auto .
Reset After Sync-Flood	Enables AB to forward downstream sync-flood message to system controller. Options available: Enable, Disable, Auto. Default setting is Auto .

2-3-5-8 Miscellaneous Options



Parameter	Description
Miscellaneous Options	
Boot Timer Enable	Enable/Disable Boot Timer. Options available: Disabled, Enabled, Auto. Default setting is Auto .

2-3-6 SOC Miscellaneous Control

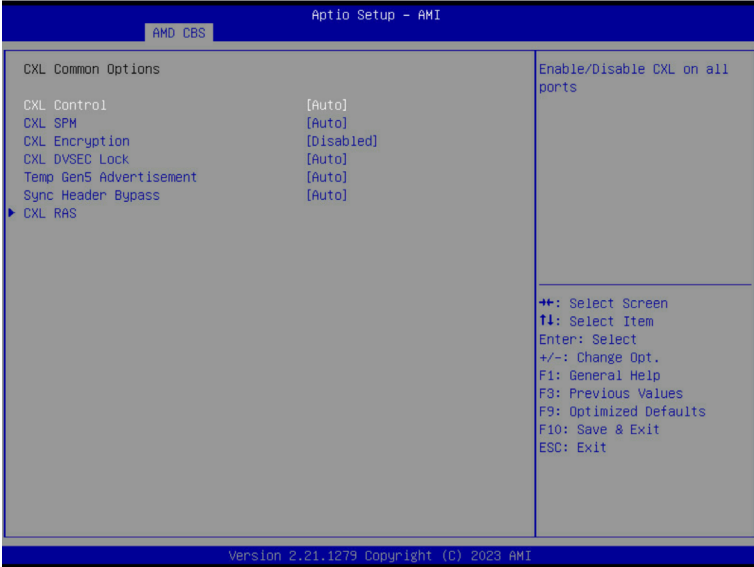


Parameter	Description
SOC Miscellaneous Control	
ABL Console Out Control ^(Note)	Enable/Disable the ConsoleOut function for ABL. Options available: Disable, Enable, Auto. Default setting is Auto .
ABL Console Out Serial Port ^(Note)	Options available: eSPI, SOC UART0, SOC UART1, Auto. Default setting is Auto .
ABL Console Out Serial Port IO	Options available: 0x3F8, 0x2F8, 0x3E8, 0x2E8, Auto. Default setting is Auto .
ABL Basic Console Out Control	Enable/Disable the Basic ConsoleOut function for ABL. Options available: Disable, Enable, Auto. Default setting is Auto .
ABL PMU message Control	To Control the total number of PMU debug messages. Options available: Auto, Detailed debug message, Coarse debug message, Stage completion, Assertion messages, Firmware completion message only. Default setting is Auto .
ABL Memory Population message Control	Options available: Warning message, Fatal error. Default setting is Warning message .
PSP error injection support	Options available: False, True. Default setting is False .

(Note) Advanced items are configurable when this item is defined.

Parameter	Description
Firmware Anti-rollback (FAR)	<p data-bbox="362 158 753 185">Press [Enter] for configuration of advanced items.</p> <ul data-bbox="362 189 950 357" style="list-style-type: none"><li data-bbox="362 189 950 241">◆ FAR enforcement state<ul data-bbox="402 216 950 241" style="list-style-type: none"><li data-bbox="402 216 950 241">– Default setting is Enabled.<li data-bbox="362 246 950 271">◆ SPL value in the CPU Fuse<li data-bbox="362 275 950 301">◆ SPL value in the SPL table<li data-bbox="362 305 950 357">◆ FAR Switch<ul data-bbox="402 332 950 357" style="list-style-type: none"><li data-bbox="402 332 950 357">– Options available: Disabled, Enabled, Auto. Default setting is Auto.

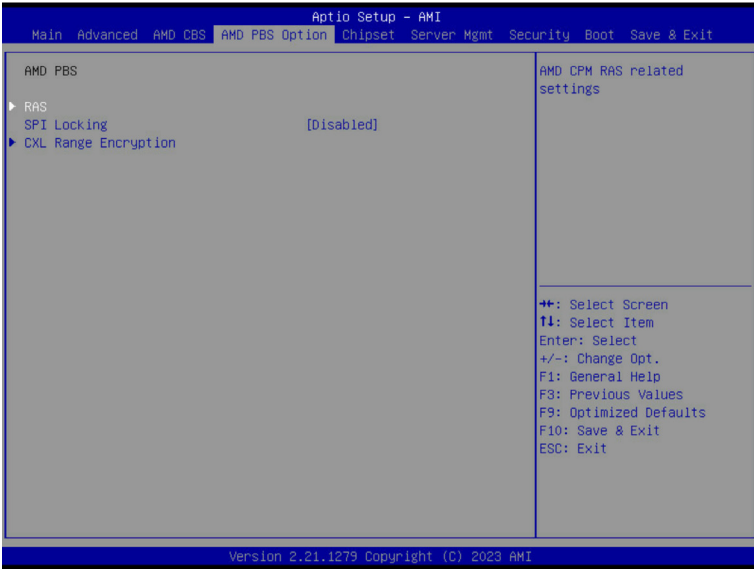
2-3-7 CXL Common Options



Parameter	Description
CXL Common Options	
CXL Control	Options available: Auto, Enabled, Disabled. Default setting is Auto .
CXL SPM	Options available: Auto, Enabled, Disabled. Default setting is Auto .
CXL Encryption	Options available: Enabled, Disabled. Default setting is Disabled .
CXL DVSEC Lock	Options available: Auto, Enabled, Disabled. Default setting is Auto .
Temp Gen5 Advertisement	Options available: Disabled, Enabled, Auto. Default setting is Auto .
Sync Header Bypass	Options available: Auto, Enabled, Disabled. Default setting is Auto .
CXL RAS	<p>Press [Enter] for configuration of advanced items.</p> <ul style="list-style-type: none"> ◆ CXL Protocol Error Reporting <ul style="list-style-type: none"> – Options available: Disabled, SameAsPcieAer, ForceAerFwFirstIfCxlPresent. Default setting is SameAsPcieAer. ◆ CXL Component Error Reporting <ul style="list-style-type: none"> – Options available: OS First, FW-First. Default setting is FW-First.

2-4 AMD PBS Menu

AMD PBS Option menu displays submenu options for configuring the function of AMD PBS. Select a submenu item, then press [Enter] to access the related submenu screen.



Parameter	Description
RAS	Press [Enter] for configuration of advanced items.
SPI Locking	Enable/Disable SPI Locking for protect ROM part. Options available: Disabled, Enabled. Default setting is Disabled .
CXL Range Encryption	Press [Enter] for configuration of advanced items. <ul style="list-style-type: none"> ◆ Range 1/2/3/4/5/6/7 <ul style="list-style-type: none"> – Configure the Range 1/2/3/4/5/6/7 Memory Base. – Configure the Range 1/2/3/4/5/6/7 Memory Limit/Size. ◆ Start CXL Range Encryption

2-4-1 RAS

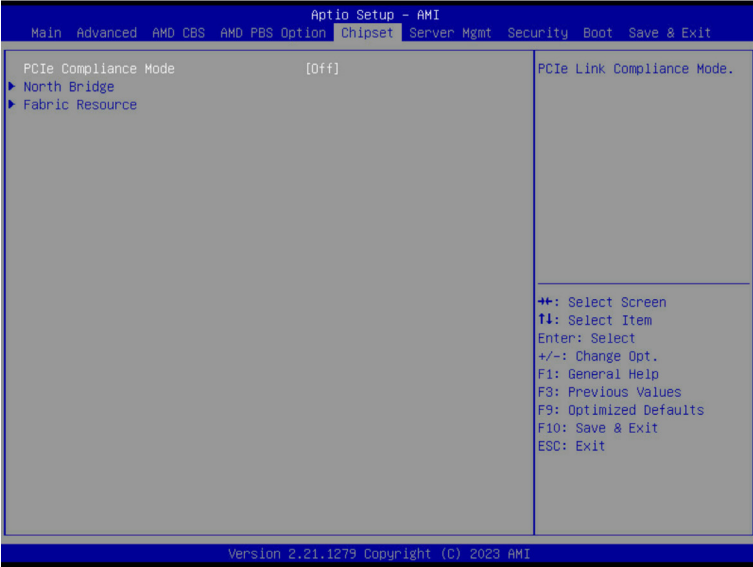


Parameter	Description
RAS Periodic SMI Control	Enable/Disable the Periodic SMI for polling [MCA Threshold] error. Options available: Disabled, Enabled. Default setting is Enabled .
SMI Threshold	Configures the SMI Threshold value.
SMI Scale	Configures the SMI Scale value.
SMI Scale Unit	Defines the unit of time scale. Options available: millisecond, second, minute. Default setting is millisecond .
SMI Period	Configures the SMI Period.
GHES Notify Type	Selects the Notification type for deferred/ corrected errors. Options available: Polled, SCI. Default setting is Polled .
GHES UnCorr Notify Type	Selects the Notification type for uncorrected errors. Options available: Polled, NMI. Default setting is NMI .
PCIe GHES Notify Type	Selects the Notification type for PCIe corrected errors. Options available: Polled, SCI. Default setting is Polled .
PCIe UnCorr GHES Notify Type	Selects the Notification type for PCIe uncorrected errors. Options available: Polled, NMI. Default setting is NMI .
PCIe Root Port Corr Err Mask Reg	Initialize the PCIe AER Corrected Error Mask register of Root Port.

Parameter	Description
PCIe Root Port UnCorr Err Mask Reg	Initialize the PCIe AER Uncorrected Error Mask register of Root Port.
PCIe Root Port UnCorr Err Sev Reg	Initialize the PCIe AER Uncorrected Error Severity register of Root Port.
PCIe Device Corr Err Mask Reg	Initialize the PCIe AER Corrected Error Mask register of PCIe device.
PCIe Device UnCorr Err Mask Reg	Initialize the PCIe AER Uncorrected Error Mask register of PCIe device.
PCIe Device UnCorr Err Sev Reg	Initialize the PCIe AER Uncorrected Error Severity register of PCIe device.
DRAM Hard Post Package Repair	This feature allows spare DRAM rows to replace malfunctioning rows via an in-field repair mechanism. Options available: Disabled, Enabled. Default setting is Disabled .
HEST DMC Structure Support	HEST DMC (Deferred Machine Check) Structure Support. Options available: Disabled, Enabled. Default setting is Disabled .
CXL Error Report Support	Enable/Disable CXL Error Reporting. Options available: Disabled, Enabled. Default setting is Disabled .

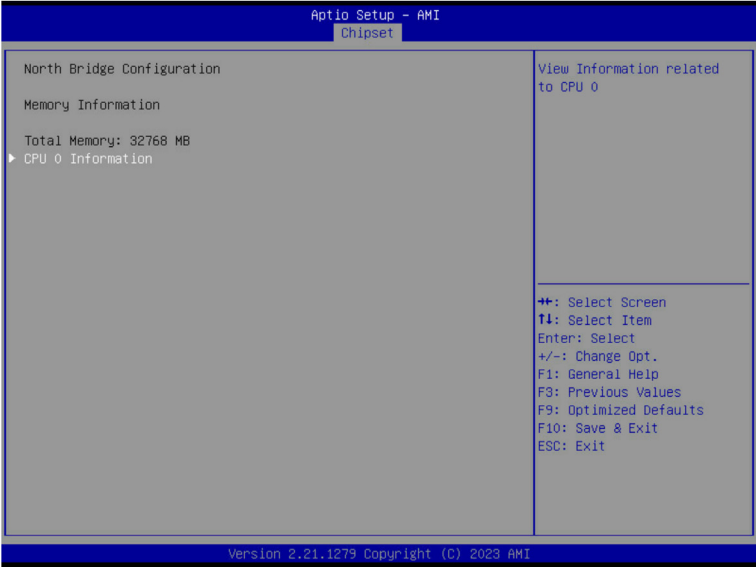
2-5 Chipset Setup Menu

Chipset Setup menu displays submenu options for configuring the function of the North Bridge. Select a submenu item, then press <Enter> to access the related submenu screen.



Parameter	Description
PCIe Compliance Mode	Options available: Off, On. Default setting is Off .
North Bridge	Press [Enter] for configuration of advanced items.
Fabric Resource	Press [Enter] for configuration of advanced items.

2-5-1 North Bridge



Parameter	Description
North Bridge Configuration	
Memory Information	
Total Memory	Displays the total memory information.
CPU 0 Information	Press [Enter] to view information related to CPU 0.

2-5-2 Fabric Resource

Aptio Setup - AMI
Chipset

Fabric Resource

Change CPU0 NBIO0 PCIe bus number

CPU0 NBIO0:
Base Bus: 0xC0
Prefetchable Mmio Above 4G Size: 1500 GB
IO Resource: 0x0
PCIe Bus Number: 40
Prefetchable Mmio Above 4G size: [System Default]
PCIe IO Resource: FFFF

CPU0 NBIO1:
Base Bus: 0x80
Prefetchable Mmio Above 4G Size: 1500 GB
IO Resource: 0x0
PCIe Bus Number: 40
Prefetchable Mmio Above 4G size: [System Default]
PCIe IO Resource: FFFF

CPU0 NBIO2:
Base Bus: 0x00
Prefetchable Mmio Above 4G Size: 1500 GB
IO Resource: 0x0
PCIe Bus Number: 40
Prefetchable Mmio Above 4G size: [System Default]
PCIe IO Resource: FFFF

++: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
ESC: Exit

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Aptio Setup - AMI
Chipset

Change CPU0 NBIO3 PCIe IO Resource

CPU0 NBIO1:
Base Bus: 0x80
Prefetchable Mmio Above 4G Size: 1500 GB
IO Resource: 0x0
PCIe Bus Number: 40
Prefetchable Mmio Above 4G size: [System Default]
PCIe IO Resource: FFFF

CPU0 NBIO2:
Base Bus: 0x00
Prefetchable Mmio Above 4G Size: 1500 GB
IO Resource: 0x0
PCIe Bus Number: 40
Prefetchable Mmio Above 4G size: [System Default]
PCIe IO Resource: FFFF

CPU0 NBIO3:
Base Bus: 0x40
Prefetchable Mmio Above 4G Size: 1500 GB
IO Resource: 0x0
PCIe Bus Number: 40
Prefetchable Mmio Above 4G size: [System Default]
PCIe IO Resource: FFFF

++: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F3: Previous Values
F9: Optimized Defaults
F10: Save & Exit
ESC: Exit

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Parameter	Description
Fabric Resource	
CPU 0 NBIO_# PCIe Bus Number	Change CPU 0 NBIO_# PCIe Bus Number.
Prefetchable Mmio Above 4G size	Change CPU 0 NBIO_# Prefetchable MMIO Above 4G Size. Options available: System Default, 0, 1G, 2G, 4G, 8G, 16G, 32G, 64G, 128G, 256G, 512G, 1T, 2T, 4T, 8T. Default setting is System Default .
PCIe IO Resource	Change CPU 0 NBIO_# PCIe IO Resource.

2-6 Server Management Menu

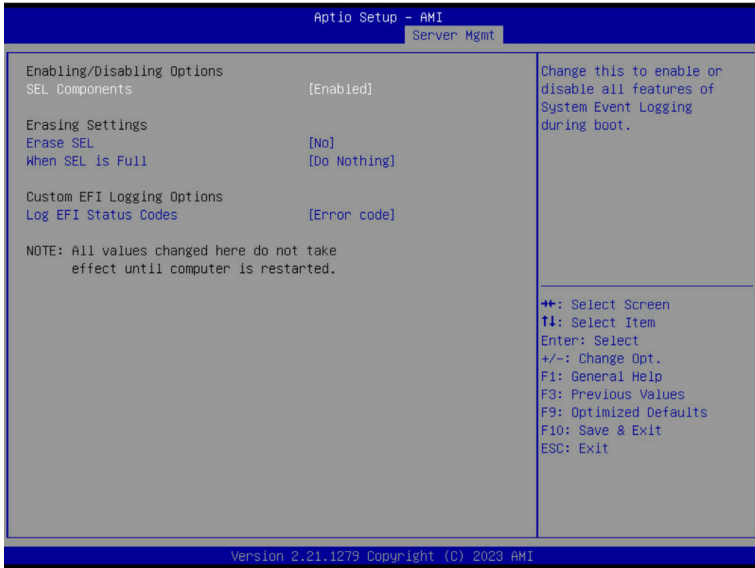


Parameter	Description
FRB-2 Timer	Enable/Disable FRB-2 timer (POST timer). Default setting is Enabled .
FRB-2 Timer timeout	Configures the FRB-2 Timer timeout. Default setting is 20 minutes .
FRB-2 Timer Policy	Configures the FRB-2 Timer policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Do Nothing .
OS Watchdog Timer	Enable/Disable OS Watchdog Timer function. Options available: Enabled, Disabled. Default setting is Disabled .
OS Wtd Timer Timeout ^(Note)	Configures OS Watchdog Timer. Options available: 5 minutes, 10 minutes, 15 minutes, 20 minutes. Default setting is 10 minutes .
OS Wtd Timer Policy ^(Note)	Configure OS Watchdog Timer Policy. Options available: Do Nothing, Reset, Power Down, Power Cycle. Default setting is Reset .
Wait BMC Ready	Post wait BMC ready and reboot system. Options available: Disabled, 2 minutes, 4 minutes, 6 minutes. Default setting is 2 minutes .

(Note) This item is configurable when **OS Watchdog Timer** is set to **Enabled**.

Parameter	Description
System Event Log	Press [Enter] to configure advanced items.
View FRU Information	Press [Enter] to view the FRU information.
BMC network configuration	Press [Enter] to configure advanced items.
IPv6 BMC Network Configuration	Press [Enter] to configure advanced items.

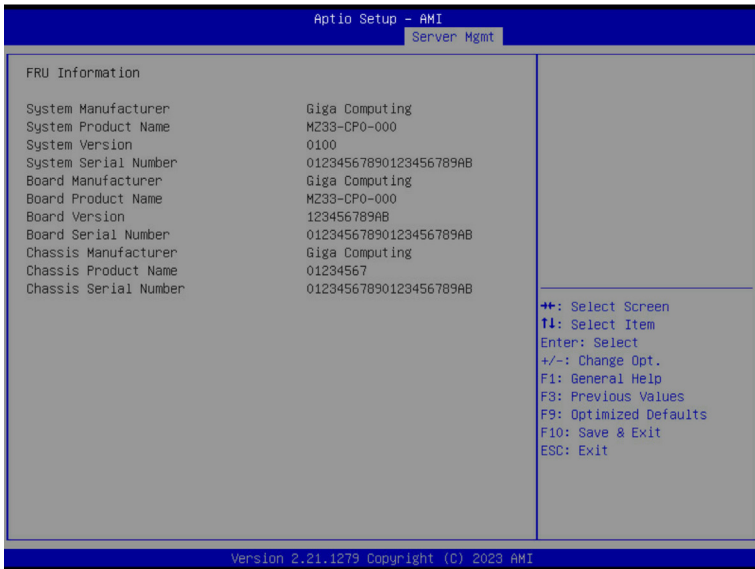
2-6-1 System Event Log



Parameter	Description
Enabling / Disabling Options	
SEL Components	Change this item to enable or disable all features of System Event Logging during boot. Options available: Disabled, Enabled. Default setting is Enabled .
Erasing Settings	
Erase SEL	Choose options for erasing SEL. Options available: No/Yes, On next reset/Yes, On every reset. Default setting is No .
When SEL is Full	Choose options for reactions to a full SEL. Options available: Do Nothing, Erase Immediately. Default setting is Do Nothing .
Custom EFI Logging Options	
Log EFI Status Codes	Enable/Disable the logging of EFI Status Codes (if not already converted to legacy). Options available: Disabled, Both, Error code, Progress code. Default setting is Error code .

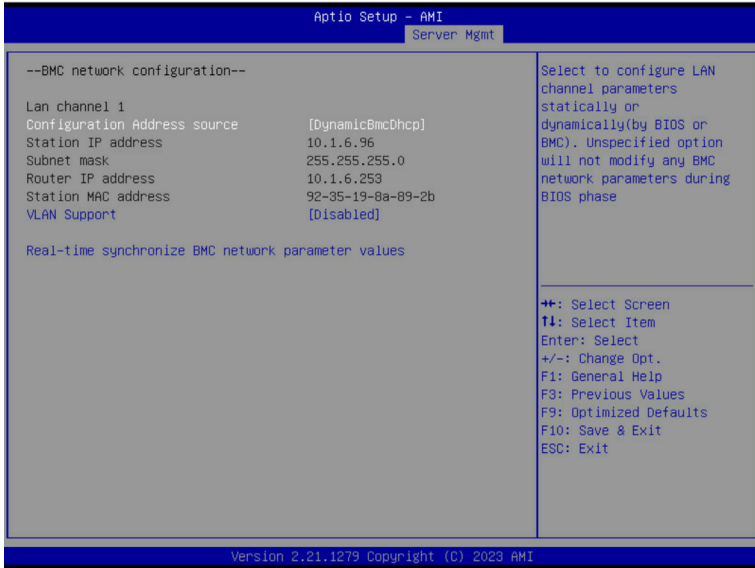
2-6-2 View FRU Information

The FRU page is a simple display page for basic system ID information, as well as System product information. Items on this window are non-configurable.



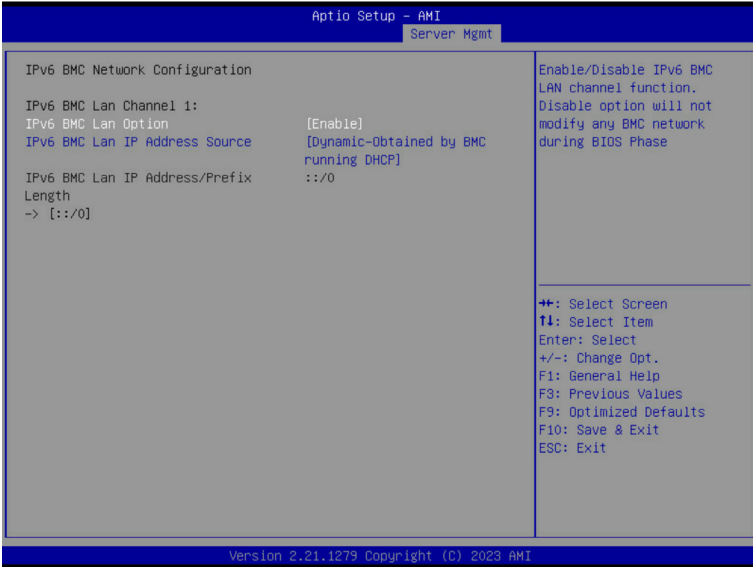
(Note) The model name will vary depends on the product you purchased

2-6-3 BMC Network Configuration



Parameter	Description
BMC network configuration	
Lan Channel 1	
Configuration Address source	Selects to configure LAN channel parameters statically or dynamically (DHCP). Do nothing option will not modify any BMC network parameters during BIOS phase. Options available: Unspecified, Static, DynamicBmcDhcp. Default setting is DynamicBmcDhcp .
Station IP address	Displays IP Address information.
Subnet mask	Displays Subnet Mask information. Please note that the IP address must be in three digitals, for example, 192.168.000.001.
Router IP address	Displays the Router IP Address information.
Station MAC address	Displays the MAC Address information.
VLAN Support	Set BMC to enable/disable VLAN support. Options available: Enabled, Disabled. Default setting is Disabled .
Real-time synchronize BMC network parameter values	Press [Enter] will set Address source(Static/DHCP) to BMC and then get Station IP address, Subnet mask and Router IP address from BMC.

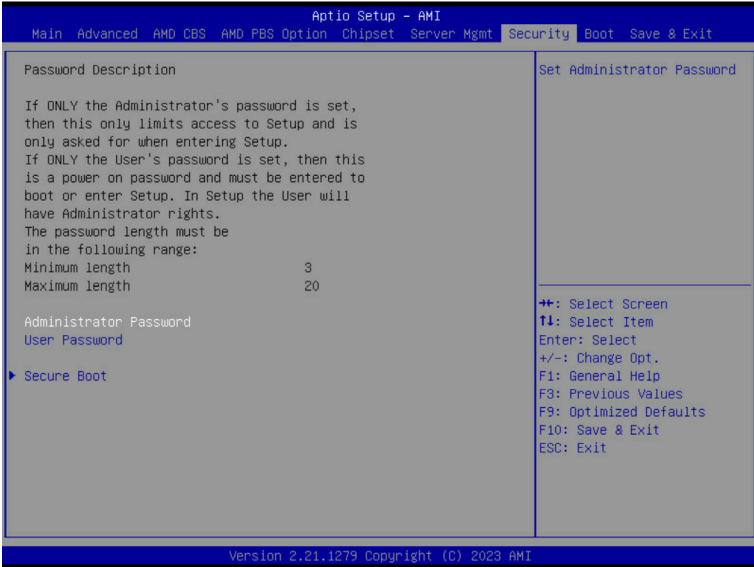
2-6-4 IPv6 BMC Network Configuration



Parameter	Description
IPv6 BMC network configuration	
IPv6 BMC Lan Channel 1	
IPv6 BMC Lan Option	Enable/Disable IPv6 BMC LAN channel function. When this item is disabled, the system will not modify any BMC network during BIOS phase. Options available: Unspecified, Disable, Enable. Default setting is Enable .
IPv6 BMC Lan IP Address Source	Selects to configure LAN channel parameters statically or dynamically (by BIOS or BMC). Options available: Unspecified, Static, Dynamic-Obtained by BMC running DHCP. Default setting is Dynamic-Obtained by BMC running DHCP .
IPv6 BMC Lan IP Address/Prefix Length	Check if the IPv6 BMC LAN IP address matches those displayed on the screen.

2-7 Security Menu

The Security menu allows you to safeguard and protect the system from unauthorized use by setting up access passwords.



There are two types of passwords that you can set:

- Administrator Password
Entering this password will allow the user to access and change all settings in the Setup Utility.
- User Password
Entering this password will restrict a user's access to the Setup menus. To enable or disable this field, a Administrator Password must first be set. A user can only access and modify the System Time, System Date, and Set User Password fields.

Parameter	Description
Administrator Password	Press [Enter] to configure the administrator password.
User Password	Press [Enter] to configure the user password.
Secure Boot	Press [Enter] to configure advanced items.

2-7-1 Secure Boot

The Secure Boot submenu is applicable when your device is installed the Windows® 8 (or above) operating system.



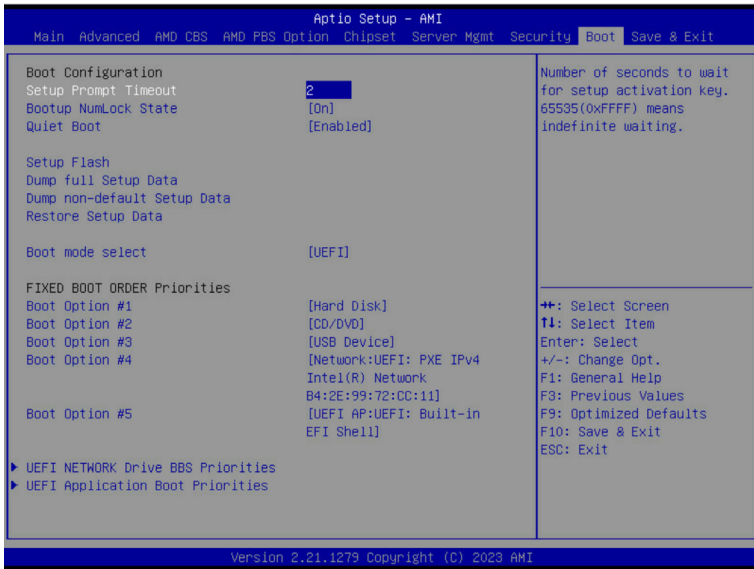
Parameter	Description
System Mode	Displays if the system is in User mode or Setup mode.
Secure Boot	Enable/ Disable the Secure Boot function. Options available: Enabled, Disabled. Default setting is Disabled .
Secure Boot Mode ^(Note)	Secure Boot requires all the applications that are running during the booting process to be pre-signed with valid digital certificates. This way, the system knows all files being loaded before Windows loads to the login screen have not been tampered with. When set to Standard, it will automatically load the Secure Boot keys from the BIOS databases. When set to Custom, you can customize the Secure Boot settings and manually load its keys from the BIOS database. Options available: Standard, Custom. Default setting is Standard .
Restore Factory Keys	Forces the system to user mode and installs factory default Secure Boot key database.
Reset To Setup Mode	Press [Enter] to reset the system mode to Setup mode.
Enter Audit Mode	Press [Enter] to set the system mode to audit mode.

(Note) Advanced items prompt when this item is set to **Custom**.

Parameter	Description
Key Management	<p data-bbox="334 158 666 177">Press [Enter] to configure advanced items.</p> <p data-bbox="334 186 937 235">Please note that this item is configurable when Secure Boot Mode is set to Custom.</p> <ul style="list-style-type: none"> <li data-bbox="334 244 944 349">◆ Factory Key Provision <ul style="list-style-type: none"> <li data-bbox="370 272 944 321">– Allows to provision factory default Secure Boot keys when system is in Setup Mode. <li data-bbox="370 327 905 349">– Options available: Enabled, Disabled. Default setting is Disabled. <li data-bbox="334 355 926 432">◆ Restore Factory Keys <ul style="list-style-type: none"> <li data-bbox="370 384 926 406">– Installs all factory default keys. It will force the system in User Mode. <li data-bbox="370 412 605 432">– Options available: Yes, No. <li data-bbox="334 439 902 517">◆ Enroll Efi Image <ul style="list-style-type: none"> <li data-bbox="370 467 902 517">– Press [Enter] to enroll SHA256 hash of the binary into Authorized Signature Database (db). <li data-bbox="334 523 898 572">◆ Secure Boot variable <ul style="list-style-type: none"> <li data-bbox="370 551 898 572">– Displays the current status of the variables used for secure boot. <li data-bbox="334 578 802 683">◆ Platform Key (PK) <ul style="list-style-type: none"> <li data-bbox="370 606 802 628">– Displays the current status of the Platform Key (PK). <li data-bbox="370 635 678 656">– Press [Enter] to configure a new PK. <li data-bbox="370 663 602 683">– Options available: Update. <li data-bbox="334 689 942 826">◆ Key Exchange Keys (KEK) <ul style="list-style-type: none"> <li data-bbox="370 718 942 740">– Displays the current status of the Key Exchange Key Database (KEK). <li data-bbox="370 746 905 794">– Press [Enter] to configure a new KEK or load additional KEK from storage devices. <li data-bbox="370 801 671 821">– Options available: Update, Append. <li data-bbox="334 832 948 969">◆ Authorized Signatures (DB) <ul style="list-style-type: none"> <li data-bbox="370 860 905 882">– Displays the current status of the Authorized Signature Database. <li data-bbox="370 889 948 937">– Press [Enter] to configure a new DB or load additional DB from storage devices. <li data-bbox="370 943 671 964">– Options available: Update, Append. <li data-bbox="334 975 902 1111">◆ Forbidden Signatures (DBX) <ul style="list-style-type: none"> <li data-bbox="370 1003 902 1025">– Displays the current status of the Forbidden Signature Database. <li data-bbox="370 1031 892 1080">– Press [Enter] to configure a new dbx or load additional dbx from storage devices. <li data-bbox="370 1086 671 1107">– Options available: Update, Append. <li data-bbox="334 1117 929 1252">◆ Authorized TimeStamps (DBT) <ul style="list-style-type: none"> <li data-bbox="370 1146 929 1168">– Displays the current status of the Authorized TimeStamps Database. <li data-bbox="370 1174 905 1223">– Press [Enter] to configure a new DBT or load additional DBT from storage devices. <li data-bbox="370 1229 671 1249">– Options available: Update, Append. <li data-bbox="334 1259 919 1381">◆ OsRecovery Signatures <ul style="list-style-type: none"> <li data-bbox="370 1287 919 1309">– Displays the current status of the OsRecovery Signature Database. <li data-bbox="370 1315 887 1364">– Press [Enter] to configure a new OsRecovery Signature or load additional OsRecovery Signature from storage devices. <li data-bbox="370 1370 671 1390">– Options available: Update, Append.

2-8 Boot Menu

The Boot menu allows you to set the drive priority during system boot-up. BIOS setup will display an error message if the legacy drive(s) specified is not bootable.

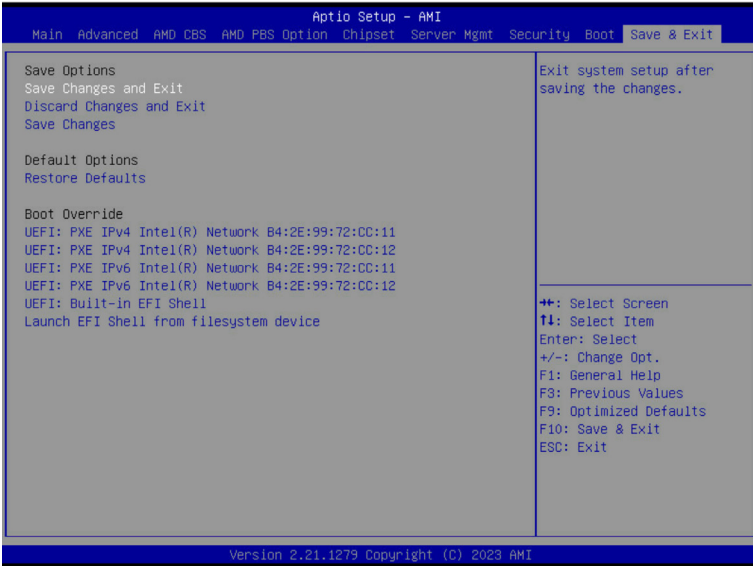


Parameter	Description
Boot Configuration	
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting. Press the numeric keys to input the desired values.
Bootup NumLock State	Enable/Disable the Bootup NumLock function. Options available: On, Off. Default setting is On .
Quiet Boot	Enable/Disable showing the logo during POST. Options available: Enabled, Disabled. Default setting is Enabled .
Setup Flash	Press [Enter] to run setup flash.
Dump full Setup Data	Press [Enter] to dump full setup data to file.
Dump non-default Setup Data	Press [Enter] to dump non-default setup data to file.
Restore Setup Data	Press [Enter] to restore setup data from file (cJson format).
Boot mode select	Selects the boot mode. Options available: LEGACY, UEFI. Default setting is UEFI .

Parameter	Description
FIXED BOOT ORDER Priorities	
Boot Option #1 / #2 / #3 / #4 / #5	<p>Press [Enter] to configure the boot priority. By default, the server searches for boot devices in the following sequence:</p> <ol style="list-style-type: none"> 1. Hard drive. 2. CD-COM/DVD drive. 3. USB device. 4. Network. 5. UEFI.
UEFI NETWORK Drive BBS Priorities	Press [Enter] to configure the boot priority.
UEFI Application Boot Priorities	Press [Enter] to configure the boot priority.

2-9 Save & Exit Menu

The Save & Exit menu displays the various options to quit from the BIOS setup. Highlight any of the exit options then press <Enter>.



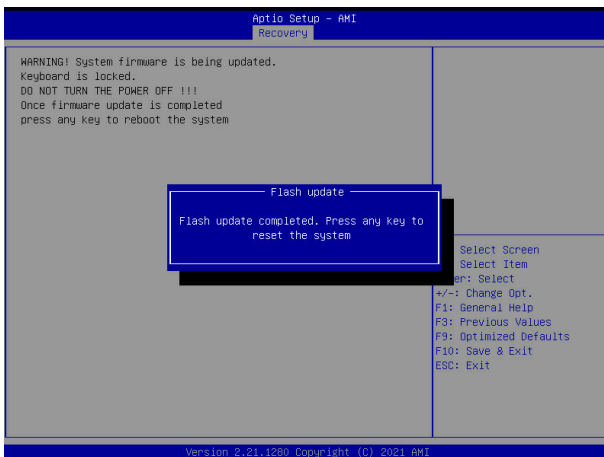
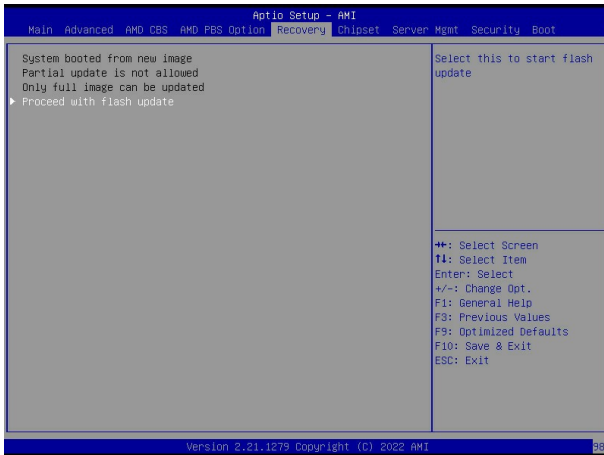
Parameter	Description
Save Options	
Save Changes and Exit	Saves changes made and closes the BIOS setup. Options available: Yes, No.
Discard Changes and Exit	Discards changes made and exits the BIOS setup. Options available: Yes, No.
Save Changes	Saves changes done so far to any of the setup options. Options available: Yes, No.
Default Options	
Restore Defaults	Loads the default settings for all BIOS setup parameters. Setup Defaults are quite demanding in terms of resources consumption. If you are using low-speed memory chips or other kinds of low-performance components and you choose to load these settings, the system might not function properly. Options available: Yes, No.
Boot Override	Press [Enter] to configure the device as the boot-up drive.
Launch EFI Shell from filesystem device	Attempts to Launch EFI Shell application (Shell.efi) from one of the available file system devices.

2-10 BIOS Recovery

The system has an embedded recovery technique. In the event that the BIOS becomes corrupt the boot block can be used to restore the BIOS to a working state. To restore your BIOS, please follow the instructions listed below:

Recovery Instruction:

1. Copy the XXX.rom to USB diskette.
2. Setting BIOS Recovery jump to enabled status.
3. Boot into BIOS recovery.
4. Run Proceed with flash update.
5. BIOS updated.



2-11 BIOS POST Beep code (AMI standard)

2-11-1 PEI Beep Codes

# of Beeps	Description
1	Memory not Installed.
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXE IPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

2-11-2 DXE Beep Codes

# of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met